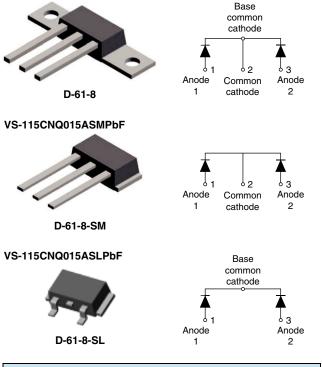


Vishay High Power Products

Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A

VS-115CNQ015APbF



PRODUCT SUMMARY				
I _{F(AV)}	2 x 55 A			
V _R at T _J = 100 °C	15 V			

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Center tap module
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- New fully transfer-mold low profile, small footprint, high current package
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	110	A	
V _{RRM}		15	V	
I _{FSM}	t _p = 5 μs sine	5050	А	
V _F	55 Apk, T _J = 75 °C (per leg)	0.33	V	
TJ	Range	- 55 to 125	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VS-115CNQ015APbF	UNITS	
Maximum DC reverse voltage	V _R	T _J = 100 °C	15	V	
Maximum working peak reverse voltage	V _{RWM}	T _J = 125 °C	5		



^{*} Pb containing terminations are not RoHS compliant, exemptions may apply



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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average per leg		50 % duty cycle at T_{C} = 112 °C, rectangular waveform		55	А	
See fig. 5 per device	I _{F(AV)}			110	~	
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and	5050	A	
non-repetitive surge current per leg See fig. 7		10 ms sine or 6 ms rect. pulse	with rated V _{RRM}	830		
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4.5 mH		54	mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum V_A = 3 x V_R typical		2	А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	. TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	55 A	T.I = 25 °C	0.37	V
		110 A	1j=25 C	0.46	
		55 A	T.I = 75 °C	0.33	
		110 A	1j=75 C	0.43	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V Deted V	20	mA
		T _J = 100 °C	V _R = Rated V _R	1200	
See fig. 2		T _J = 100 °C	V _R = 12 V	900	
		T _J = 100 °C	V _R = 5 V	540	
Maximum junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		5500	pF
Typical series inductance per leg	Ls	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

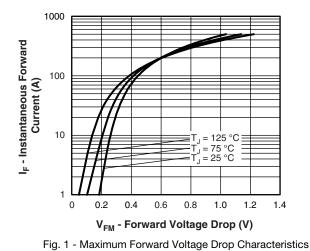
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperatu	Maximum junction temperature range T _J			- 55 to 125	°C
Maximum storage temperatu	re range	T _{Stg}		- 55 to 150	C
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation See fig. 4	0.5	
Maximum thermal resistance, junction to case per package	viaximum thermai resistance,		DC operation	0.25	°C/W
Typical thermal resistance, case to heatsink (D-61-8 only)	R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	
Approvimate weight				7.8	g
Approximate weight				0.28	oz.
Mounting torque	minimum			40 (35)	kgf · cm
(D-61-8 only)	maximum			58 (50)	(lbf · in)
Marking device			Case style D-61	115CN	Q015A
			Case style D-61-8-SM	115CNQ015ASM	
			Case style D-61-8-SL	115CNQ	015ASL

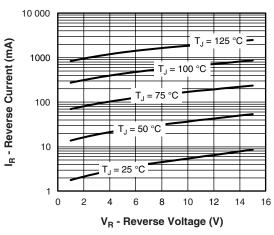


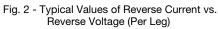
Schottky Rectifier Vishay Hig New Generation 3 D-61 Package, 2 x 55 A

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(Per Leg)





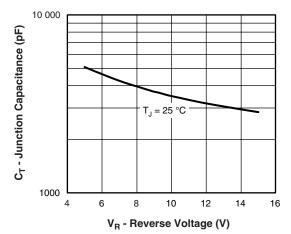


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

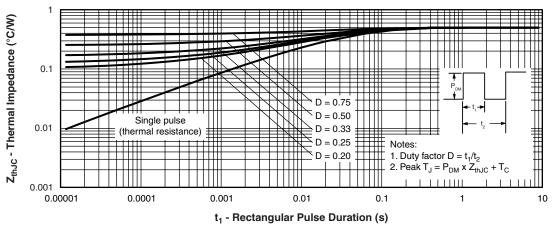
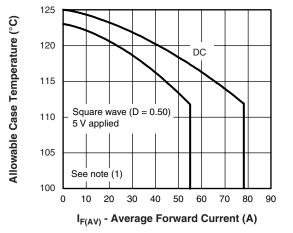


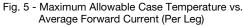
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

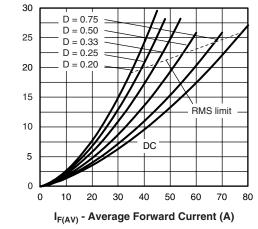
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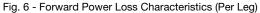
Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A

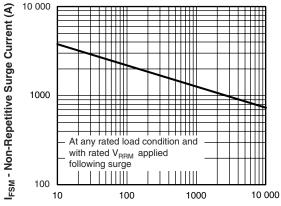
Average Power Loss (W)



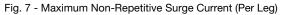








 t_p - Square Wave Pulse Duration (µs)



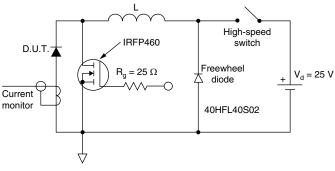


Fig. 8 - Unclamped Inductive Test Circuit

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{5} \ \mathsf{V} \end{array}$

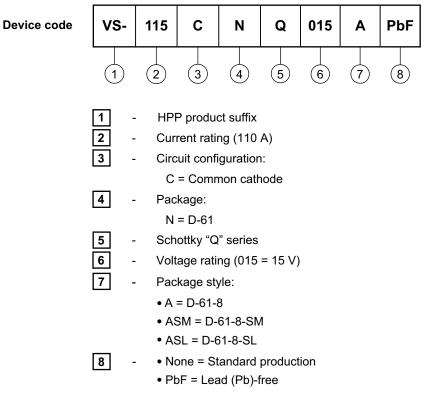


Schottky Rectifier

Vishay High Power Products

New Generation 3 D-61 Package, 2 x 55 A

ORDERING INFORMATION TABLE



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

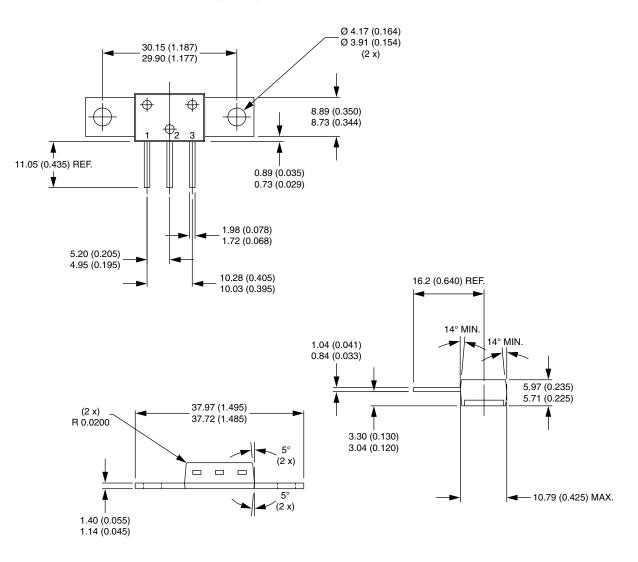
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95354</u>				
Part marking information	www.vishay.com/doc?95356			

Vishay Semiconductors



D-61-8, D-61-8-SM, D-61-8-SL

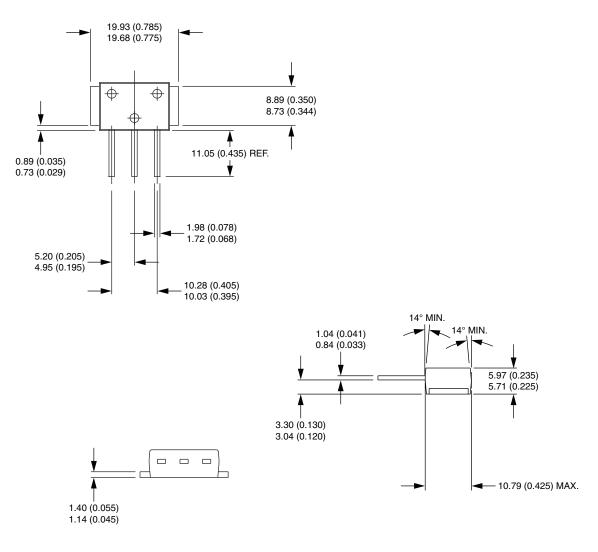
DIMENSIONS - D-61-8 in millimeters (inches)





DIMENSIONS - D-61-8-SM in millimeters (inches)

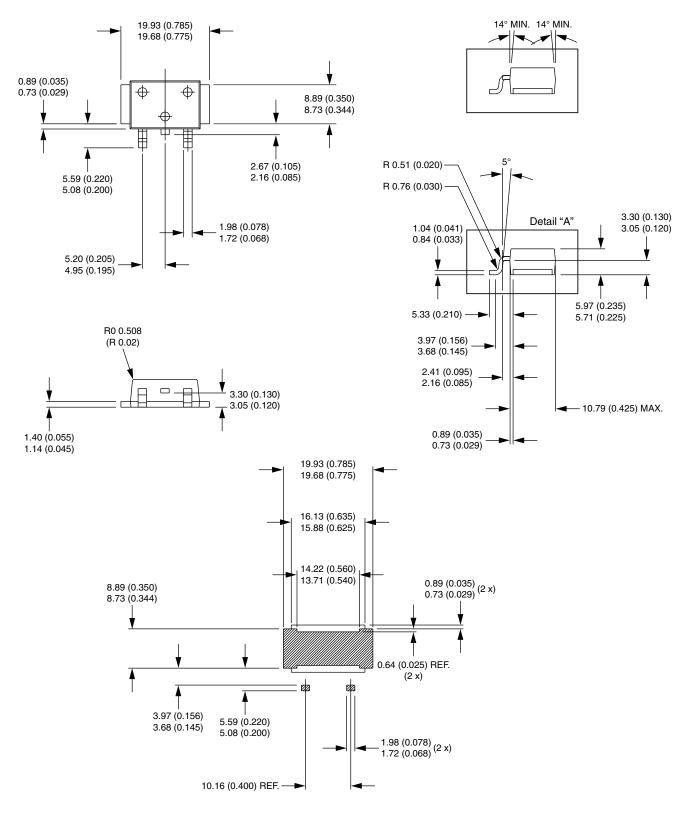
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DIMENSIONS - D-61-8-SL in millimeters (inches)

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Revision: 28-Sep-11 3 Document Number: 95354 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



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