

Clock Oscillators Surface Mount Type KC7050P-L3 Series

KYOCERA

LVDS/ 3.3V/ 7.0×5.0mm



Pb Free

RoHS Compliant

Features

- Miniature ceramic package
- Highly reliable with seam welding
- LVDS output
- Supply voltage V_{cc}=3.3V
- ±25×10⁻⁶ available

Table 1

Freq. Tol. Code × 10 ⁻⁶	Operating Temperature Range (°C)	Note
0 ± 50	0 to +70	Standard specifications
S ± 30		
U ± 25		With only certain frequencies
F ±100	-40 to +85	
G ± 50		

How to Order

KC7050P 125.000 L 3 0 E 00
 ① ② ③ ④ ⑤ ⑥ ⑦

- ① Type (7.0×5.0mm SMD)
- ② Output Frequency
- ③ Output Type (LVDS)
- ④ Supply Voltage (3.3V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ Enable Function (45/ 55%, Stand-by)
- ⑦ Customer Special Model Suffix (STD Specification is "00")

Packaging (Tape & Reel 1000 pcs./ reel)

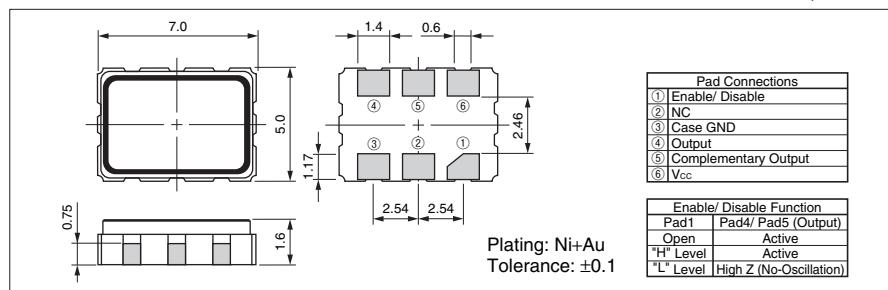
Specifications

Item	Symbol	Conditions	Min.	Max.	Units
Output Frequency Range	f _o		50	190	MHz
Frequency Tolerance	f _{_tol}	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	Op. Temp.: -40 to +85°C Op. Temp.: 0 to +70°C/-40 to +85°C Op. Temp.: 0 to +70°C Op. Temp.: 0 to +70°C	-100 -50 -30 -25	+100 +50 +30 +25
					×10 ⁻⁶
Storage Temperature Range	T _{_stg}		-55	+125	°C
Operating Temperature Range	T _{_use}	Standard Specifications Extend (Option)	0 -40	+70 +85	°C
Max. Supply Voltage	—		-0.5	+5	V
Supply Voltage	V _{cc}	Freq. Tol. Code: 0, S, F Freq. Tol. Code: U, G	2.97 3.14	3.63 3.46	V
Current Consumption	I _{cc}		—	70	mA
Stand-by Current	I _{_std}		—	30	μA
Symmetry	SYM	100ohm @50% Output Swing	45	55	%
Rise/ Fall Time (20% to 80% Output Level)	t _{r/ tf}	100ohm	—	0.6	nS
Low Level Output Voltage	V _{OL}	Typ. 1.1V	0.9	—	V
High Level Output Voltage	V _{OH}	Typ. 1.43V	—	1.6	V
Differential Output Voltage	V _{OD}	Typ. 330mV	247	454	mV
Differential Output Voltage Error	dV _{OD}	dV _{OD} = V _{OD1} -V _{OD2}	—	50	mV
Offset Voltage	V _{OS}	Typ. 1.25V	1.125	1.375	V
Offset Voltage Error	dV _{OS}	dV _{OS} = V _{OS1} -V _{OS2}	—	50	mV
Output Load	R _L	LVDS Output	—	100	ohm
Input Voltage Range	V _{IN}		0	V _{cc}	V
Low Level Input Voltage	V _{IL}		—	30% V _{cc}	V
High Level Input Voltage	V _{IH}		70% V _{cc}	—	V
Disable Time	t _{_dis}		—	200	nS
Enable Time	t _{_ena}		—	10	μS
Start-up Time	t _{_str}	@Minimum operation voltage to be 0 sec.	—	10	μS
Deterministic Jitter (DJ)	DJ		—	2	pS
1 Sigma Jitter	J _{1Sigma}	Measured with Wavecrest DTS-2079 VISI 6.3.1	—	4	pS
Peak to Peak Jitter	J _{PK-PK}		—	30	pS

Note: All electrical characteristics are defined at the maximum load and operating temperature range.

Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Dimensions



Recommended Land Pattern

