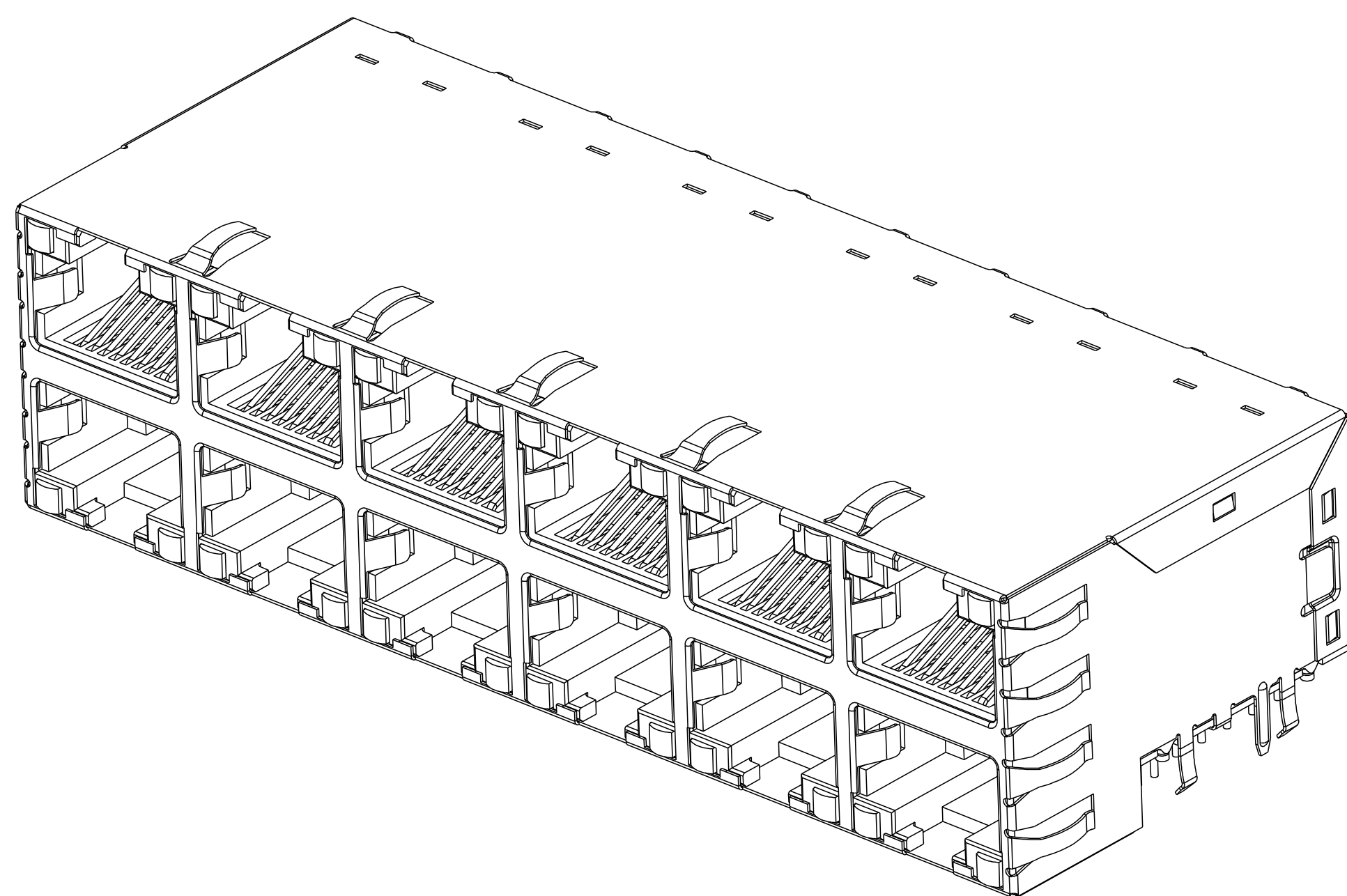
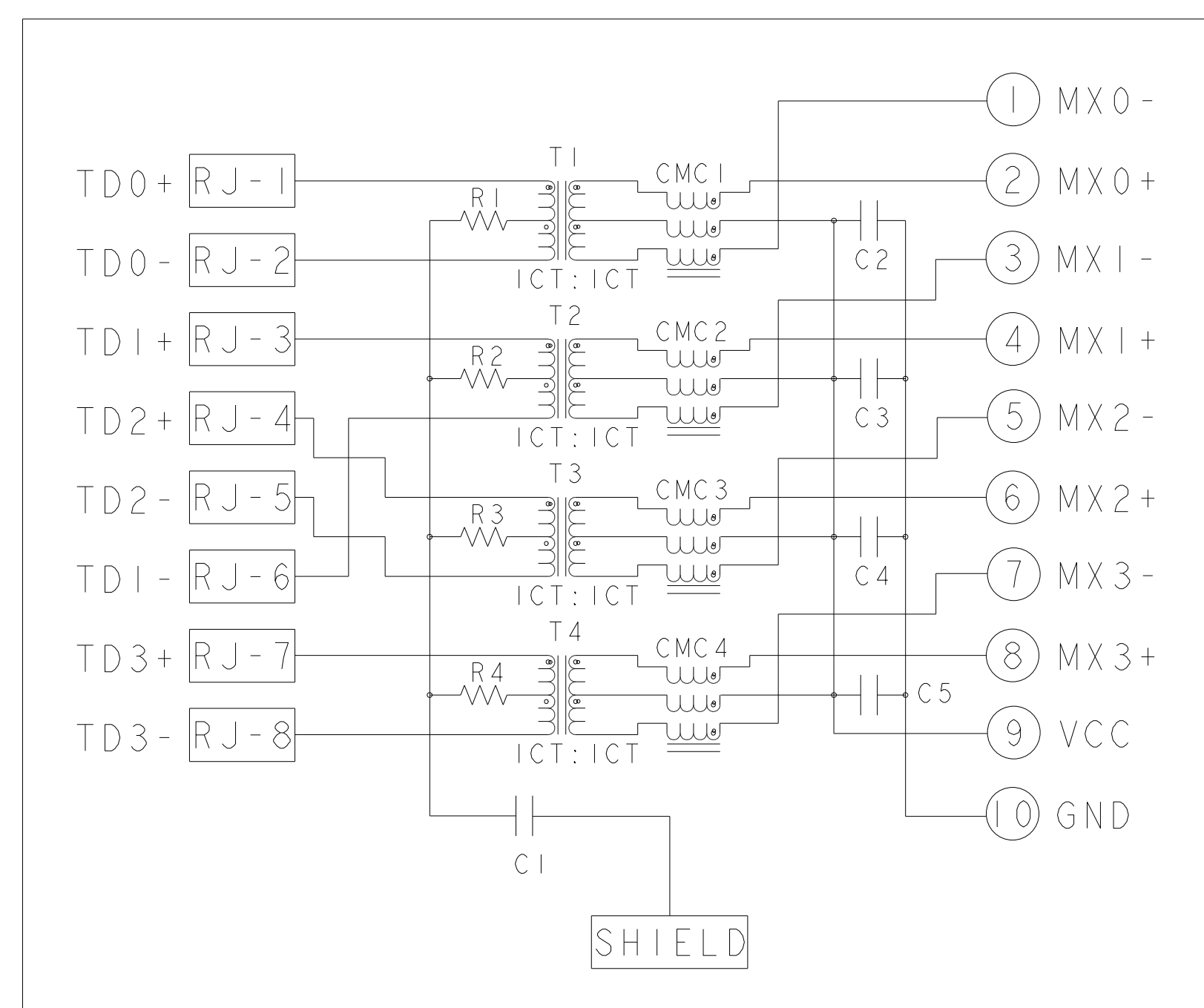


P	LTR	DESCRIPTION	DATE	DWN	AP
	C	LOGO CHANGE	06JUN2013	RL	KZ



S8G31 GIGABIT CIRCUIT

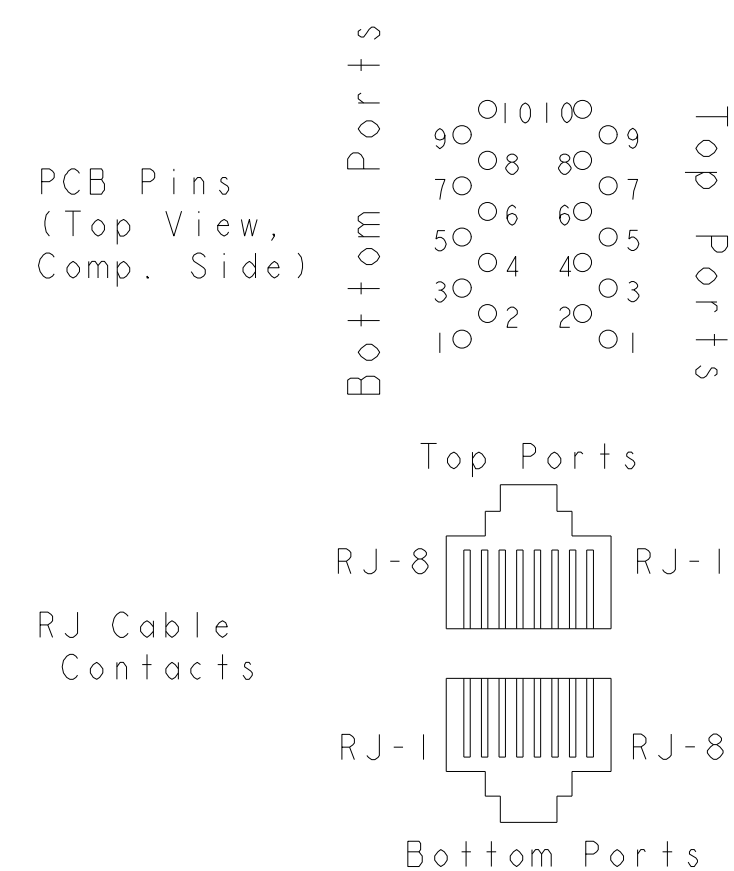
Top and Bottom Ports



C1 = 1000pF, 2kV
C2-C5 = 470pF, 10%, 50V
R1-R4 = 75ohms, 1/16W

Pin Designations

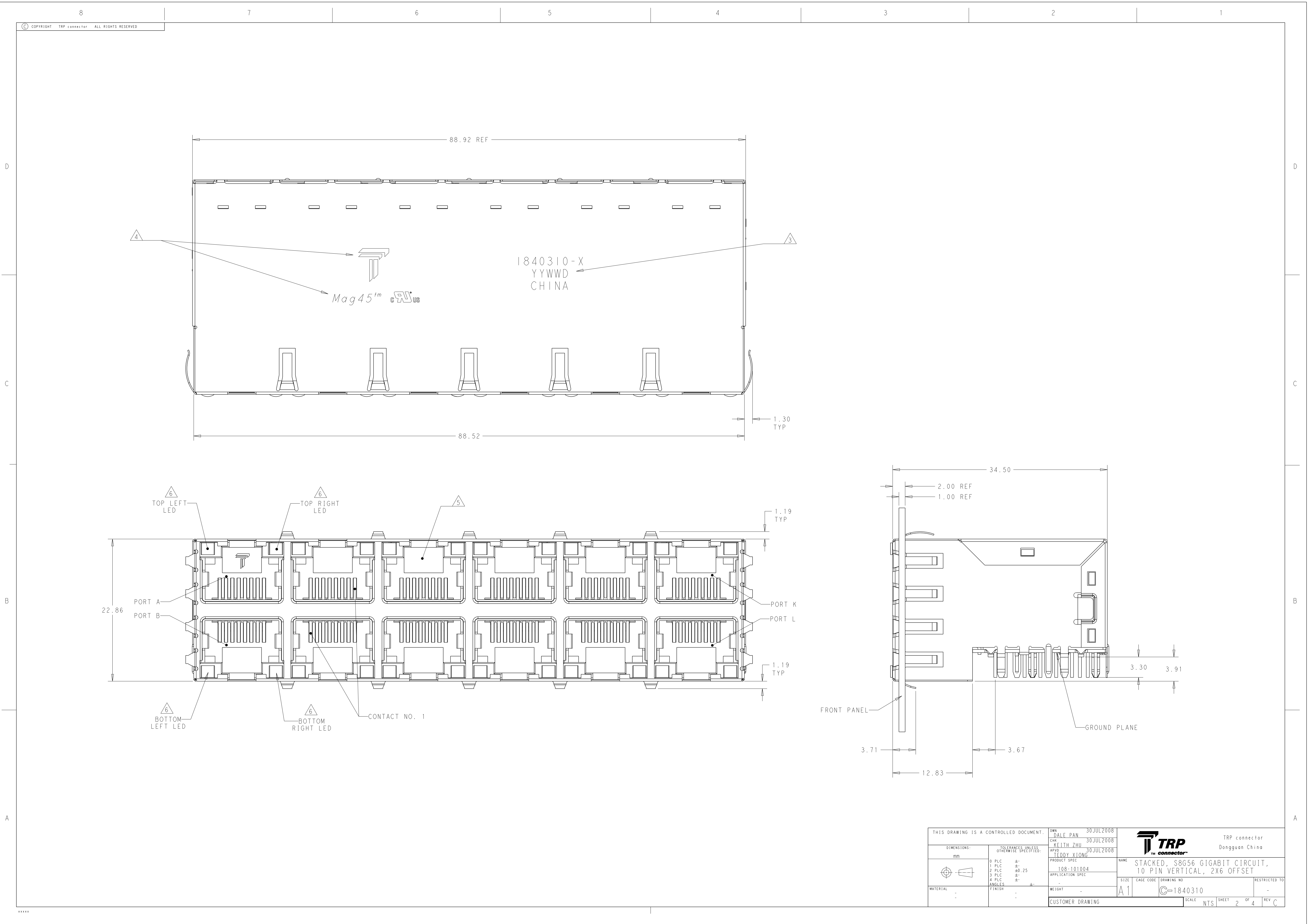
(Repeat for each vertical pair of ports.)



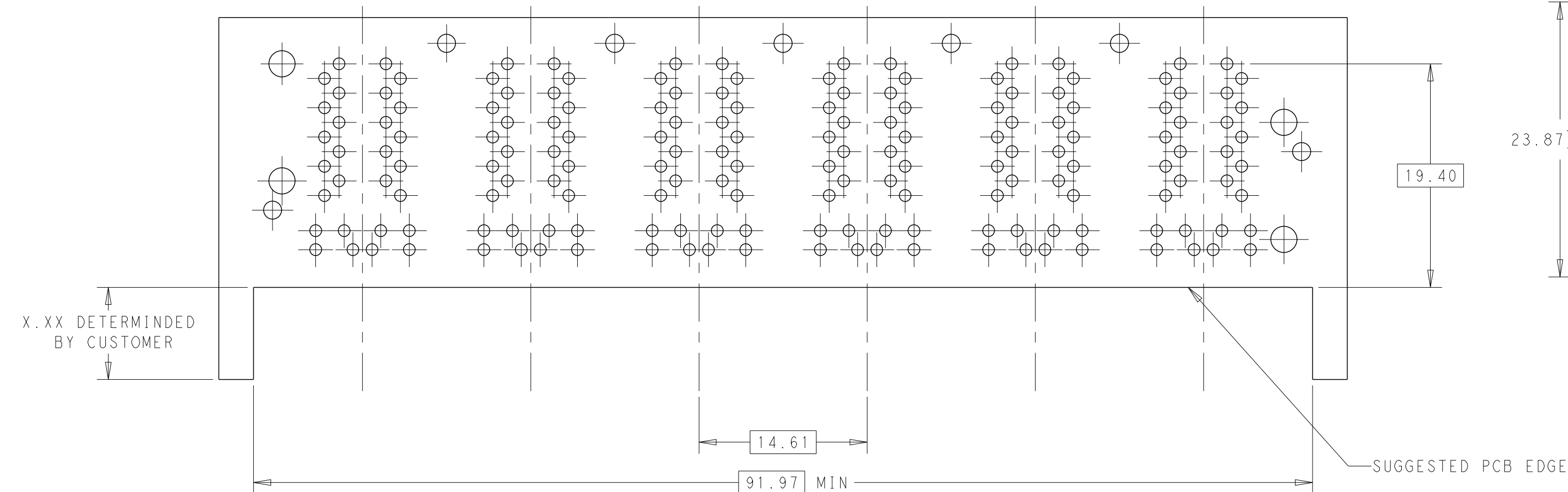
1. MATERIALS:
- HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0.
- SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL, POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.
- CONTACTS: PHOSPHOR BRONZE, PLATED WITH 1.27um MIN OVERALL NICKEL UNDERPLATE, SELECTIVE 1.27um MIN GOLD AT MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
- LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME LEADS, PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL OVER 1.02um MIN COPPER UNDERPLATE, POST PLATED WITH 2.54um MIN MATTE TIN AND/OR 2.54um MIN SAC SOLDER DIP OR PURE TIN SOLDER DIP.
2. MAGNETICS
-APPLICATION: 10/100/1000 BASE-T
-IMPEDANCE: 100 Ohms
-TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
-OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0 °C TO 70 °C, ALL FOUR PAIRS
-ALL FOUR PAIRS BI-DIRECTIONAL
-PERFORMANCE @ 25 °C:
 INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
 CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 COMMON MODE REJECTION RATIO (CMRR):
 30dB MIN FROM 0.5MHz TO 100MHz
-ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA, 40.6.1.1 ITEM b.
3. PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN THE APPROXIMATE AREA SHOWN. DATE CODE YYWWD WHERE "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK WITH SUNDAY=1
4. TRP CONNECTOR LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN THE APPROXIMATE AREA SHOWN.
5. RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68 SUBPART F.
6. LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX. 20mA.
LED COLOR: DOMINANT WAVELENGTH (λD)
GREEN 568nm TYP AT IF=20mA
YELLOW 588nm TYP AT IF=20mA
FORWARD VOLTAGE(VF)
GREEN, 2.2V TYP AT IF=20mA
YELLOW, 2.1V TYP AT IF=20mA
7. INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDI
8. OPERATING TEMPERATURE: FROM 0 °C TO 70 °C
9. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 265 °C MAX. 10 SECONDS MAX.

YELLOW/GREEN	YELLOW/GREEN	YELLOW/GREEN	YELLOW/GREEN	1840310-4
TOP LEFT LED	TOP RIGHT LED	BOTTOM LEFT LED	BOTTOM RIGHT LED	PART NO.

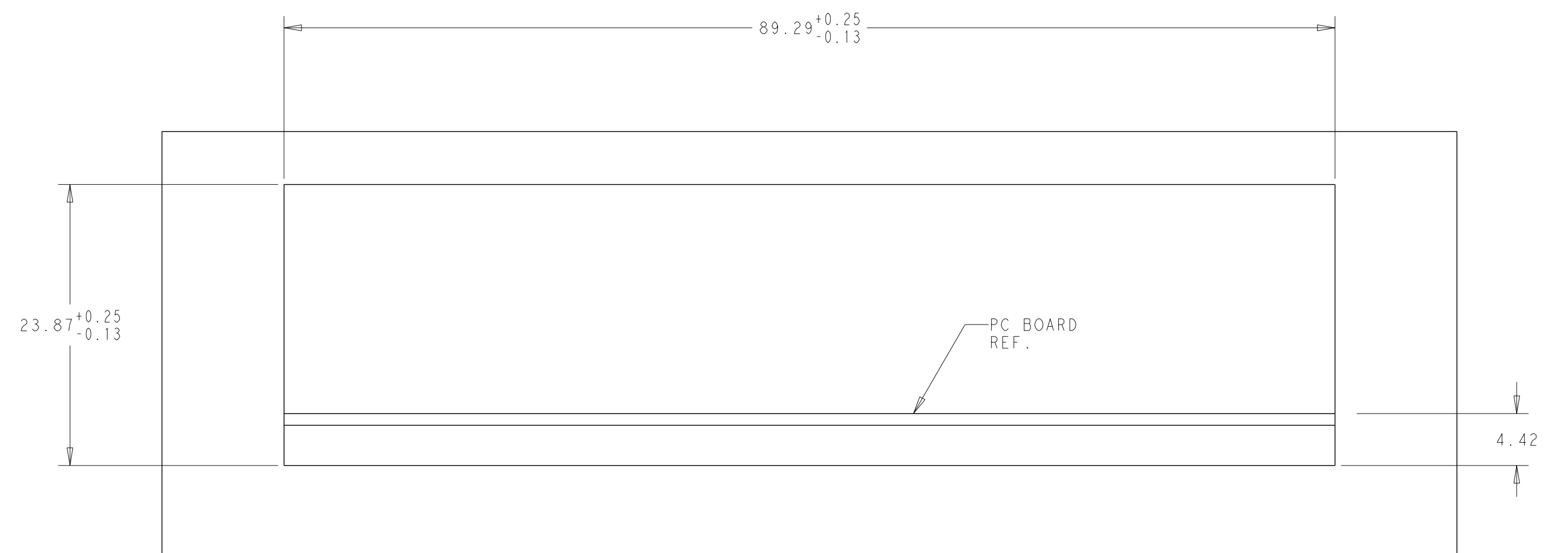
THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN DATE PAN 30JUL2008 CHN KEITH ZHU 30JUL2008 APVD TEDDY XIONG 30JUL2008		 TRP connector Dongguan China	
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 P L C ±. 2 P L C ±.025 3 P L C ±. 4 P L C ±. ANGLES ±.		NAME STACKED, S8G56 GIGABIT CIRCUIT, 10 PIN VERTICAL, 2X6 OFFSET	
		PRODUCT SPEC 108-101004 APPLICATION SPEC		SIZE CASE CODE DRAWING NO A1 C-1840310	
MATERIAL -		FINISH -		RESTRICTED	
WEIGHT -		CUSTOMER DRAWING		SCALE N.T.S. SHEET 1 OF 4 REV -	



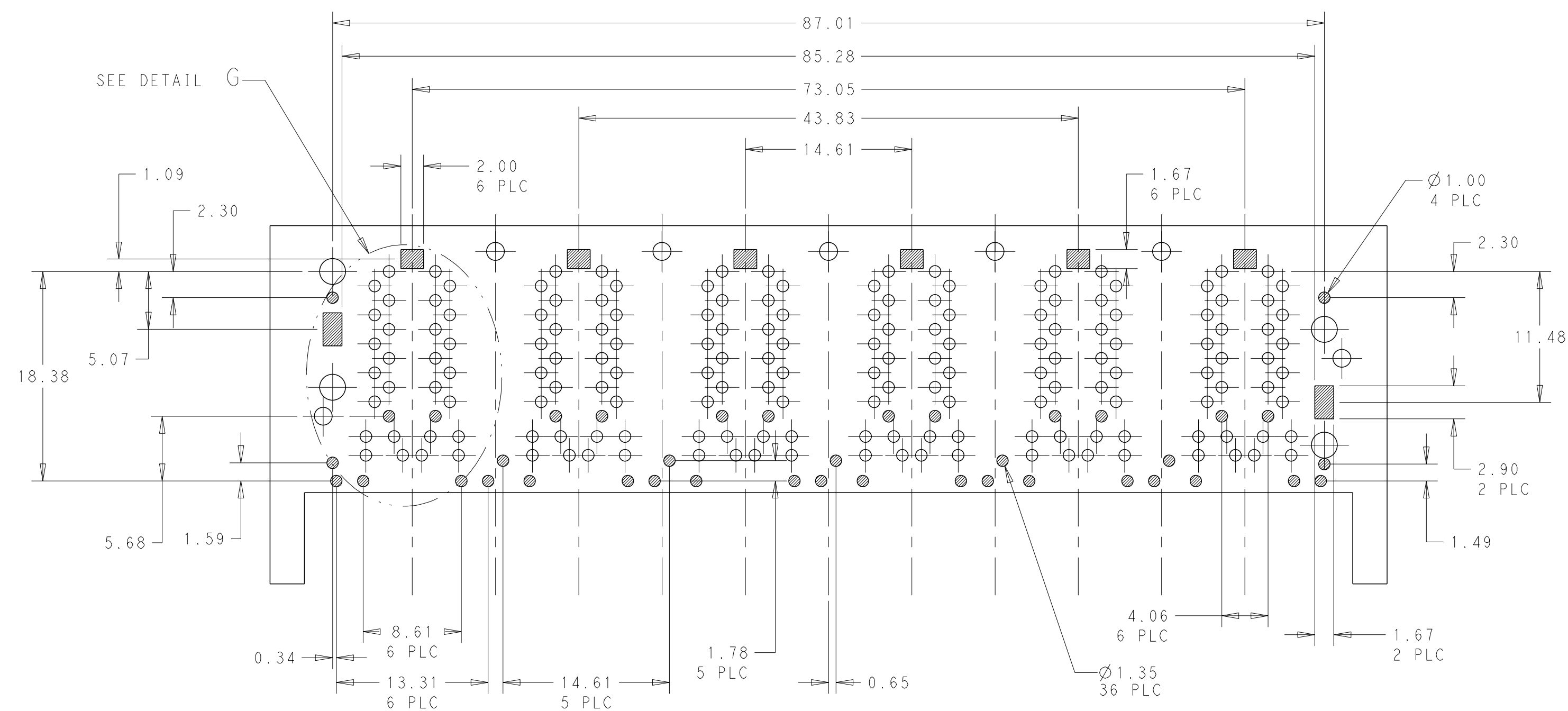
THIS DRAWING IS A CONTROLLED DOCUMENT.				DWN 30JUL2008		 TRP connector Dongguan China									
DALE PAN				30JUL2008											
KEITH ZHU				30JUL2008											
TEDDY XIONG				30JUL2008		NAME									
PRODUCT SPEC				108-101004		STACKED, S8G56 GIGABIT CIRCUIT,									
APPLICATION SPEC				108-101004		10 PIN VERTICAL, 2X6 OFFSET									
WEIGHT				-		SIZE		CAGE CODE		DRAWING NO		RESTRICTED TO			
CUSTOMER DRAWING				A 1		C=1840310		SCALE		NTS		SHEET 2 of 4		REV C	



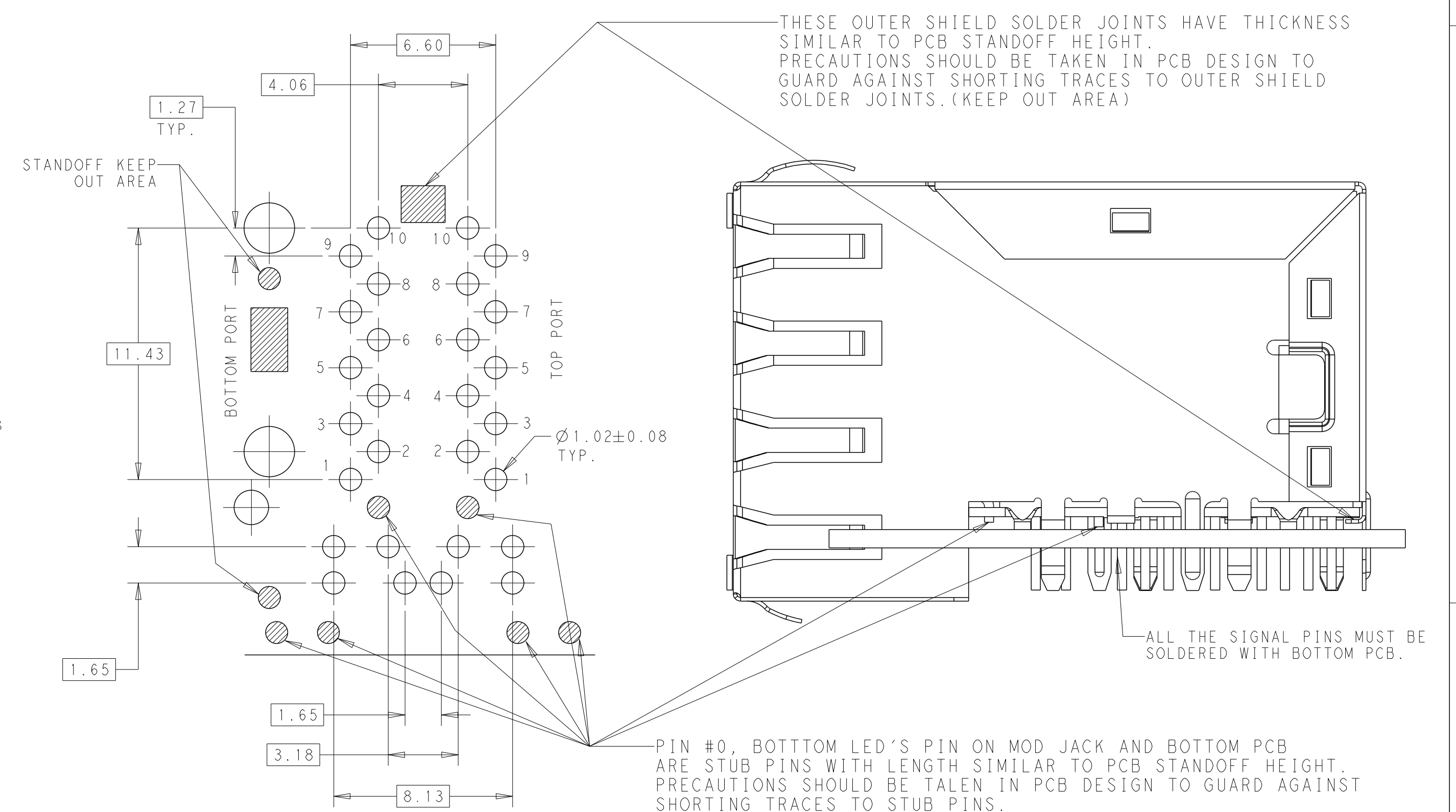
RECOMMENDED PC BOARD LAYOUT
VIEWED FROM COMPONENT SIDE



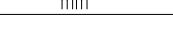
RECOMMENDED PANEL CUT-OUT

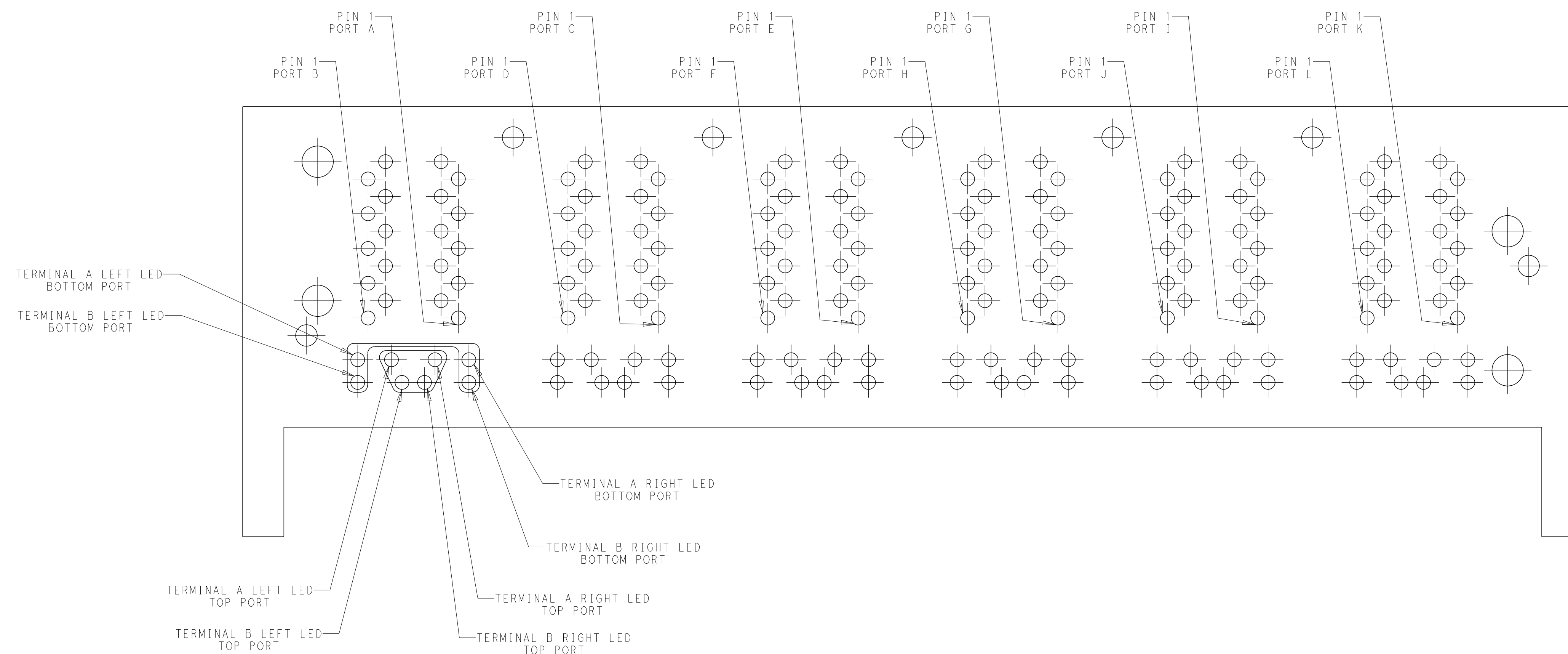


RECOMMENDED PC BOARD LAYOUT
VIEWED FROM COMPONENT SIDE



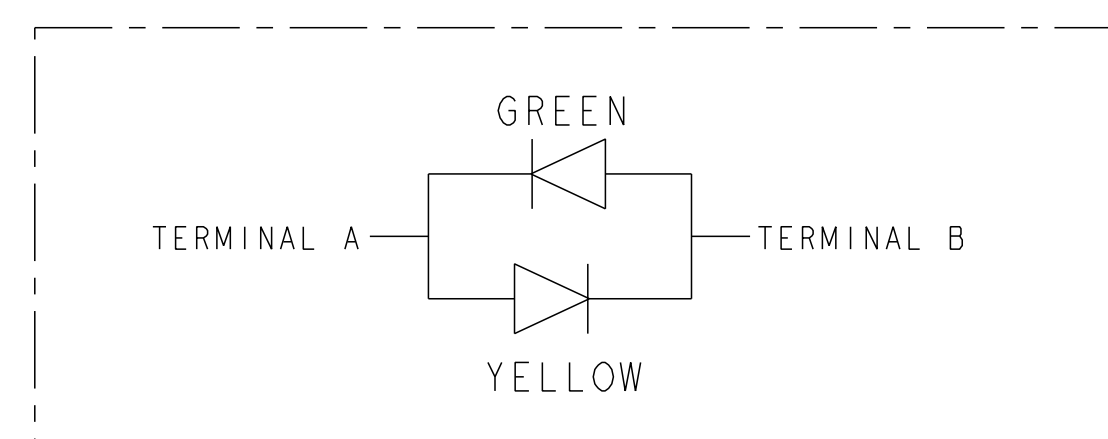
DETAIL G
SCALE 6:1

THIS DRAWING IS A CONTROLLED DOCUMENT.				DWN 30JUL2008				TRP connector	
				CHK 30JUL2008				Dongguan China	
				APVD 30JUL2008					
				APVD TEDDY XIONG					
DIMENSIONS:		TOLERANCES UNLESS OTHERWISE SPECIFIED:		PRODUCT SPEC		NAME		STACKED, S8G56 GIGABIT CIRCUIT, 10 PIN VERTICAL, 2X6 OFFSET	
mm		0 PLC ±		108-101004		SIZE			
		1 PLC ±0.25		APPLICATION SPEC		CAGE CODE			
		3 PLC ±				DRAWING NO		RESTRICTED TO	
		4 PLC ±				A1		C=1840310	
MATERIAL		FINISH		WEIGHT		CUSTOMER DRAWING		SCALE	
-		-		-		-		NTS	
								SHEET 3 OF 4	
								REV C	



LED HOLE DESIGNATIONS VIEWED FROM COMPONENT SIDE
SCALE 5:1

BI COLOR LED



THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN DALE PAN 30JUL2008 CHK KEITH ZHU 30JUL2008 APVD LEODY XIONG 30JUL2008		 TRP connector Dongguan China	
DIMENSIONS: mm		TO TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±: 2 PLC ±0.25 3 PLC ±: 4 PLC ANGLES ±:		PRODUCT SPEC 108-101004 APPLICATION SPEC -	
		NAME STACKED, S8566 GIGABIT CIRCUIT, 10 PIN VERTICAL, 2X6 OFFSET		SIZE A1	
MATERIAL -		FINISH -		CAGE CODE C=1840310	
WEIGHT -		DRAWING NO 108-101004		RESTRICTED TO -	
CUSTOMER DRAWING -		SCALE NTS		SHEET 4 OF 4 REV -	