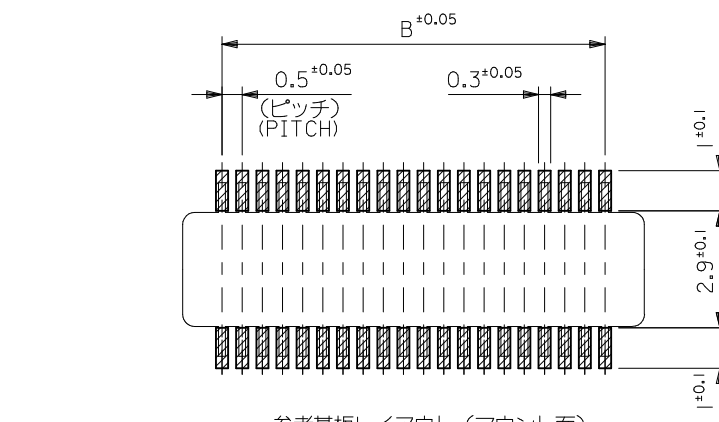
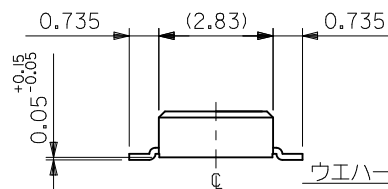
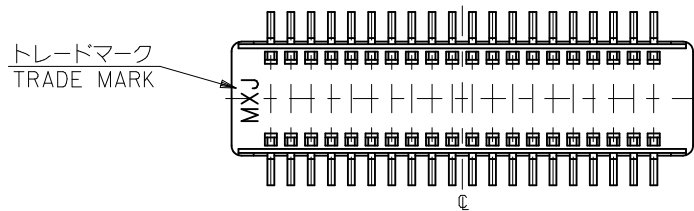
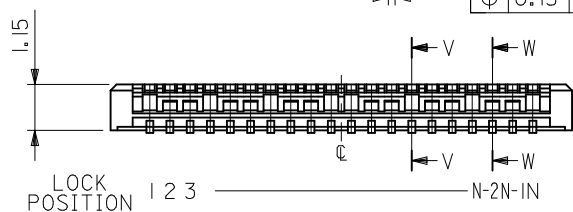
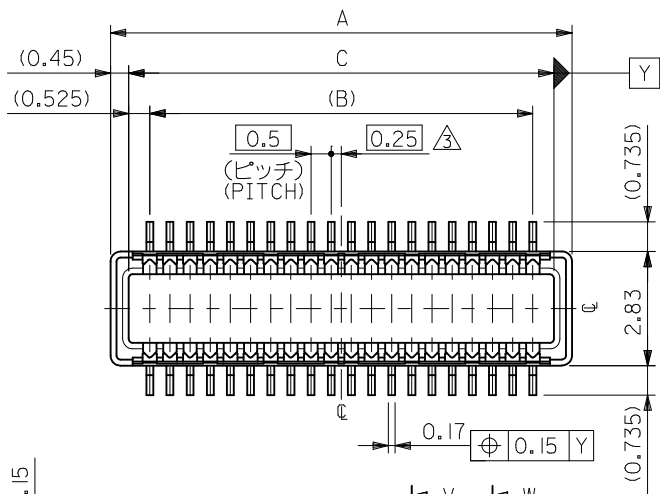
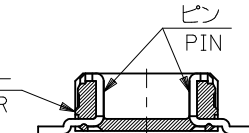
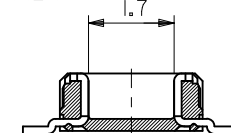




参考基板レイアウト (マウント面)
P.C. BOARD PATTERN
DIMENSION (REF.)
(MOUNTING AREA)



SECT.W-W(LOCK PART)



SECT.V-V(W/O LOCK PART)

NON	20.55	19.5	21.45	55560-0801	80
10,21	15.55	14.5	16.45	-0601	60
2,4,8,10,16,18,22,24	13.05	12.0	13.95	-0501	50
2,3,5,6,8,9,12,13,15,16,18,19	10.55	9.5	11.45	-0401	40
2,3,4,5,7,8,9,10,11,13,14,15,16	9.05	8.0	9.95	-0341	34
ALL	8.05	7.0	8.95	-0301	30
	6.55	5.5	7.45	-0241	24
	6.05	5.0	6.95	-0221	22
	5.55	4.5	6.45	-0201	20
	4.55	3.5	5.45	55560-0161	16
LOCK POSITION	C	B	A	MATERIAL NO.	極数 CIRCUIT

REVISED EC NO: J2012-0135 DRWN:YON002 CHKD:TASAKAWA APPR:MSASAO 2011/07/01 2011/07/01 2011/07/19	DESCRIPTION	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE ---	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
		10 UNDER	± 0.20	DRAWN BY SAIHARA	DATE 2003/03/31	TITLE 0.5 B-TO-B CONN PLUG ASSY (HGT=1.5)				
		10 OVER 30 UNDER	± 0.25	CHECKED BY MSASAO	DATE 2003/03/31					
		30 OVER	± 0.30	APPROVED BY MSASAO	DATE 2003/03/31	MOLEX INCORPORATED				
		ANGULAR ±3 °		MATERIAL NO.		DOCUMENT NO.		SHEET NO.		
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE CHART		SD-55560-006		1 OF 2		
	K	REV	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

