Evaluation Board for Step-Down DC-to-DC Controller

ADP1877HC-EVALZ

FEATURES

Input range: 6 V to 14 V
Two output voltages: 1.8 V and 1.05 V
Output current: 13 A per channel
Switching frequency: 600 kHz
Operate in PWM or PSM
Compact, low cost, and efficient design

EVALUATION BOARD DESCRIPTION

This data sheet describes the design, operation, and test results of the ADP1877 13 A evaluation board. The input range for this evaluation board is 6 V to 14 V, and the two regulated output voltages are set to 1.8 V and 1.05V, each with a maximum load current of 13 A. A switching frequency (f_{SW}) of 600 kHz achieves a good balance between efficiency and the sizes of the power components.

ADP1877 DEVICE DESCRIPTION

The ADP1877 is a Flexmode® (proprietary architecture of Analog Devices, Inc.), dual-channel, step-down switching controller with integrated drivers that drive N-channel synchronous power MOSFETs. The two PWM outputs are phase shifted 180°, which reduces the input rms current, thus minimizing required input capacitance.

The boost diodes are built into the ADP1877, thus lowering the overall system cost and component count. The ADP1877 can be set to operate in pulse skip high efficiency mode under light loads or in PWM continuous conduction mode.

The ADP1877 includes externally adjustable soft start, output overvoltage protection, externally adjustable current limit, power good, a tracking function, and a programmable oscillator frequency that ranges from 200 kHz to 1.5 MHz. The ADP1877 provides an output voltage accuracy of ±1% over temperature. This part can be powered from a 2.75 V to 14.5 V supply, operates over the −40°C to +125°C junction temperature range and is available in a 32-lead 5 mm × 5 mm LFCSP package.

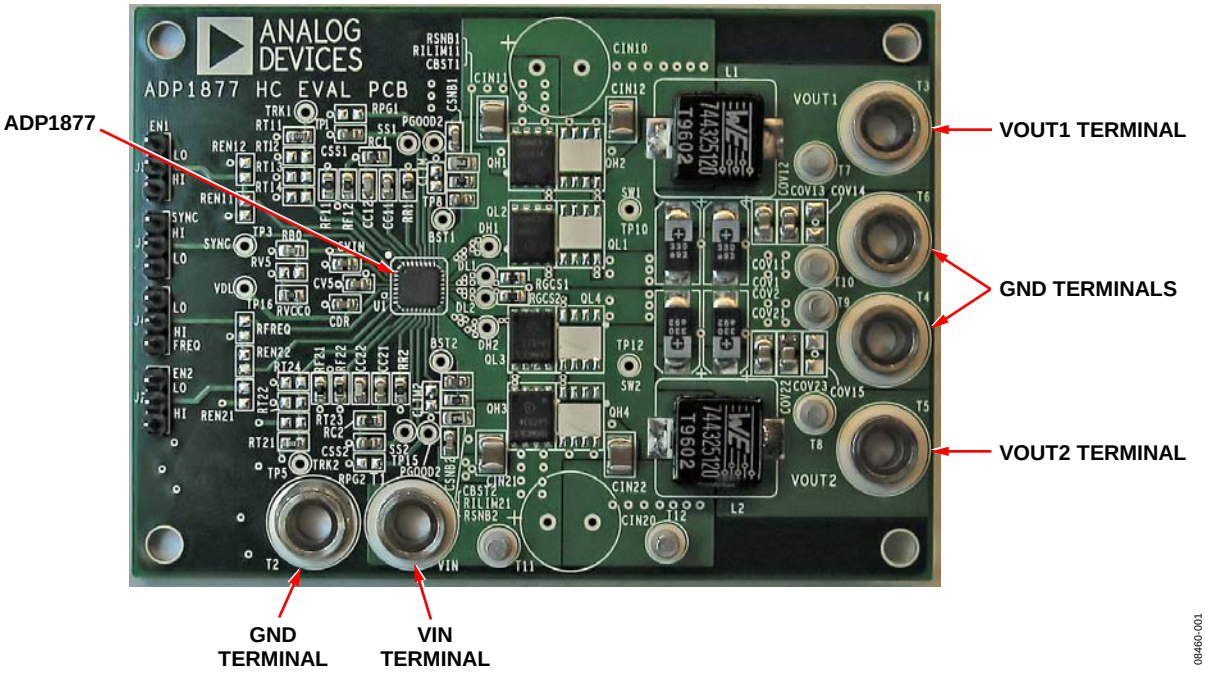

Figure 1. ADP1877HC Evaluation Board

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Tel: 781.329.4700 **www.analog.com**
Fax: 781.461.3113

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REVISION HISTORY

8/10—Rev. 0 to Rev. A

Change to Figure 1 1

8/09—Revision 0: Initial Version

COMPONENT DESIGN

For information in selecting power components and calculating component values, see the ADP1877 data sheet.

INDUCTOR SELECTION

A 1.2 µH inductor with a 20 A average current rating (744325120 from Würth Elektronik) is selected. This is a compact inductor with a ferrite core, which offers high performance in terms of low R_{DC} and low core loss.

INPUT CAPACITORS

Because of the very low ESR and high input current rating of multilayer ceramic capacitors (MLCCs), two 10 µF MLCCs in Size 1210 are selected as the input capacitors for each input.

OUTPUT CAPACITORS

A combination of POSCAP™ polymer capacitors and MLCCs are selected for the output rails. Polymer capacitors have low ESR and high current ripple rating. Connecting polymer capacitors and MLCCs in parallel is very effective in reducing voltage ripple. Two 330 µF POSCAP capacitors and two 22 µF MLCCs in Size 0805 are selected.

MOSFET SELECTION

For low output or low duty cycle, select a high-side MOSFET with fast rise and fall times and with low input capacitance to significantly reduce the switching power loss. As for the synchronous rectifier (low-side MOSFET), select a MOSFET with low R_{DSON} because switching speed is not critical and there is no switching loss in the low-side MOSFET.

For the high-side MOSFET, the BSC080N03LS from Infineon Technologies in the PG-TDSON-8 or Super-SO8 package is selected. This part has low input capacitance (1.2 nF) and fast transition times (3 ns). As for the low-side MOSFET, the BSC030N03LS, with R_{DSON} of 4.7 mΩ at a V_{GS} of 4.5 V, is selected.

SWITCHING NOISE AND OVERSHOOT REDUCTION

In reducing the overshoot voltage and ringing at the SWx node, gate resistors of 3 Ω and RC snubbers of 4 Ω and 1.2 nF are added.

TEST RESULTS

T_A = 25°C.

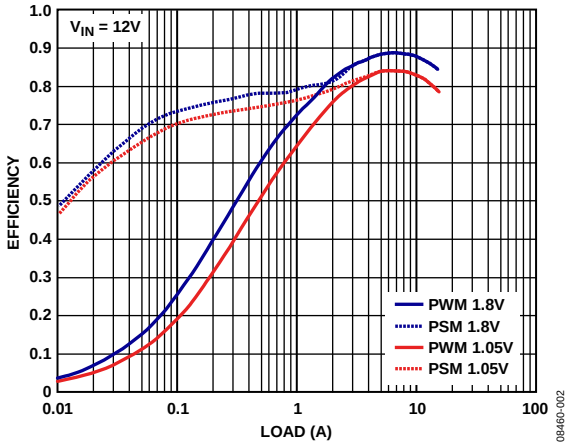

Figure 2. Efficiency

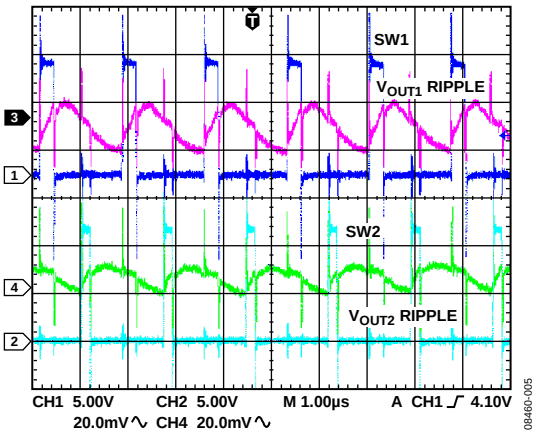

Figure 5. Output Ripple, 13 A Load

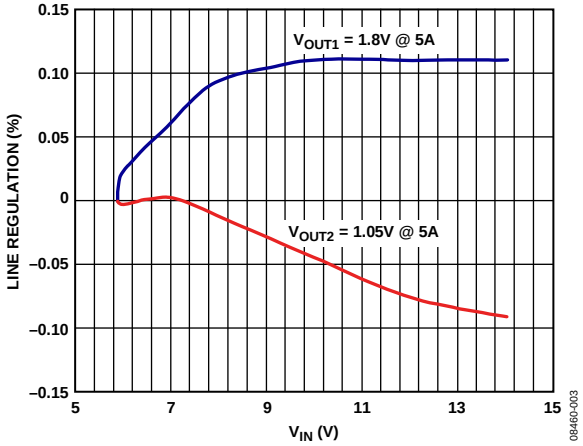

Figure 3. Line Regulation

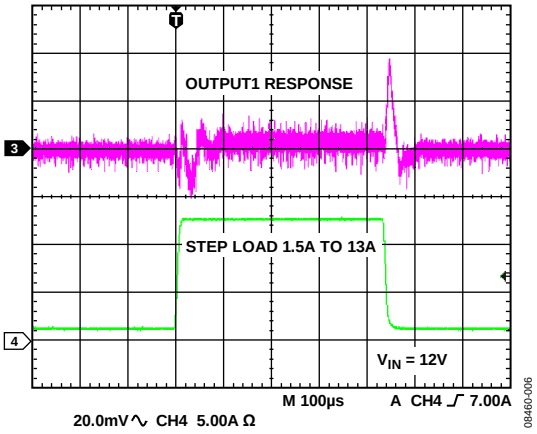

Figure 6. Step Load Transient, V_{OUT1}

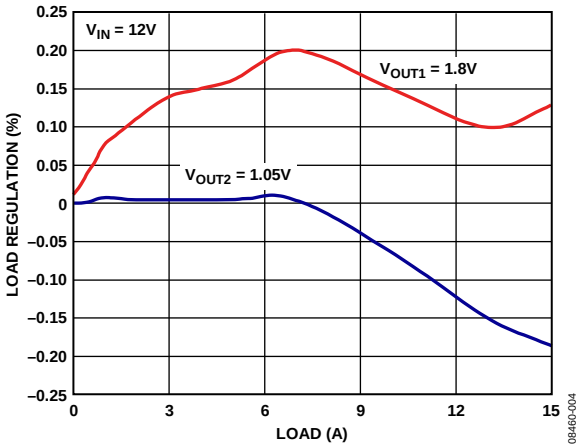

Figure 4. Load Regulation

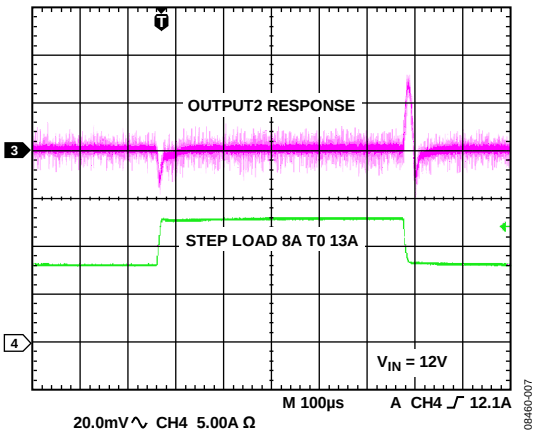

Figure 7. Step Load Transient, V_{OUT2}

EVALUATION BOARD OPERATING INSTRUCTIONS

1. Connect Jumper J3 (EN1) to the HI position to enable Channel 1 of the ADP1877.
2. Connect Jumper J2 (EN2) to the HI position to enable Channel 2 of the ADP1877.
3. Connect Jumper J4 (FREQ) to the HI position for 600 kHz operation.
4. Connect Jumper J1 (SYNC) to the HI position for PWM operation or to LO for PSM operation.
5. Connect the positive terminal of the input power supply to the input terminal, T1. The input range is 6 V to 14 V.

Table 1. Jumper Description

Jumper	Description	Function
J1	SYNC	Connect to HI for PWM or LO for PSM operation. For synchronization, run an external clock source to this pin.
J2	EN2	Connect to HI to enable Channel 2 of the ADP1877, or to LO to disable the channel.
J3	EN1	Connect to HI to enable Channel 1 of the ADP1877, or to LO to disable the channel.
J4	FREQ	Connect to LO for 300 kHz or to HI for 600 kHz operation. For this 13 A evaluation board, connect J4 to HI.

Table 2. Performance Summary (T_A = 25°C)

Parameter	Condition
V_{IN}	6 V to 14 V
f_{SW}	Switching frequency, 600 kHz
V_{OUT1}	1.8 V
I_{OUT1}	0 A to 13 A
V_{OUT1} Ripple, DC Load	20 mV at 13 A load
V_{OUT1} Deviation Upon Step Load Release	2% with a 11 A step load; 3% with a 13 A step load
V_{OUT2}	1.05 V
I_{OUT2}	0 A to 13 A
V_{OUT2} Ripple, DC Load	13 mV at 13 A load
V_{OUT2} Deviation Upon Step Load Release	3% with a 5 A step load

EVALUATION BOARD SCHEMATIC AND ARTWORK

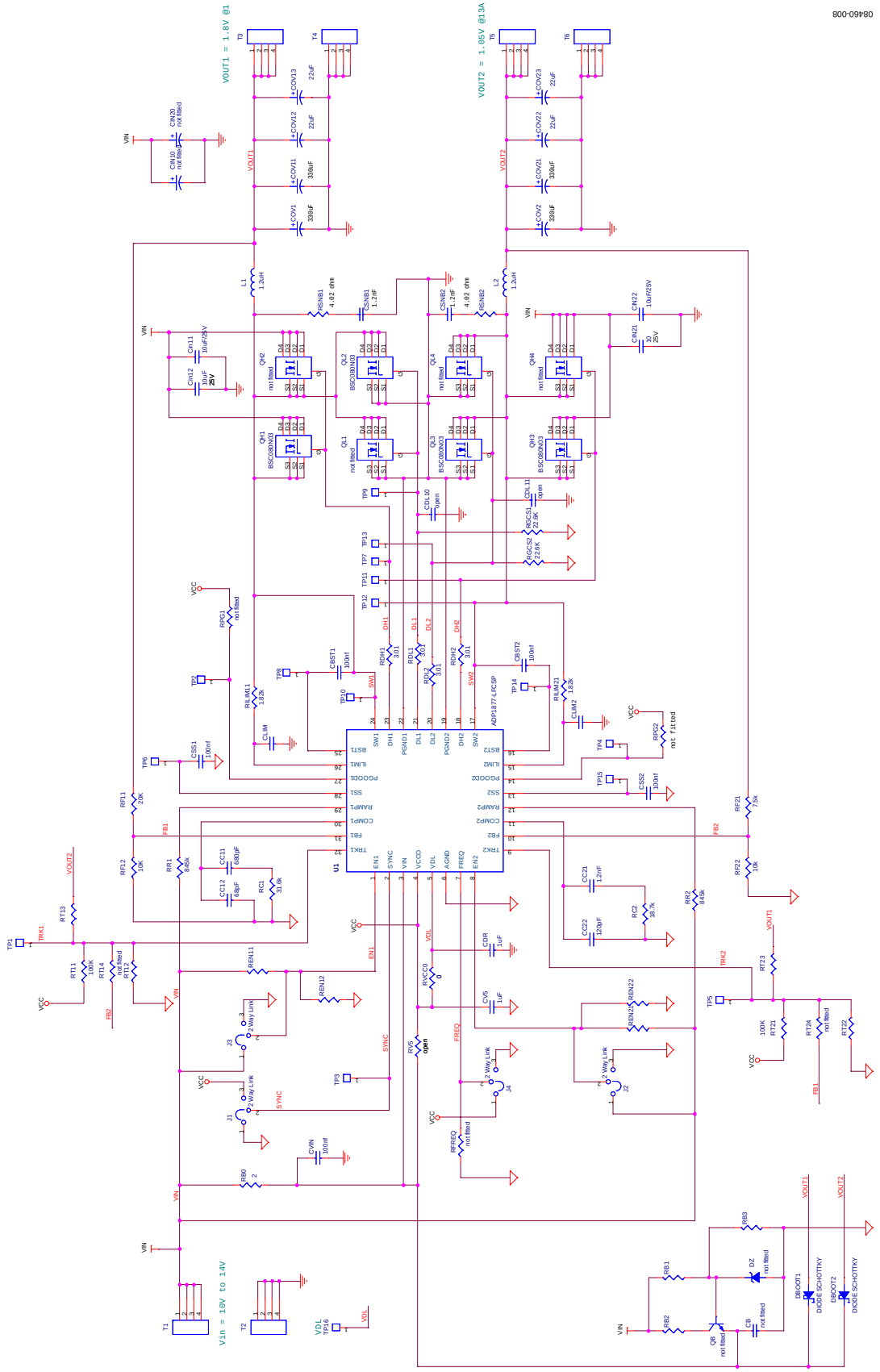

Figure 8. Evaluation Board Schematic

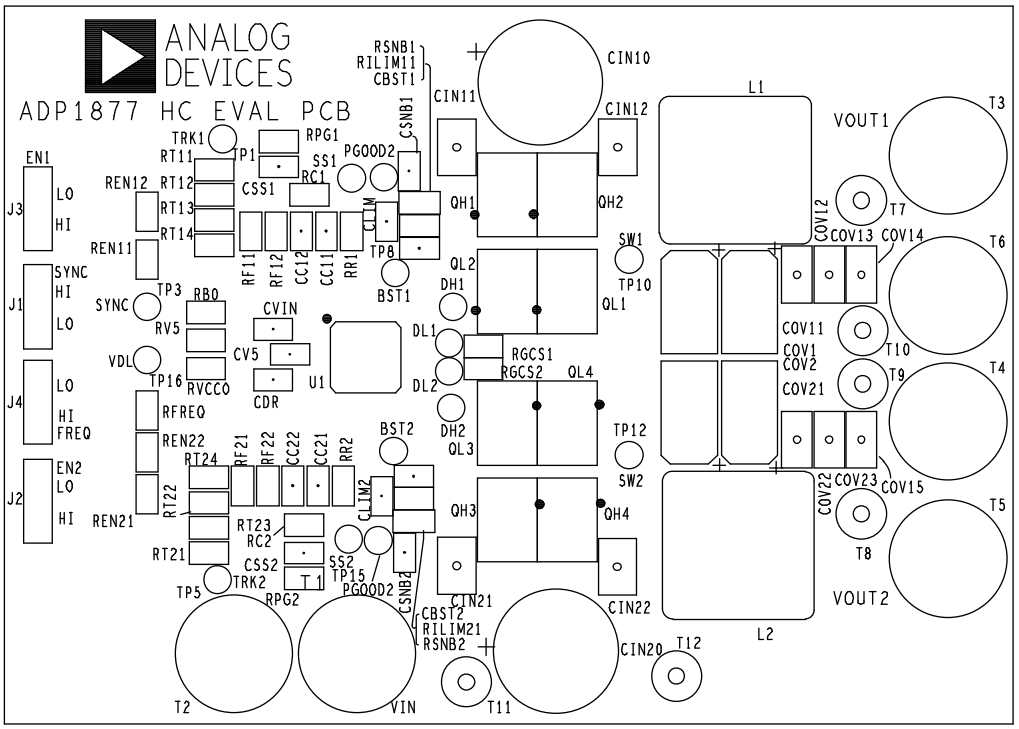

Figure 9. Top Silkscreen

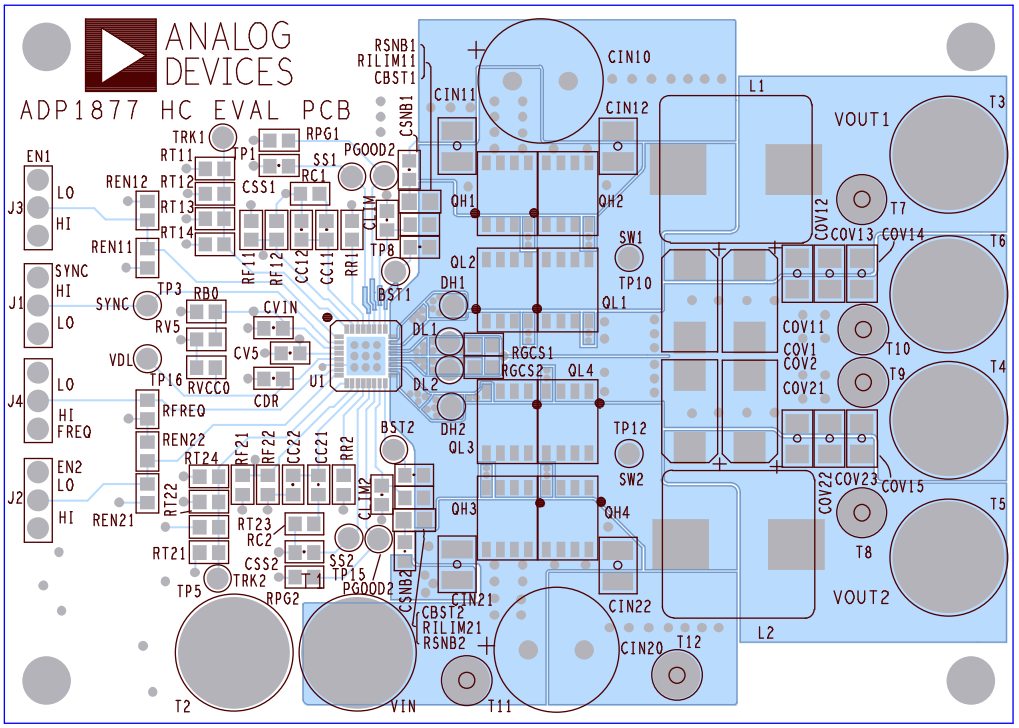

Figure 10. Top Layer

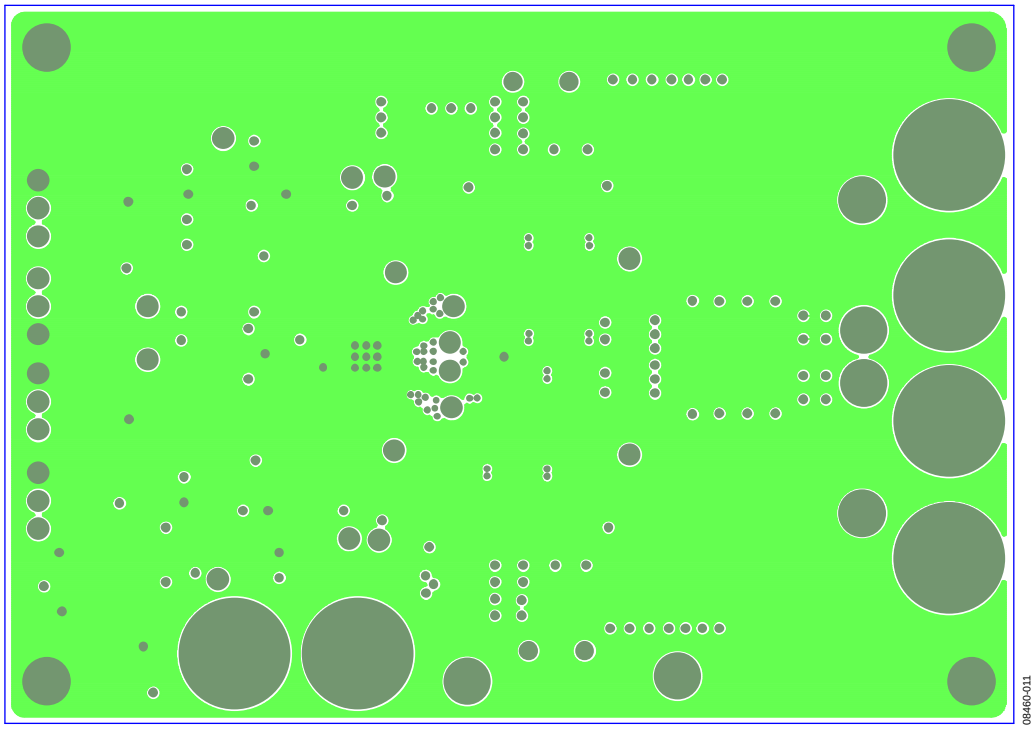

Figure 11. Second Layer (AGND plane)

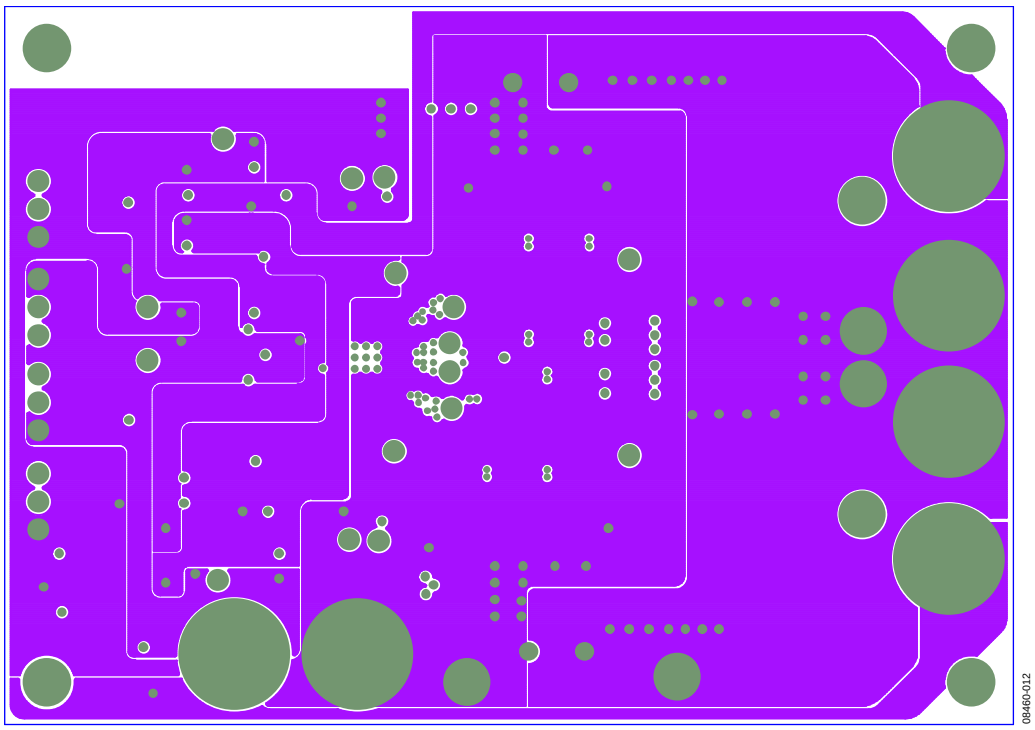

Figure 12. Third Layer (PGND Layer)

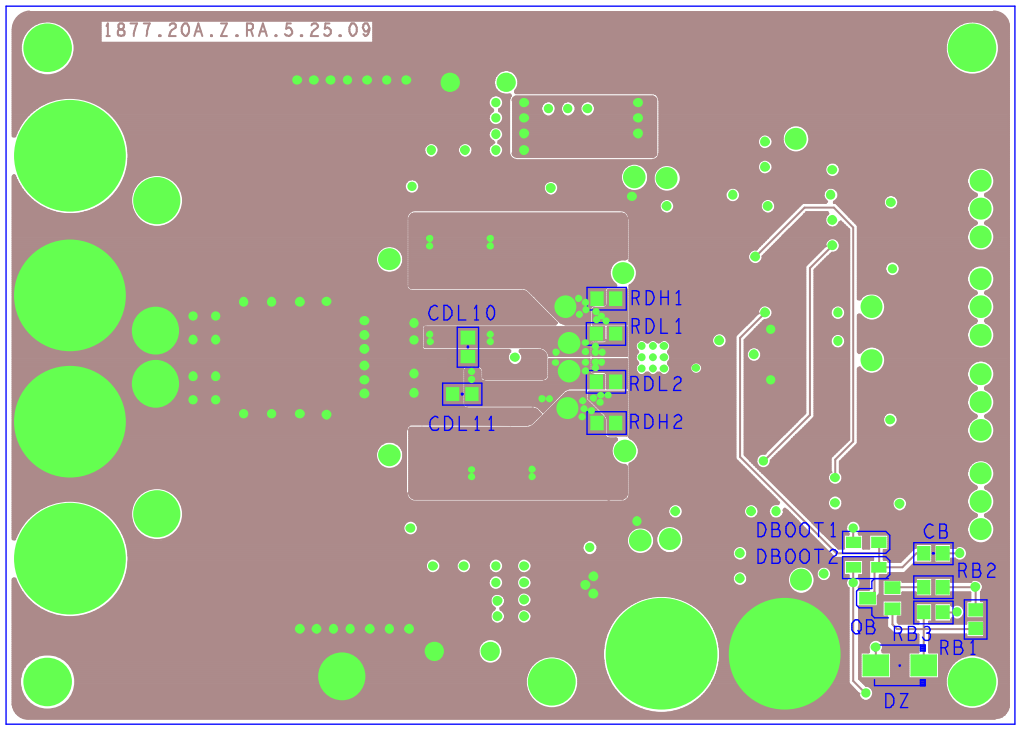

Figure 13. Bottom Layer (PGND Layer)

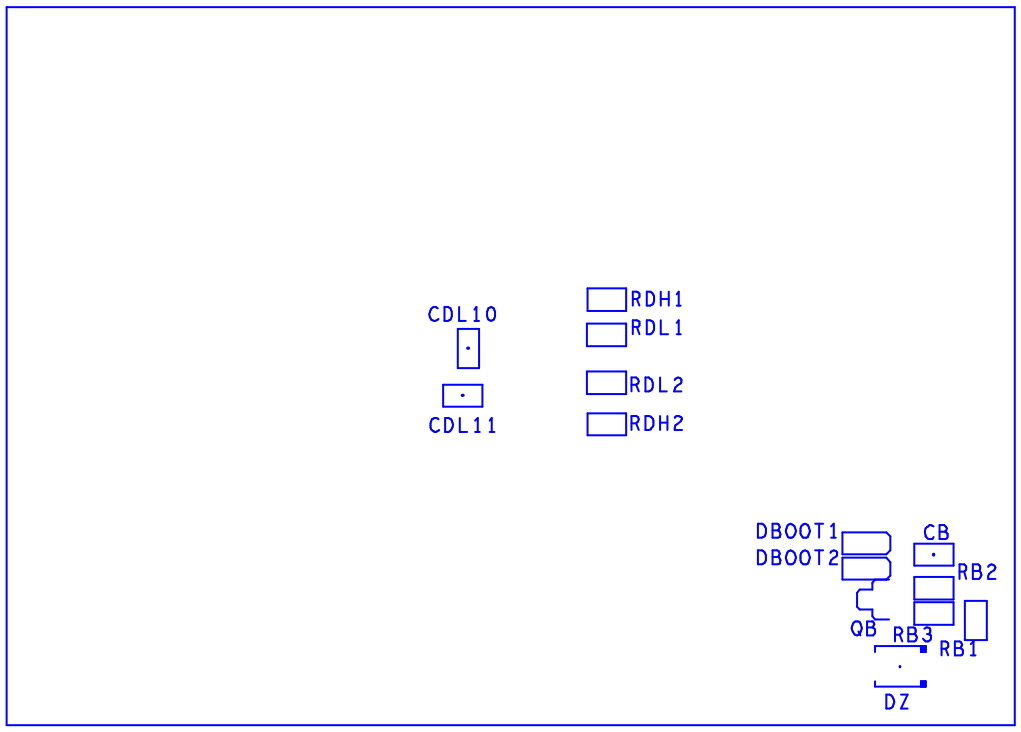

Figure 14. Bottom Silk Screen

ORDERING INFORMATION

BILL OF MATERIALS

Table 3. Component Listing

Description	Qty	Reference Designator	Manufacturer	Part No
Device Under Test, LFCSP	1	U1	Analog Devices	ADP1877
Input Capacitors				
MLCC, 10 µF, X7R, 25 V, 1210	4	CIN11, CIN12, CIN21, CIN22	Murata	GRM32DR71E106KA12
Miscellaneous Parts				
MLCC, 100 nF, X7R, 16 V, 0603	5	CSS1, CSS2, CBST1, CBST2, CVIN	Murata	GRM188R71E104KA01
MLCC, 1.0 µF, X5R, 6.3 V, 0603	2	CV5, CDR	Murata	GRM185R60J105KE21
Resistor, 2 Ω, 0603	1	RB0	Vishay	CRCW06032R00F
Resistor, 0 Ω, 0603	1	RVCCO	Vishay	CRCW06030R00F
Resistor, Not Fitted, 0603	1	RV5	Vishay	
Resistor, Not Fitted, 0603	1	RFREQ	Vishay	
Resistor, 22.6 kΩ, 0603	2	RGCS1, RGCS2	Vishay	CRCW06032262F
Resistor, 845 kΩ, 0603	2	RR1, RR2	Vishay	CRCW06038453F
Output Capacitors				
POSCAP, 330 µF, 2.5 V, 9 mΩ	4	COV1, COV2, COV11, COV21	Sanyo	2R5TPE330M9C2
MLCC, 22 µF, X5R, 0805	4	COV12, COV13, COV22, COV23	Murata	GRM21BR60J226ME39
Inductors				
Inductor, 1.2 µH, 1.8 mΩ, I_N = 20 A, I_{SAT} = 25 A	2	L1, L2	Würth Elektronik	744325120
Feedback Resistors				
Resistor, 10 kΩ, 0603	2	RF22, RF12	Vishay	CRCW06031002F
Resistor, 7.5 kΩ, 0603	1	RF21	Vishay	CRCW06037501F
Resistor, 20 kΩ, 0603	1	RF11	Vishay	CRCW06032002F
Resistor, 3.01 Ω, 0603	4	RDH1, RDH2, RDL1, RDL2	Vishay	CRCW06033R01F
Power MOSFETs				
N MOSFET, 30 V, 9 mΩ, Super-SO8	2	QH1, QH3	Infineon	BSC080N03LS
N MOSFET, 30 V, 4.5 mΩ, Super-SO8	2	QL2, QL3	Infineon	BSC030N03LS
Compensation				
MLCC, 680 pF, 0603	1	CC11	Vishay	VJ0603Y681KXAA
MLCC, 1.2 nF, 0603	1	CC21	Vishay	VJ0603Y122KXAA
MLCC, 68 pF, 0603	1	CC12	Vishay	VJ0603A121KXAA
MLCC, 120 pF, 0603	1	CC22	Vishay	VJ0603A680KXAA
Resistor, 31.6 kΩ, 0603	1	RC1	Vishay	CRCW06033162F
Resistor, 18.7 kΩ, 0603	1	RC2	Vishay	CRCW06031872F
Resistor, 1.82 kΩ, 0603	2	RLIM11, RLIM21	Vishay	CRCW06031821F
Snubber				
Capacitor, Ceramic, 1.2 nF, 0603	2	CSNB1, CSNB2	Vishay	VJ0603Y122KXAA
Resistor, 4.02 Ω, 0805	2	RSNB1, RSNB2	Vishay	CRCW08054R02F
Tracking				
Resistor, 100 kΩ, 0603	2	RT11, RT21	Vishay	CRCW06031003F
Resistor, 0603, Not Fitted	4	RT12, RT13, RT22, RT23	Vishay	
Resistor, 0603, Not Fitted	2	RT14, RT24	Vishay	
External LDO on Back Side (For High Input Voltage)				
Resistor, 0603, Not Fitted	1	RB2	Vishay	
Resistor, 0603, Not Fitted	2	RB1, RB3	Vishay	
MLCC, 0603, Not Fitted	1	CB	Vishay	
Bipolar Signal NPN, Not Fitted	1	QB		
Zener Diode, 5.5 V, Not Fitted	1	DZ		
Schottky Diode, Not Fitted	2	DBOOT1, DBOOT2		

Description	Qty	Reference Designator	Manufacturer	Part No
Resistor (PGOOD Pull-Up, Optional), 0603, Not Fitted	2	RPG1, RPG2	Vishay	
MLCC (optional), 0603, Not Fitted	2	CLIM, CLIM2		
3-Terminal Jumpers, 0.1 Inch Spacing	4	J1, J2, J3, J4	Any	
Test points, 40 mil (1 mm) Through Hole , Not Fitted	15	PGOOD1, PGOOD2, SS1, SS2, TRK1, TRK2, SW1, SW2, DH1, DH2, DL1, DL2, BST1, BST2, VDL	Any	
Test points, 110 mil Through Hole	6	T7, T8, T9, T10, T11, T12	Keystone Electronics Corp.	1502-1
Terminals, Banana Jack	6	VIN, GND, VOUT1, VOUT2, GND, GND	Keystone Electronics Corp.	575-4

ORDERING GUIDE

Model[1]	Description
ADP1877HC-EVALZ	Evaluation Board

[1] Z = RoHS Compliant Part.

ESD CAUTION

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

NOTES

EB08460-0-8/10(A)