Complementary Silicon Plastic Power Transistors

These devices are designed for use in general purpose amplifier and switching applications.

Features

- High Current Gain Bandwidth Product
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage BD243B, BD244B BD243C, BD244C	V _{CEO}	80 100	Vdc
Collector-Base Voltage BD243B, BD244B BD243C, BD244C	V _{CB}	80 100	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current – Continuous	I _C	6	Adc
Collector Current – Peak	I _{CM}	10	Adc
Base Current	I _B	2.0	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	65 0.52	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.92	°C/W



ON Semiconductor®

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6 AMPERE POWER TRANSISTORS COMPLEMENTARY SILICON 80-100 VOLTS 65 WATTS

PNP	NPN		
COLLECTOR 2, 4	COLLECTOR 2, 4		
BASE EMITTER 3	BASE EMITTER 3		



MARKING DIAGRAM

BD24xyG AY WW BD24xy = Device Code x = 3 or 4y = B or C

A = Assembly Location Y = Year

WW = Work Week
G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
BD243BG	TO-220 (Pb-Free)	50 Units / Rail
BD243CG	TO-220 (Pb-Free)	50 Units / Rail
BD244BG	TO-220 (Pb-Free)	50 Units / Rail
BD244CG	TO-220 (Pb-Free)	50 Units / Rail

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Sustaining Voltage (Note 1) (I _C = 30 mAdc, I _B = 0) BD243B, BD244B BD243C, BD244C	V _{CEO(sus)}	80 100	_ _	Vdc
Collector Cutoff Current (V _{CE} = 60 Vdc, I _B = 0) BD243B, BD243C, BD244B, BD244C	I _{CEO}	-	0.7	mAdc
Collector Cutoff Current (V _{CE} = 80 Vdc, V _{EB} = 0) BD243B, BD244B (V _{CE} = 100 Vdc, V _{EB} = 0) BD243C, BD244C	I _{CES}	-	400 400	μAdc
Emitter Cutoff Current $(V_{BE} = 5.0 \text{ Vdc}, I_{C} = 0)$	I _{EBO}	-	1.0	mAdc
ON CHARACTERISTICS (Note 1)				
DC Current Gain ($I_C = 0.3$ Adc, $V_{CE} = 4.0$ Vdc) ($I_C = 3.0$ Adc, $V_{CE} = 4.0$ Vdc)	h _{FE}	30 15		_
Collector–Emitter Saturation Voltage ($I_C = 6.0 \text{ Adc}$, $I_B = 1.0 \text{ Adc}$)	V _{CE(sat)}	-	1.5	Vdc
Base–Emitter On Voltage (I _C = 6.0 Adc, V _{CE} = 4.0 Vdc)	V _{BE(on)}	-	2.0	Vdc
DYNAMIC CHARACTERISTICS				
Current–Gain – Bandwidth Product (Note 2) $(I_C = 500 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f_{test} = 1.0 \text{ MHz})$	f _T	3.0	-	MHz
Small-Signal Current Gain (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{fe}	20	_	_

^{1.} Pulse Test: Pulsewidth ≤ 300 μs, Duty Cycle ≤ 2.0%.

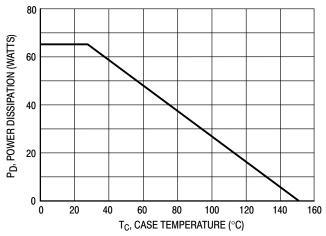
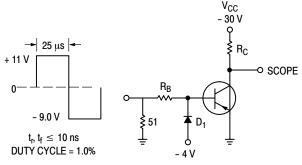


Figure 1. Power Derating

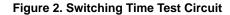
^{2.} $f_T = h_{fe} \bullet f_{test}$



 R_B and R_C varied to obtain desired current levels D_1 must be fast recovery type eg. 1N5825 used above $I_B\approx 100$ ma MSD6100 used below $I_B\approx 100$ ma

2.0 $T_J = 25^{\circ}C$ 1.0 $V_{CC} = 30 \text{ V}$ 0.7 $I_C/I_B = 10$ 0.5 t, TIME (µs) 0.3 0.2 0.1 $t_d @ V_{BE(off)} = 5.0 V$ 0.07 0.05 0.03 0.02 🗀 0.1 0.4 0.6 1.0 4.0 6.0 I_C, COLLECTOR CURRENT (AMP)

Figure 3. Turn-On Time



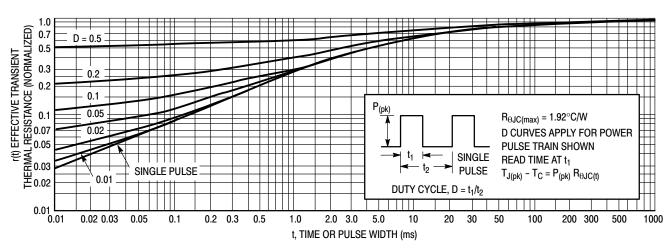


Figure 4. Thermal Response

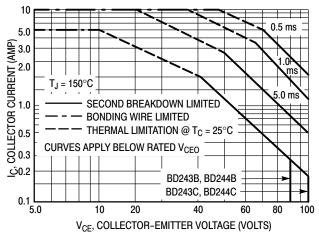


Figure 5. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on $T_{J(pk)} = 150^{\circ}\text{C}$: T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \leq 150^{\circ}\text{C}$, $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

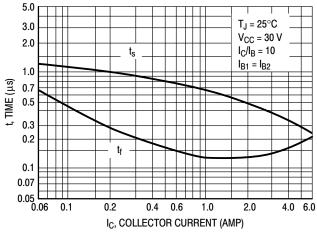


Figure 6. Turn-Off Time

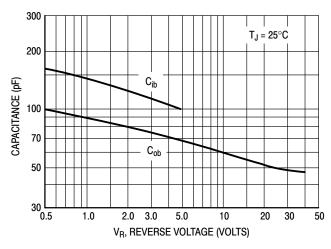


Figure 7. Capacitance

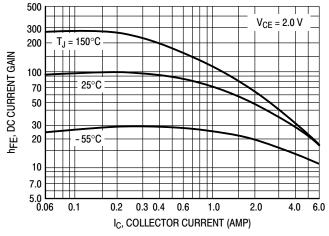


Figure 8. DC Current Gain

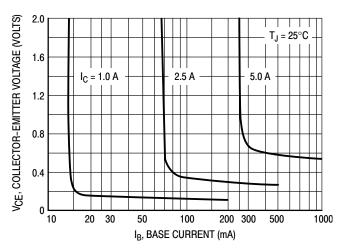


Figure 9. Collector Saturation Region

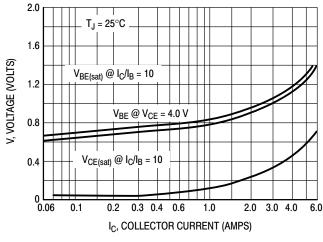


Figure 10. "On" Voltages

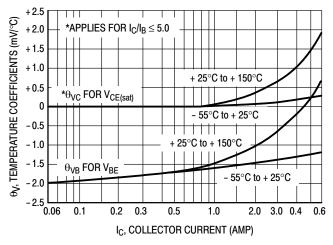


Figure 11. Temperature Coefficients

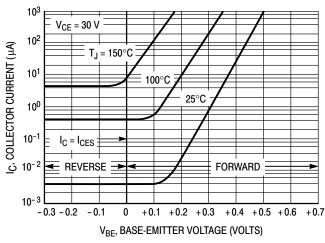


Figure 12. Collector Cut-Off Region

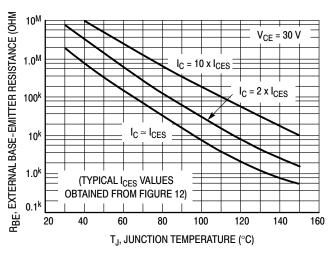
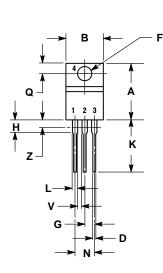
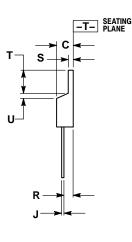


Figure 13. Effects of Base-Emitter Resistance

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AG**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.405	9.66	10.28	
С	0.160	0.190	4.07	4.82	
D	0.025	0.036	0.64	0.91	
F	0.142	0.161	3.61	4.09	
G	0.095	0.105	2.42	2.66	
Н	0.110	0.161	2.80	4.10	
J	0.014	0.025	0.36	0.64	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
N	0.190	0.210	4.83	5.33	
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
T	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
٧	0.045		1.15		
Z		0.080		2.04	

STYLE 1:

PIN 1. BASE

- COLLECTOR
- EMITTER COLLECTOR

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