



2STX2220

High Gain Low Voltage
PNP Power Transistor

General features

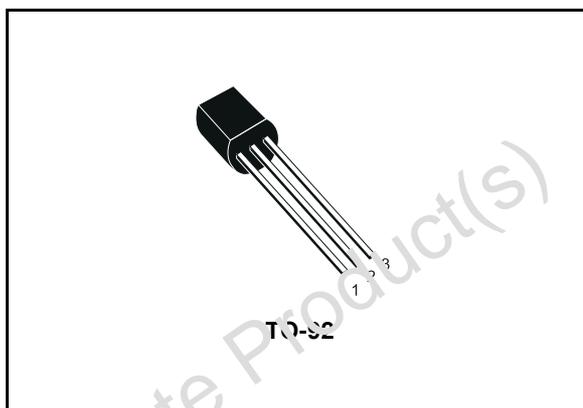
- Very low Collector to Emitter saturation voltage
- D.C. Current gain, $h_{FE} > 100$
- 1.5 A continuous collector current
- In compliance with the 2002/93/EC European Directive

Description

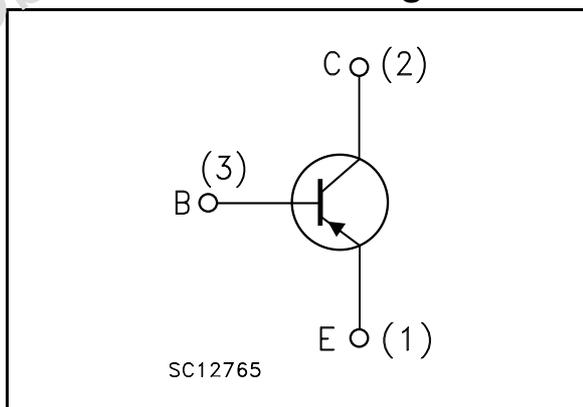
The device is a PNP transistor manufactured using new "PB-HDC" (Power Bipolar High Density Current) technology. The resulting transistor shows exceptional high gain performances coupled with very low saturation voltage.

Applications

- Power management in portable equipment
- Switching regulator in battery charger applications



Internal schematic diagram



Order codes

Part Number	Marking	Package	Packing
2STX2220	X2220	TO-92	Bulk

Contents

1	Electrical ratings	3
2	Electrical characteristics	4
2.1	Electrical characteristics (curves)	5
2.2	Test circuits	7
3	Package mechanical data	8
4	Revision history	10

Obsolete Product(s) - Obsolete Product(s)

1 Electrical ratings

Table 1. Absolute maximum rating

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-base voltage ($I_E = 0$)	-20	V
V_{CEO}	Collector-emitter voltage ($I_B = 0$)	-20	V
V_{EBO}	Emitter-base voltage ($I_C = 0$)	-5	V
I_C	Collector current	-1.5	A
I_{CM}	Collector peak current ($t_P < 5\text{ms}$)	-3	A
I_B	Base current	-0.1	A
I_{BM}	Base peak current ($t_P < 5\text{ms}$)	-0.2	A
P_{tot}	Total dissipation at $T_{amb} = 25^\circ\text{C}$	0.9	W
T_{stg}	Storage temperature	-65 to 150	$^\circ\text{C}$
T_J	Max. operating junction temperature	150	$^\circ\text{C}$

Table 2. Thermal data

Symbol	Parameter	Value	Unit
$R_{thj-case}$	Thermal resistance junction-case max	44.6	$^\circ\text{C/W}$
$R_{thj-amb}$	Thermal resistance junction-amb max	139	$^\circ\text{C/W}$

2 Electrical characteristics

($T_{\text{case}} = 25^{\circ}\text{C}$ unless otherwise specified)

Table 3. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{CBO}	Collector cut-off current ($I_{\text{E}} = 0$)	$V_{\text{CB}} = -20\text{V}$			-0.1	μA
I_{EBO}	Emitter cut-off current ($I_{\text{C}} = 0$)	$V_{\text{EB}} = -5\text{V}$			-0.1	μA
$V_{(\text{BR})\text{CEO}}^{(2)}$	Collector-emitter breakdown voltage ($I_{\text{B}} = 0$)	$I_{\text{C}} = -10\text{mA}$	-20			V
$V_{(\text{BR})\text{EBO}}$	Emitter-base breakdown voltage ($I_{\text{C}} = 0$)	$I_{\text{E}} = -100\mu\text{A}$	-5			V
$V_{\text{CE}(\text{sat})}^{(2)}$	Collector-emitter saturation voltage	$I_{\text{C}} = -0.5\text{A}$ $I_{\text{B}} = -50\text{mA}$ $I_{\text{C}} = -1.5\text{A}$ $I_{\text{B}} = -150\text{mA}$			-0.25 -0.45	V V
$V_{\text{BE}(\text{sat})}^{(2)}$	Base-emitter saturation voltage	$I_{\text{C}} = -0.5\text{A}$ $I_{\text{B}} = -50\text{mA}$ $I_{\text{C}} = -1.5\text{A}$ $I_{\text{B}} = -150\text{mA}$			-1 -1.1	V V
$V_{\text{BE}(\text{on})}^{(2)}$	Base-emitter on voltage	$I_{\text{C}} = -1\text{A}$ $V_{\text{CE}} = -2\text{V}$			-1	V
$h_{\text{FE}}^{(2)}$	DC current gain	$I_{\text{C}} = -100\text{mA}$ $V_{\text{CE}} = -2\text{V}$ $I_{\text{C}} = -500\text{mA}$ $V_{\text{CE}} = -2\text{V}$ $I_{\text{C}} = -1.5\text{A}$ $V_{\text{CE}} = -2\text{V}$ $I_{\text{C}} = -3\text{A}$ $V_{\text{CE}} = -2\text{V}$	200 170 120 75		600	
C_{CBQ}	Collector-base capacitance	$I_{\text{E}} = 0$ $V_{\text{CB}} = -10\text{V}$ $f = 1\text{MHz}$		30		pF
t_{on}	Resistive load Turn-on time	$I_{\text{C}} = -1.5\text{A}$ $V_{\text{CC}} = -10\text{V}$		60		ns
t_{off}	Turn-off time	$I_{\text{B}1} = -I_{\text{B}2} = -150\text{mA}$		250		ns

Note (2) Pulsed duration = 300 μs , duty cycle $\leq 1.5\%$

2.1 Electrical characteristics (curves)

Figure 1. Output characteristics

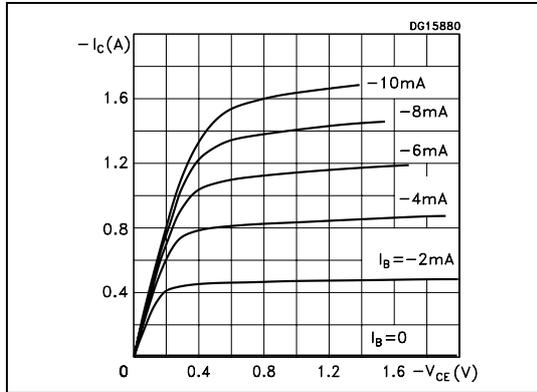


Figure 2. DC current gain

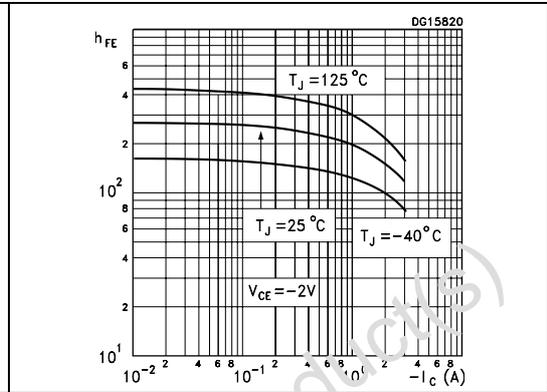


Figure 3. DC current gain

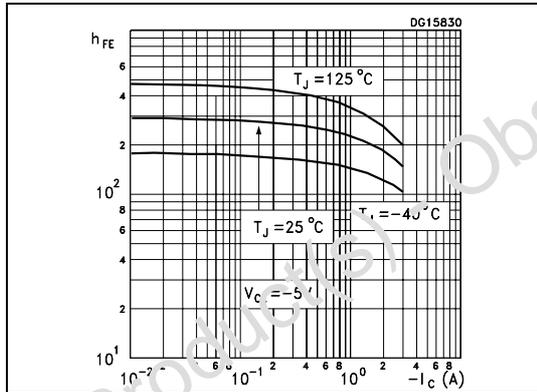


Figure 4. Collector-emitter saturation voltage

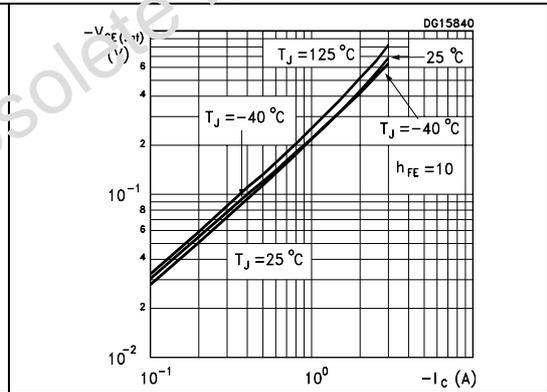


Figure 5. Base-emitter saturation voltage

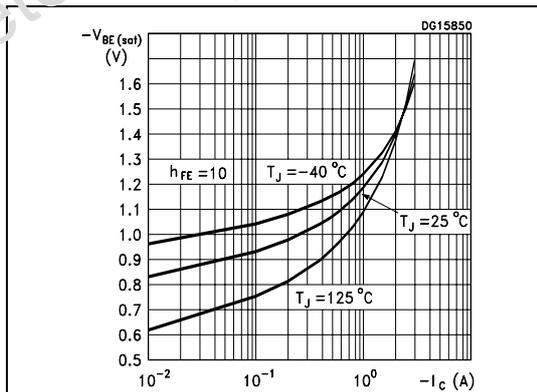


Figure 6. Base-emitter on voltage

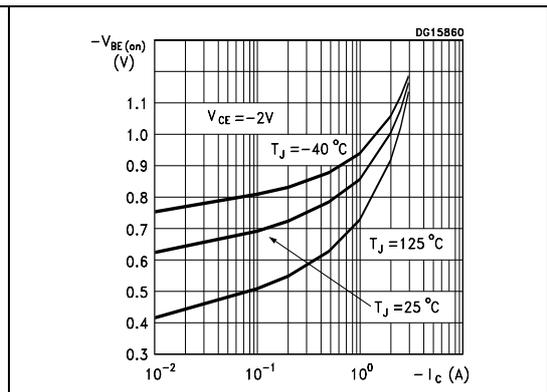


Figure 7. Capacitance curves

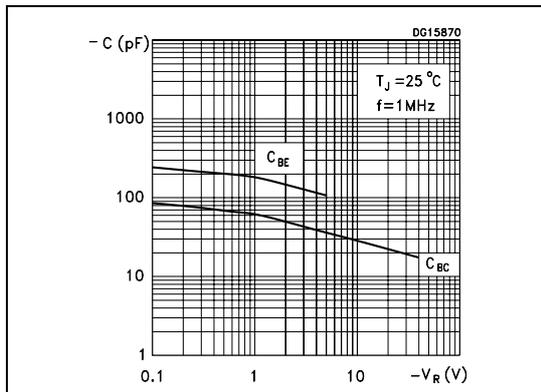


Figure 8. Switching time resistive load

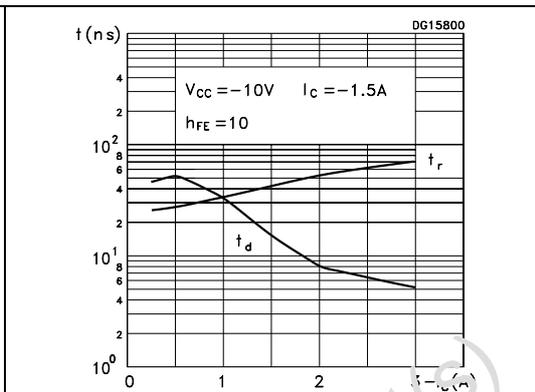
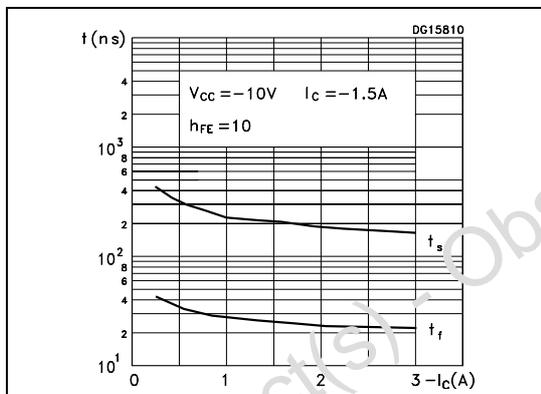
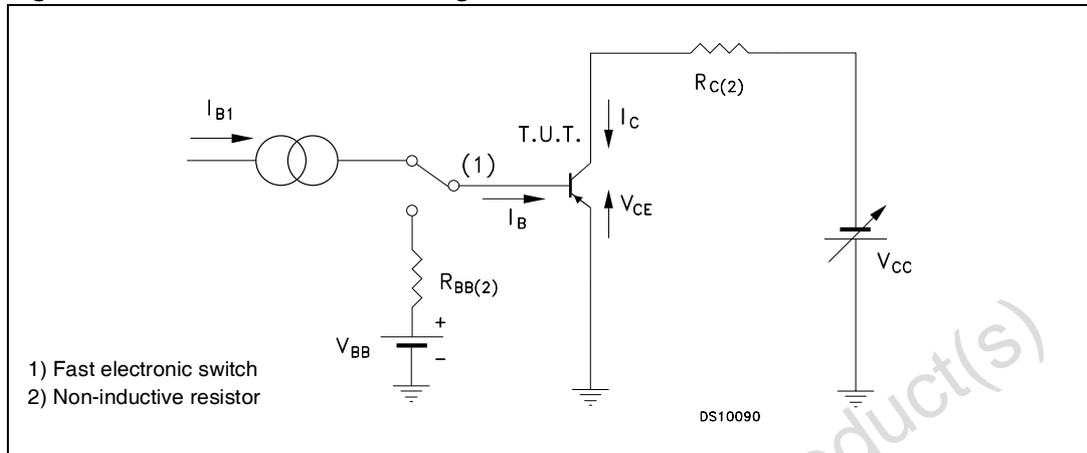


Figure 9. Switching time resistive load



2.2 Test circuits

Figure 10. Resistive load switching test circuit



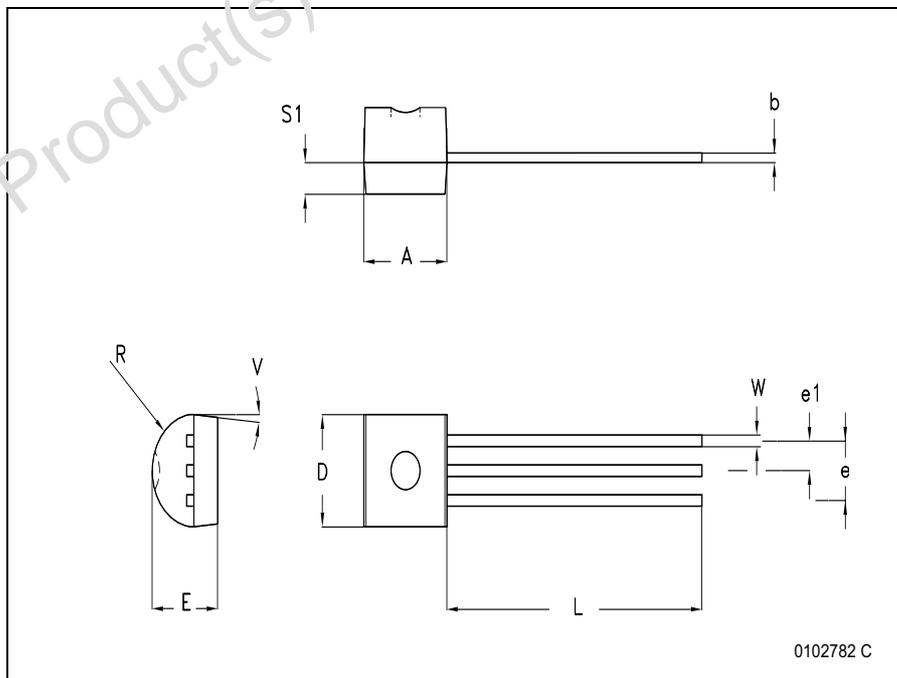
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

Obsolete Product(s) - Obsolete Product(s)

TO-92 BULK SHIPMENT MECHANICAL DATA

DIM.	mm.		
	MIN.	TYP	MAX.
A	4.32		4.95
b	0.36		0.51
D	4.45		4.95
E	3.30		3.94
e	2.41		2.67
e1	1.14		1.40
L	12.70		15.49
R	2.16		2.41
S1	0.92		1.52
W	0.41		0.56
V		5°	



4 Revision history

Table 4. Revision history

Date	Revision	Changes
19-Jun-2006	1	Initial release.
25-Sep-2006	2	New maturity code.

Obsolete Product(s) - Obsolete Product(s)

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED REPRESENTATIVE OF ST, ST PRODUCTS ARE NOT DESIGNED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS, WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2006 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

