

STR9 ZigBee[®] extension board for the SN260 network processor

Data Brief

Features

- Digital interfaces:
 - 16 general purpose I/Os (GPIOs)
 - One I²C bus interface
 - Two SPI bus interfaces: one is used to interface with the STR9 dongle and the other is available to extend the board with another SPI device
 - 2 external interrupts
- Digital MEMS in an LGA14 package (optional)
- 1 debug port
- 1 button and 2 LEDs

Description

The STR9 ZigBee extension board provides an SN260 ZigBee interface to an application using the STR9 dongle (part number STEVAL-IFD001V1).

The STMicroelectronics SN260 integrates a 2.4 GHz, IEEE 802.15.4-compliant transceiver with a 16-bit network processor (XAP2b core) to run EmberZNet™, the ZigBee-compliant network stack.

The SN260 accesses the EmberZNet API across a standard SPI module, allowing application development on a host processor. This means that the SN260 can be viewed as a ZigBee peripheral connected over an SPI. The XAP2b microprocessor is a power-optimized core integrated in the SN260. It contains integrated Flash and RAM memory along with an optimized peripheral set to enhance the operation of the network stack. For additional information regarding the SN260, please refer to the device datasheet.

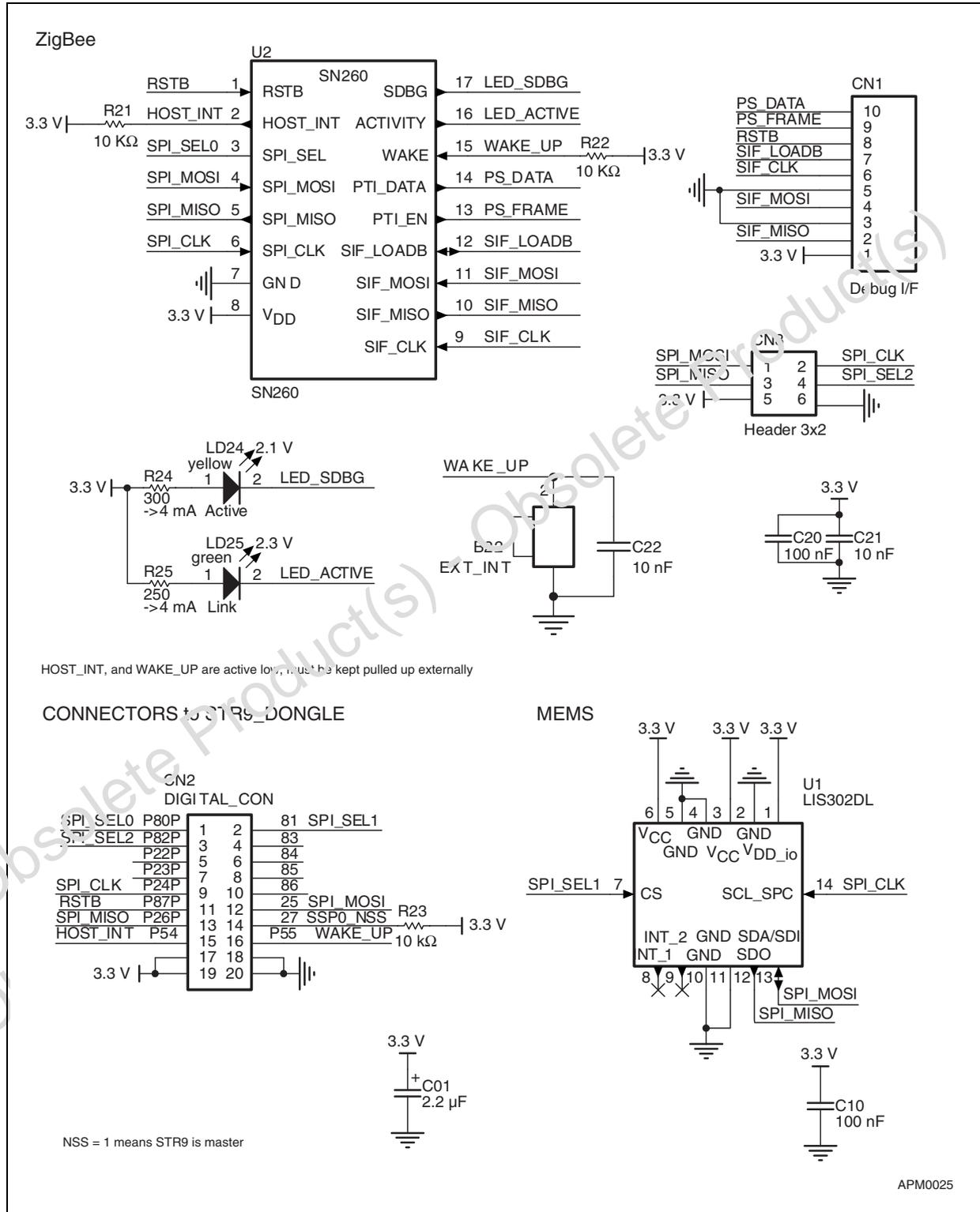
This document should be used in conjunction with user manual UM0282, which describes the STR9 dongle (part number STEVAL-IFD001V1).



STEVAL-IFS010V1

1 Block diagram

Figure 1. STR9 ZigBee extension board schematic



2 Revision history

Table 1. Document revision history

Date	Revision	Changes
07-Jul-2008	1	Initial release.

Obsolete Product(s) - Obsolete Product(s)
Obsolete Product(s) - Obsolete Product(s)

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