

Evaluation board for SPEAr300 embedded MPU

Data brief

Features

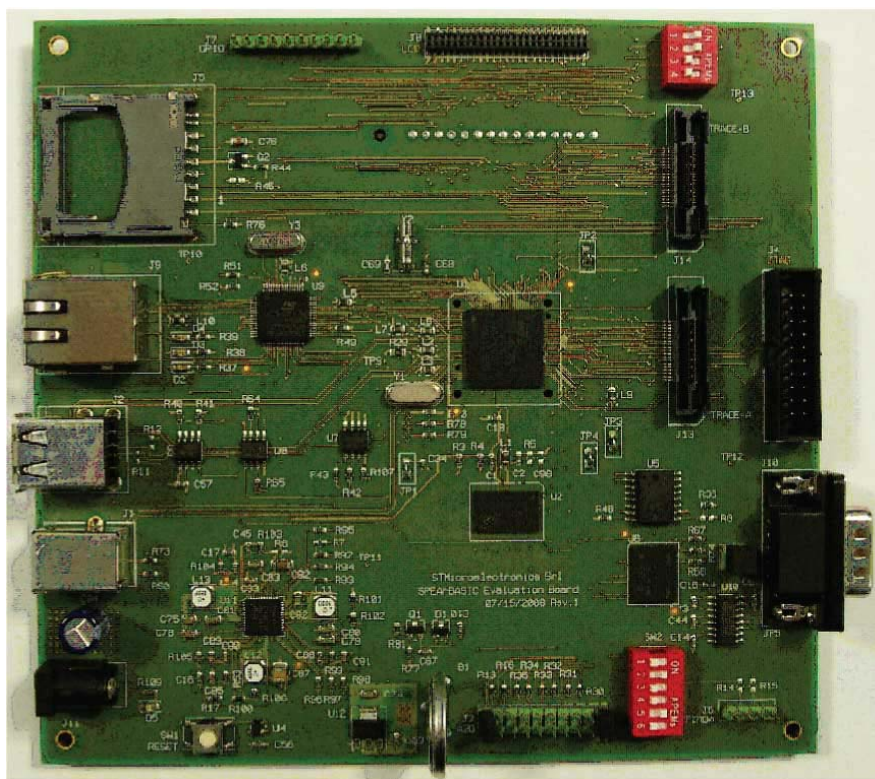
- SPEAr300 embedded MPU
- Up to 2 Gbits DDR2 333 MHz (std 128 MB)
- Up to 16 MB serial Flash memory (std 8 MB)
- Up to 2 Gb NAND Flash memory (std 64 MB)
- 4 Kb serial I2C
- 4 Mb SPI Flash memory
- Two USB 2.0 full Host port channels
- One USB 2.0 HS Device
- 10/100 Ethernet port
- One serial port (up to 115 Kbaud)
- Debug ports (JTAG + ETM9)

- 8 ADC channels (10 bit, 1 Msamples)
- 8 GPIOs
- LCD I/F up to 24 bits-per-pixel (bpp)
- SD card interface

Description

The EVALSPEAr300 evaluation board allows you to quickly evaluate and debug software for the SPEAr300. It acts as a learning tool to rapidly get familiar with the SPEAr300 features and provides a starting point for the development of the final application board. It is equipped with most of the interfaces offered by the SPEAr300.

Figure 1. EVALSPEAr300 evaluation board



Revision history

Table 1. Document revision history

Date	Revision	Changes
20-Sep-2010	1	Initial release.

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