



CPH5506

Bipolar Transistor

(-30V, (-)5A, Low $V_{CE(sat)}$ Complementary Dual CPH5

ON Semiconductor®

<http://onsemi.com>

Applications

- Relay drivers, Lamp drivers, Motor drivers

Features

- Composite type with a PNP transistor and an NPN transistor contained in one package, facilitating high-density mounting
- The CPH5506 consists of two chips encapsulated in a package which are equivalent to the CPH3115 and the CPH3215, respectively
- Ultrasmall package facilitate miniaturization in end products. (0.9mm mounting height)

Specifications () : PNP

Absolute Maximum Ratings at $T_a=25^{\circ}\text{C}$

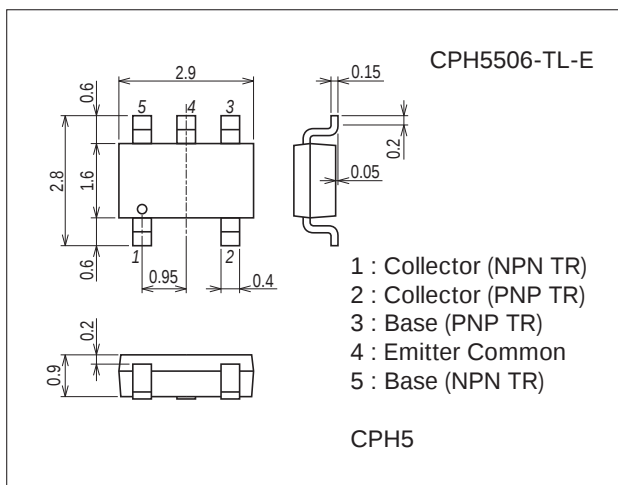
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CBO}		(-30)40	V
Collector-to-Emitter Voltage	V_{CEO}		(-)30	V
Emitter-to-Base Voltage	V_{EBO}		(-)5	V
Collector Current	I_C		(-)1.5	A
Collector Current (Pulse)	I_{CP}		(-)5	A
Base Current	I_B		(-)300	mA
Collector Dissipation	P_C	Mounted on a ceramic board (600mm ² ×0.8mm)	0.9	W
Total Power Dissipation	P_T	Mounted on a ceramic board (600mm ² ×0.8mm)	1.2	W
Junction Temperature	T_J		150	°C
Storage Temperature	T_{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

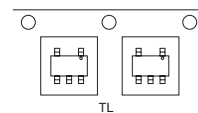
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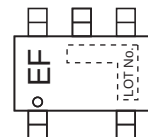
Product & Package Information

- Package : CPH5
- JEITA, JEDEC : SC-74A, SOT-25
- Minimum Packing Quantity : 3,000 pcs./reel

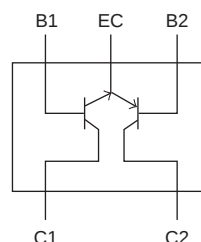
Packing Type : TL



Marking



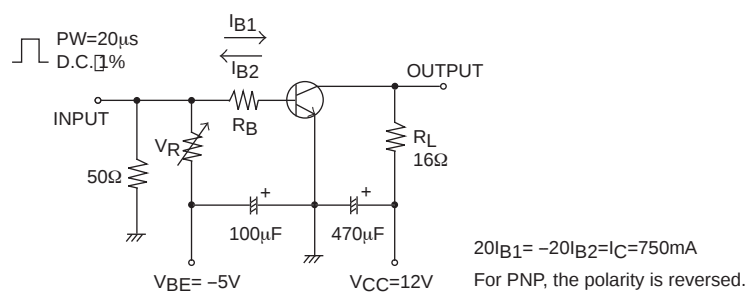
Electrical Connection



Electrical Characteristics at Ta=25°C

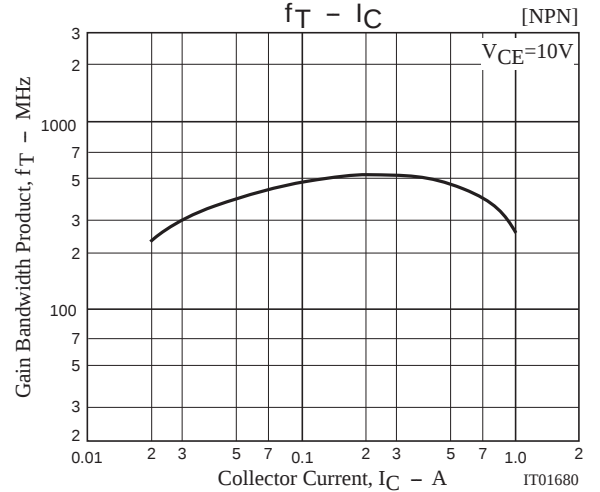
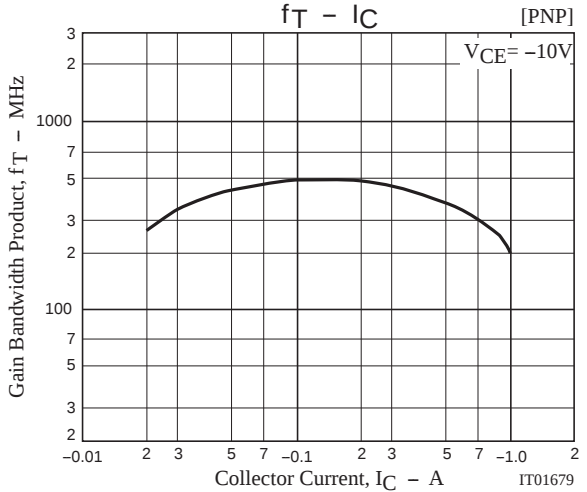
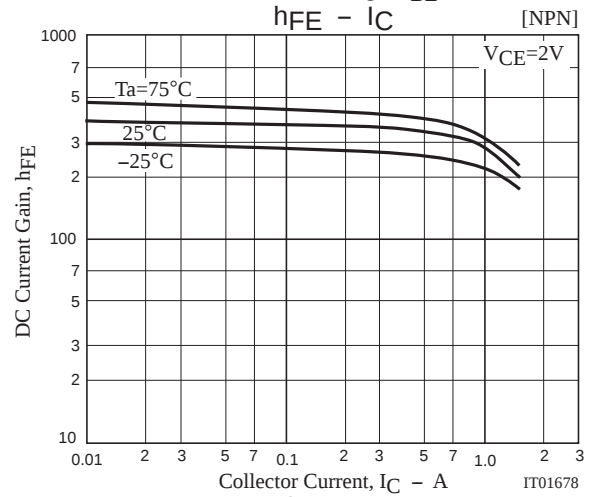
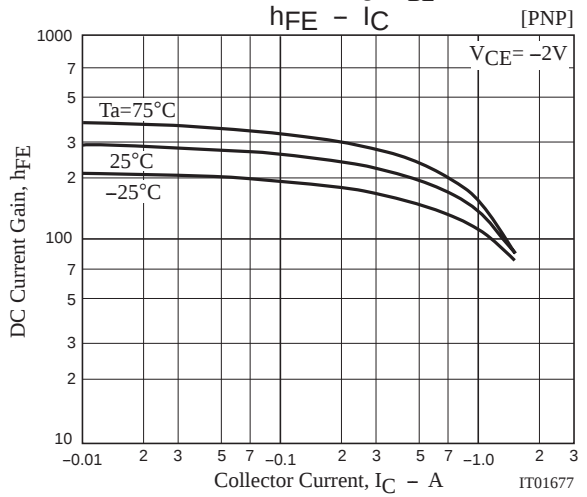
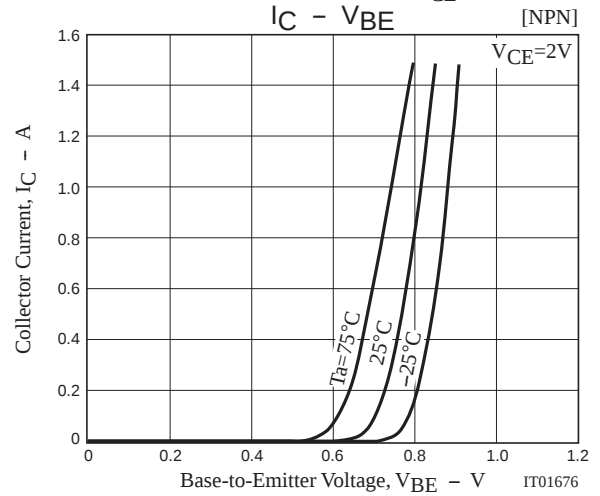
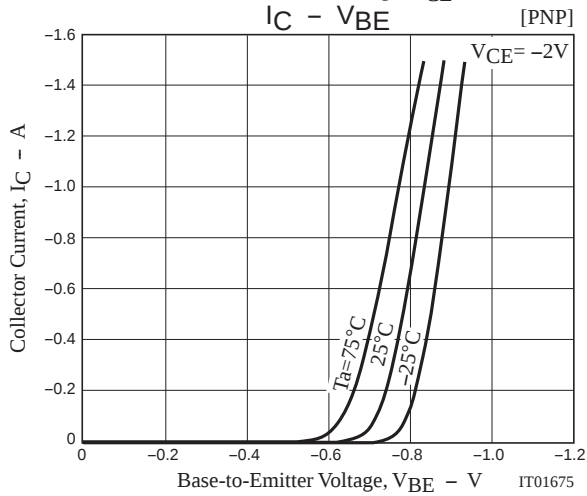
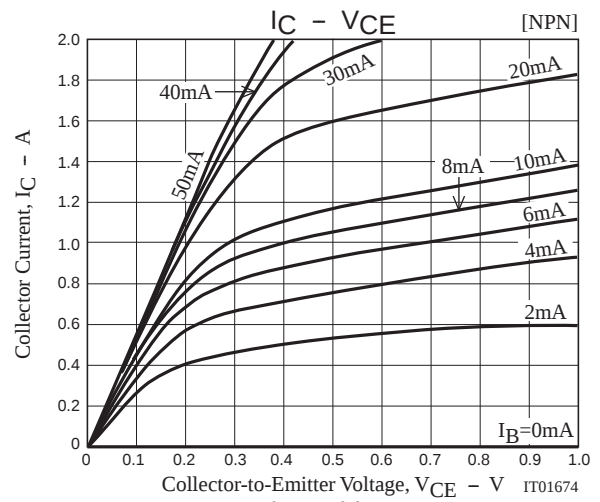
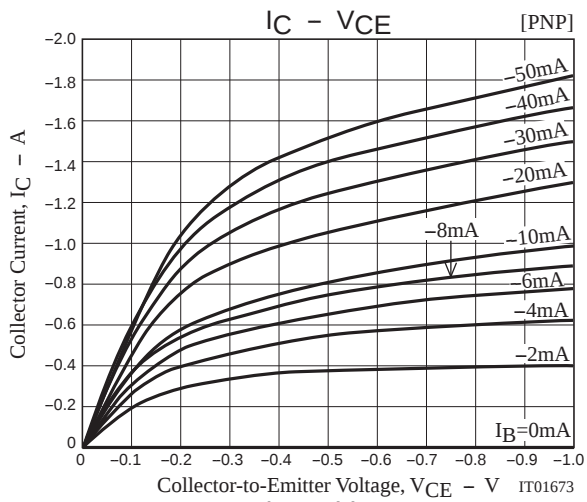
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V _{CB} =(-)30V, I _E =0A			(-)0.1	μA
Emitter Cutoff Current	IEBO	V _{EB} =(-)4V, I _C =0A			(-)0.1	μA
DC Current Gain	h _{FE}	V _{CE} =(-)2V, I _C =(-)100mA	200		560	
Gain-Bandwidth Product	f _T	V _{CE} =(-)10V, I _C =(-)300mA		(450)500		MHz
Output Capacitance	Cob	V _{CB} =(-)10V, f=1MHz		(9)8		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)750mA, I _B =(-)15mA		(-250)150	(-375)225	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)750mA, I _B =(-)15mA		(-)0.85	(-)1.2	V
Collector-to-Base Breakdown Voltage	V _{(BR)CBO}	I _C =(-)10μA, I _E =0A	(-30)40			V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C =(-)1mA, R _{BE} =∞	(-)30			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)10μA, I _C =0A	(-)5			V
Turn-On Time	t _{on}	See specified Test Circuit.		35		ns
Storage Time	t _{stg}			(115)205		ns
Fall Time	t _f			30		ns

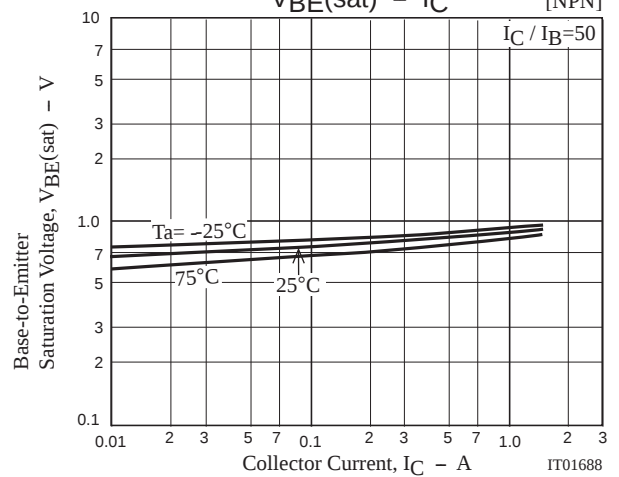
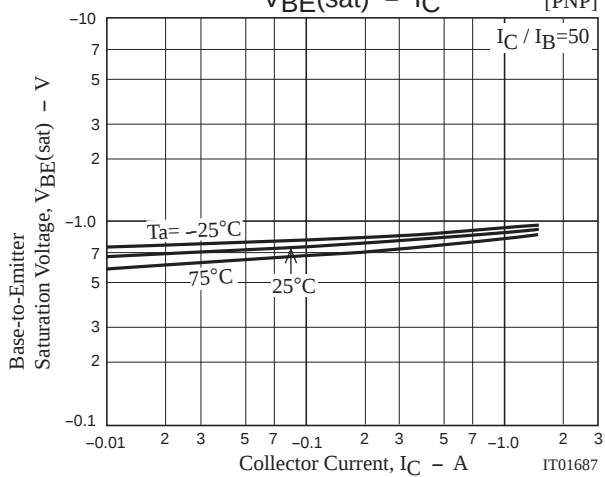
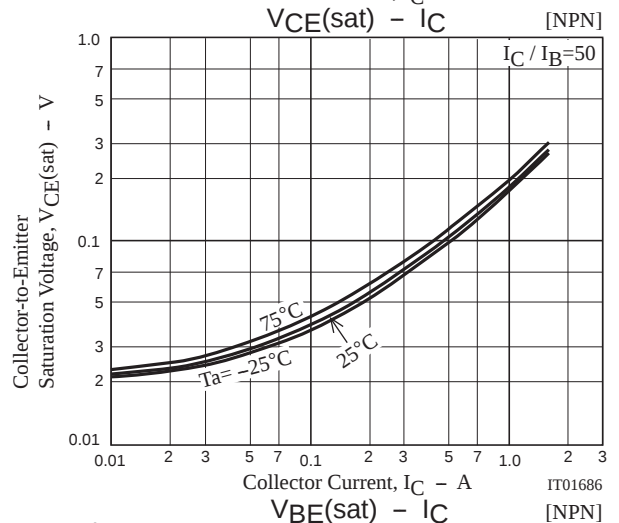
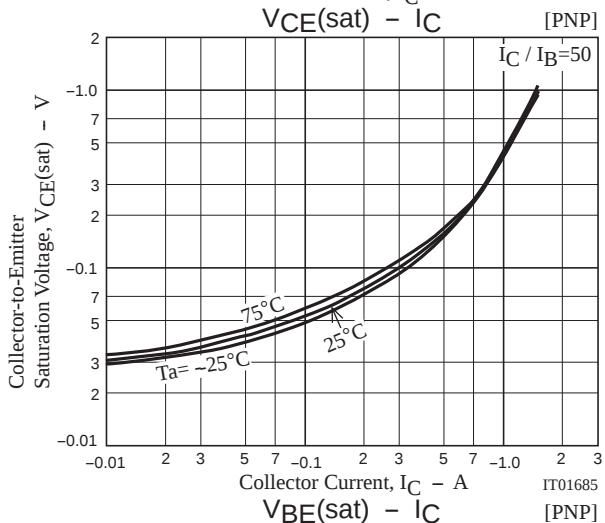
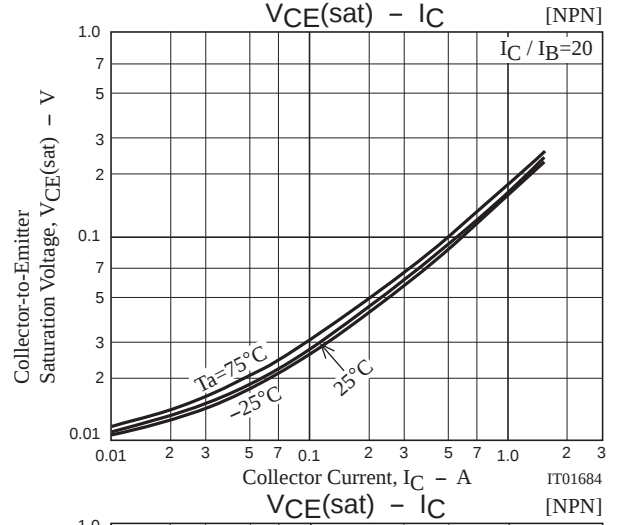
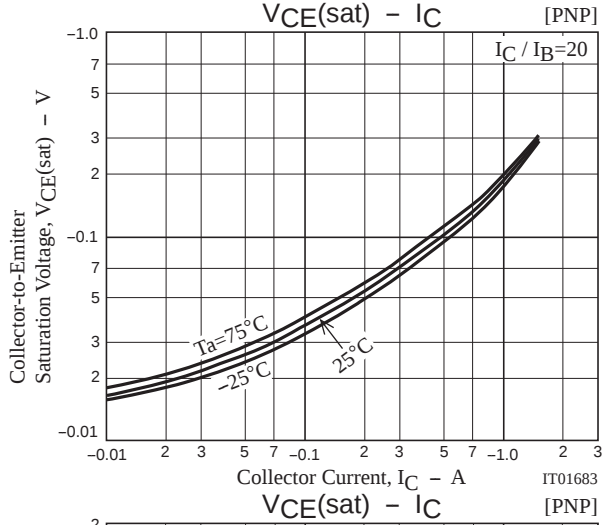
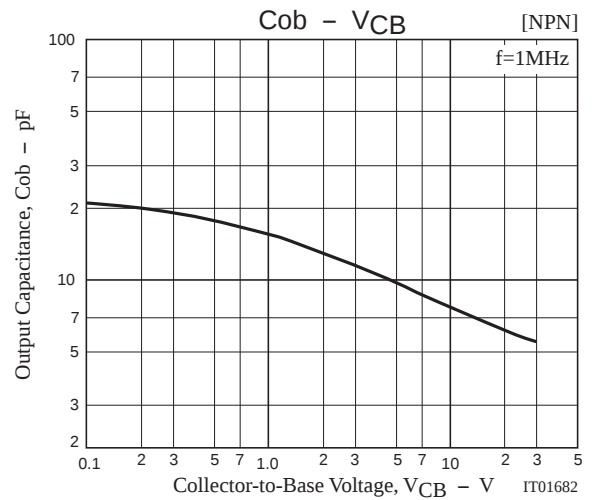
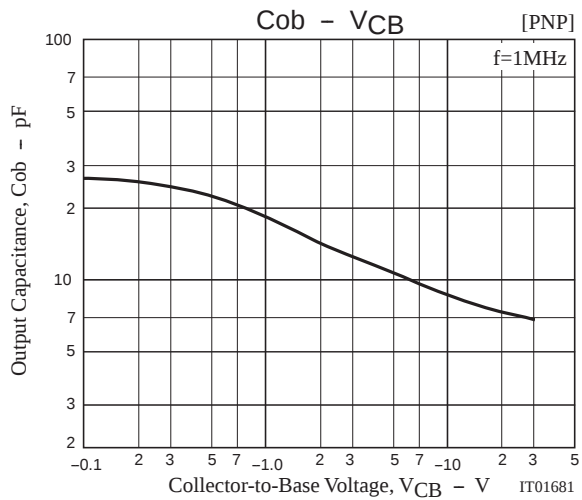
Switching Time Test Circuit

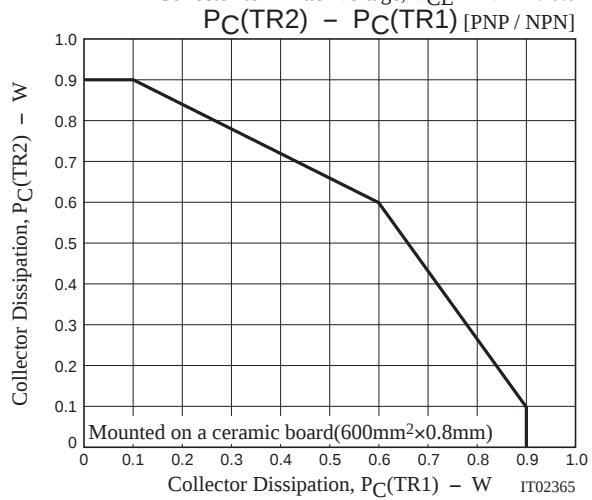
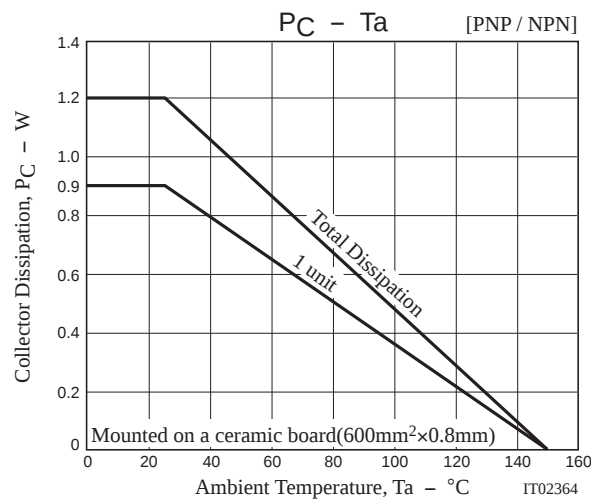
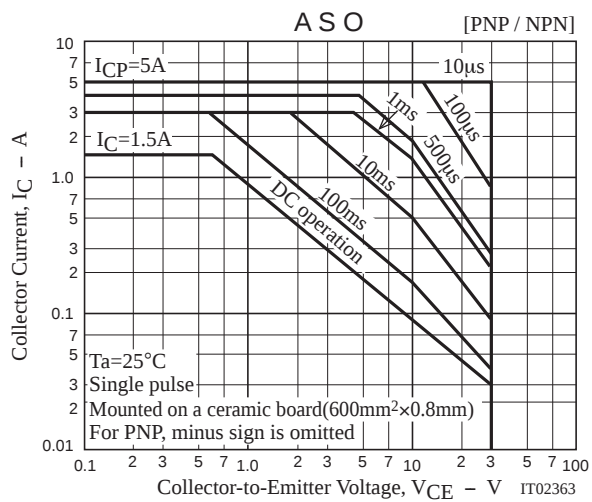


Ordering Information

Device	Package	Shipping	memo
CPH5506-TL-E	CPH5	3,000pcs./reel	Pb Free







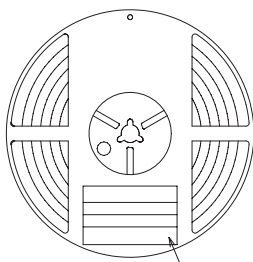
Embossed Taping Specification

CPH5506-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH5	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

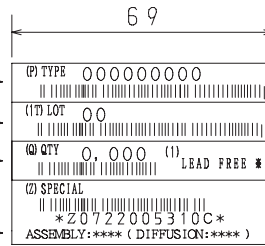
Packing method



Reel label

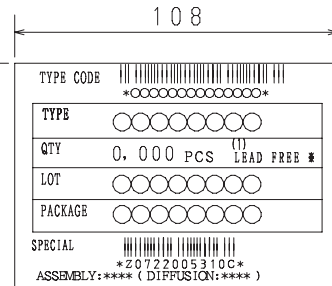
Type No.
LOT No.
Quantity
Origin

Reel label, Inner box label
(unit:mm)



Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical
distribution process.



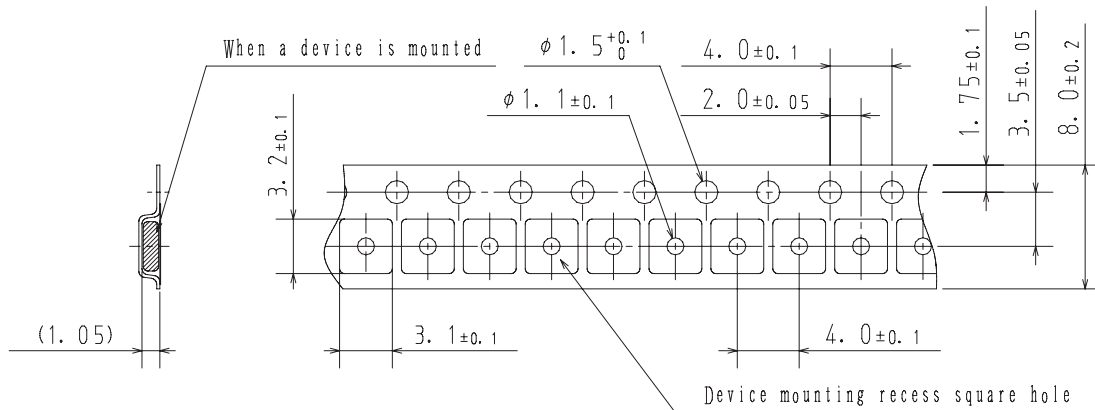
NOTE (1)

The LEAD FREE * description shows that the surface
treatment of the terminal is lead free.

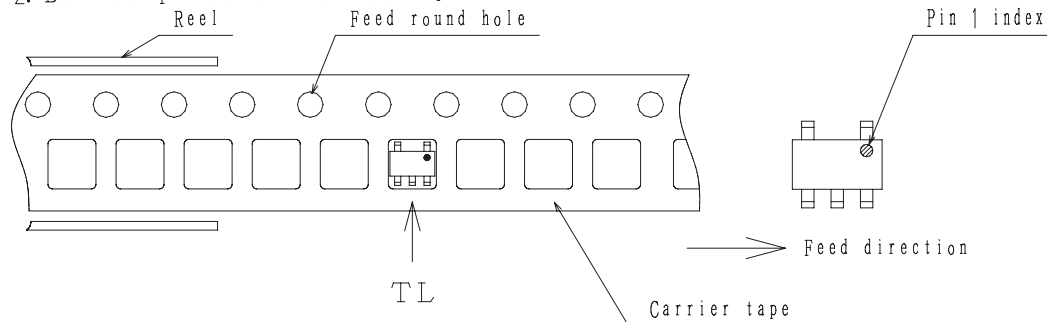
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

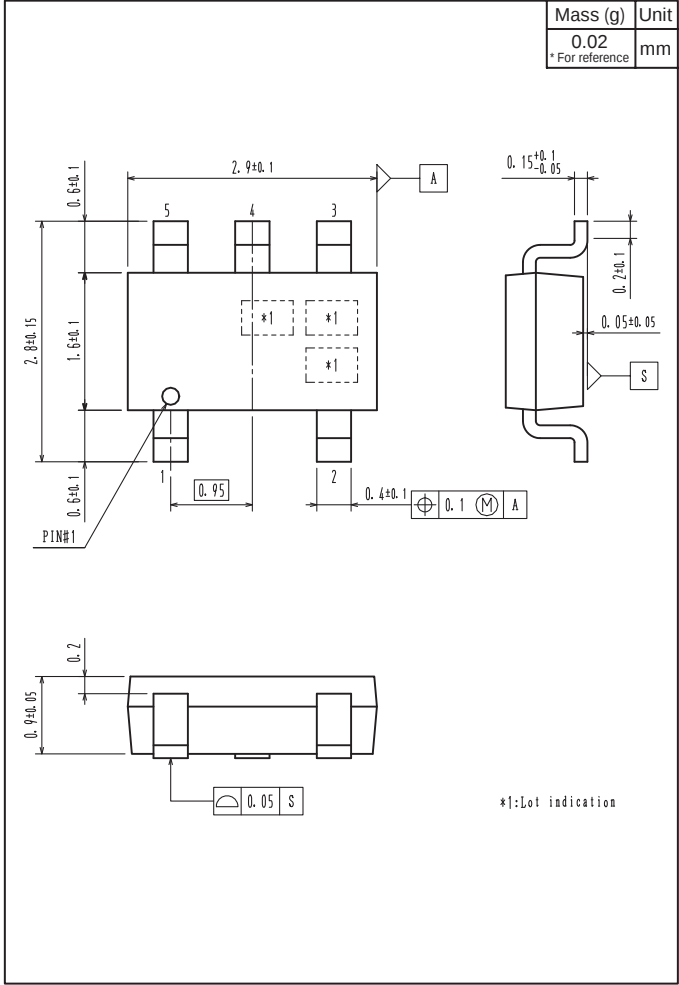


2-2. Device placement direction

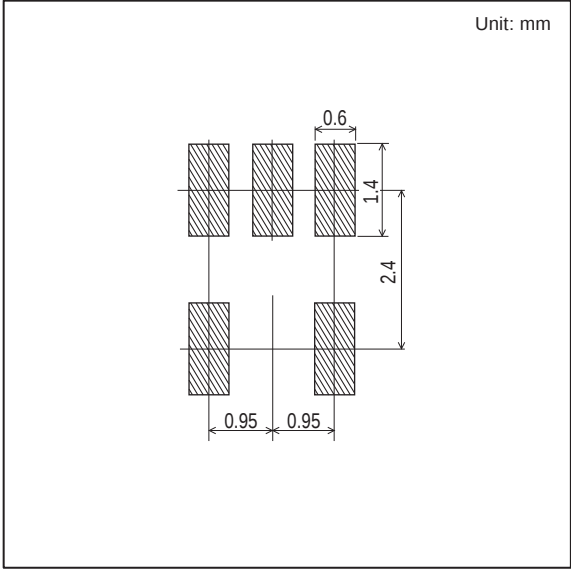


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
CPH5506-TL-E



Land Pattern Example



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