

# CPW3-1700S025-Silicon Carbide Schottky Diode Chip

Z-REC<sup>TM</sup> RECTIFIER

 $\mathbf{V}_{\mathsf{RRM}} = 1700 \ \mathsf{V}$ 

 $\mathbf{I}_{\mathsf{F}(\mathsf{AVG})} = 25 \; \mathsf{A}$ 

 $Q_{a} = 170 \text{ nC}$ 

Features Chip Outline

- 1700-Volt Schottky Rectifier
- Zero Reverse Recovery
- Zero Forward Recovery
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V<sub>E</sub>



Part Number	Anode	Cathode	Package	Marking
CPW3-1700S025B	Al	Ni/Ag	Sawn on Foil	Wafer # on Foil

# **Maximum Ratings**

Symbol	Parameter	Value	Unit	Test Conditions	Note
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	1700	V		
V <sub>RSM</sub>	Surge Peak Reverse Voltage	1700	V		
V <sub>DC</sub>	DC Blocking Voltage	1700	V		
$I_{F(AVG)}$	Average Forward Current	25	А	T <sub>1</sub> =175°C	
I <sub>FRM</sub>	Repetitive Peak Forward Surge Current	82 43	А	$T_c$ =25°C, $t_p$ =10 ms, Half Sine Wave, D=1 $T_c$ =110°C, $t_p$ =10 ms, Half Sine Wave, D=1	1
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current	117 84	А	$T_c$ =25°C, $t_p$ =10 ms, Half Sine Wave, D=1 $T_c$ =110°C, $t_p$ =10 ms, Half Sine Wave, D=1	1
T <sub>J</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature	-55 to +175	°C		

#### **Electrical Characteristics**

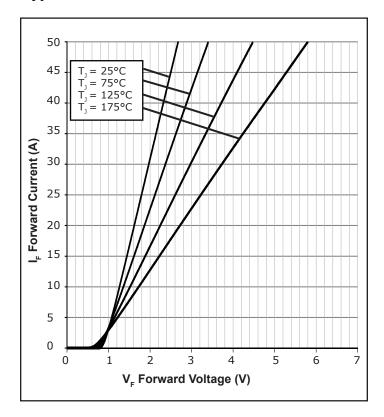
Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V <sub>F</sub>	Forward Voltage	1.8 3.2	2 4	V	$I_F = 25 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 25 \text{ A } T_J = 175^{\circ}\text{C}$	
$I_R$	Reverse Current	20 100	100 400	μΑ	V <sub>R</sub> = 1700 V T <sub>J</sub> =25°C V <sub>R</sub> = 1700 V T <sub>J</sub> =175°C	
Q <sub>c</sub>	Total Capacitive Charge	170	270	nC	$V_R = 1700 \text{ V, } I_F = 25 \text{ A}$ $di/dt = 400 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	
С	Total Capacitance	2250 200 140		pF	V <sub>R</sub> = 0 V, T <sub>J</sub> = 25°C, f = 1 MHz V <sub>R</sub> = 200 V, T <sub>J</sub> = 25°C, f = 1 MHz V <sub>R</sub> = 400 V, T <sub>J</sub> = 25°C, f = 1 MHz	

Note:

1. Assumes  $\theta_{J-C}$  Thermal Resistance of 0.63°C/W or less.



# **Typical Performance**



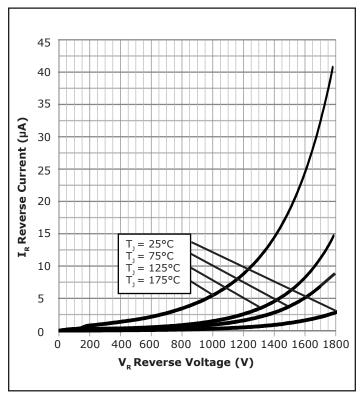


Figure 1. Forward Characteristics

Figure 2. Reverse Characteristics

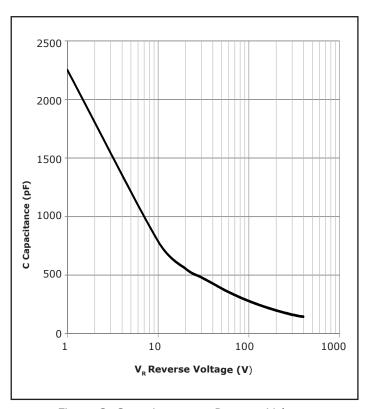


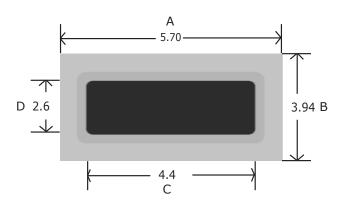
Figure 3. Capacitance vs. Reverse Voltage



#### **Mechanical Parameters**

Parameter	Тур.	Unit
Die Size	5.70 X 3.94	mm
Anode Pad Size	5.1 X 3.3	mm
Anode Pad Opening	4.4 X 2.6	mm
Thickness	387 ± 10%	μm
Anode Metalization (Al)	4	μm
Cathode Metalization (Ni/Ag)	1.8	μm

### **Chip Dimensions**



Symbol	Dimension mm inch		
А	5.70	0.224	
В	3.94	0.155	
С	4.4	0.173	
D	2.6	0.102	

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The die-on-tape method of delivering these SiC die may be considered a means of temporary storage only. Due to an increase in adhesion over time die stored for an extended period may affix too strongly to the tape. These die should be stored in a temperature-controlled nitrogen dry box soon after receipt. Cree will further recommend that all die be removed from tape to a waffle pack, to a similar storage medium, or used in production within 2 – 3 weeks of delivery to assure 100% release of all die without issues.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, air traffic control systems, or weapons systems.

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