

DUAL N-CANNEL ENHANCEMENT MODE MOSFET
Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D $T_A = 25^\circ\text{C}$
24V	15m Ω @ $V_{GS} = 4.5\text{V}$	6.5A
	20m Ω @ $V_{GS} = 2.5\text{V}$	5.6A

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

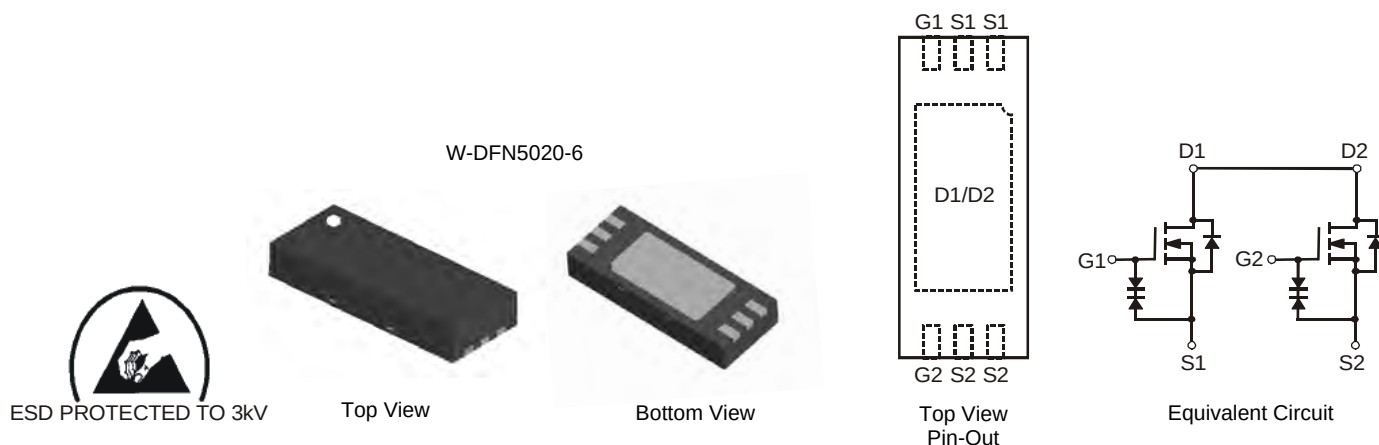
- DC-DC Converters
- Power management functions

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected up to 3kV**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: W-DFN5020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.03 grams (approximate)


Ordering Information (Note 4)

Part Number	Case	Packaging
DMG5802LFX-7	W-DFN5020-6	3000 / Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information


ME = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: X = 2010)
 M = Month (ex: 9 = September)

Date Code Key

Date Code Key

Year	2010	2011	2012	2013	2014	2015	2016	2017	2018
Code	X	Y	Z	A	B	C	D	E	F

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	24	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	T _A = +25°C	I _D	6.5	A
		T _A = +70°C		5.2	
Continuous Drain Current (Note 5) V _{GS} = 2.5V	Steady State	T _A = +25°C	I _D	5.6	A
		T _A = +70°C		4.5	
Pulsed Drain Current (Note 6)			I _{DM}	70	A

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	P _D	0.98	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	126.5	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	24	-	-	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	1.0	μA	V _{DS} = 24V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±10	μA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.6	0.9	1.5	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	-	11	15	mΩ	V _{GS} = 4.5V, I _D = 6.5A
		-	12	17		V _{GS} = 4V, I _D = 5.6A
		-	13	18		V _{GS} = 3.1V, I _D = 5.6A
		-	14	20		V _{GS} = 2.5V, I _D = 5.6A
		-	17	-		V _{DS} = 5V, I _D = 6.5A
Forward Transfer Admittance	Y _{fs}	-	17	-	S	V _{DS} = 5V, I _D = 6.5A
Diode Forward Voltage	V _{SD}	-	0.6	0.9	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	-	1066.4	-	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	132.0	-		
Reverse Transfer Capacitance	C _{rss}	-	127.1	-		
Gate Resistance	R _g	-	1.47	-	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge V _{GS} = 4.5V	Q _g	-	14.5	-	nC	V _{GS} = 4.5V, V _{DS} = 15V, I _D = 5.8A
Total Gate Charge V _{GS} = 10V	Q _g	-	31.3	-		
Gate-Source Charge	Q _{gs}	-	2.0	-		
Gate-Drain Charge	Q _{gd}	-	3.1	-		
Turn-On Delay Time	t _{D(on)}	-	3.69	-	ns	V _{GS} = 10V, V _{DS} = 15V, R _L = 2.1Ω, R _G = 3Ω
Turn-On Rise Time	t _r	-	13.43	-	ns	
Turn-Off Delay Time	t _{D(off)}	-	32.18	-	ns	
Turn-Off Fall Time	t _f	-	22.45	-	ns	

- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 - Repetitive rating, pulse width limited by junction temperature.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

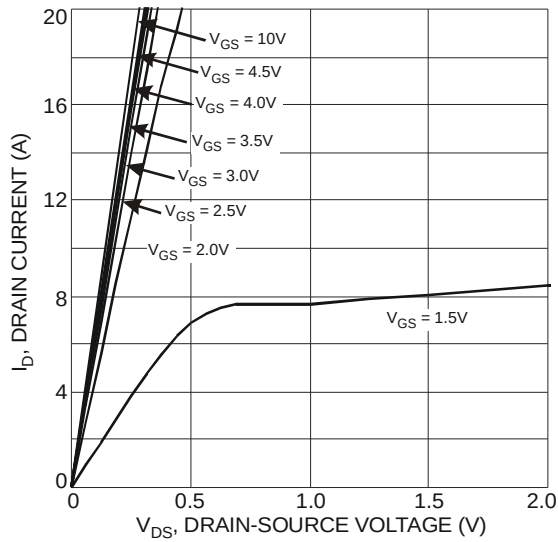


Fig. 1 Typical Output Characteristic

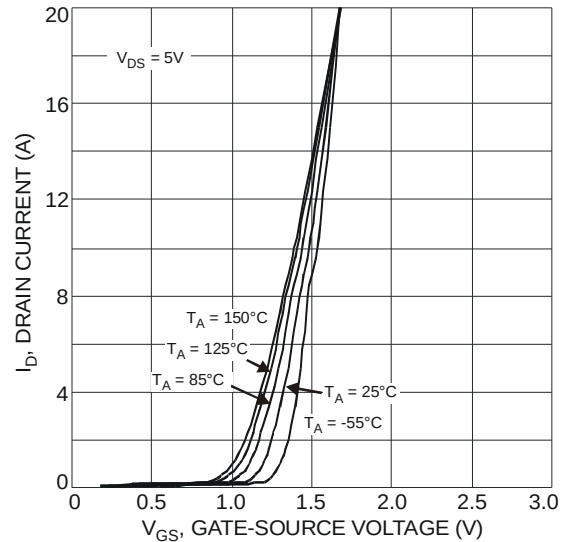


Fig. 2 Typical Transfer Characteristic

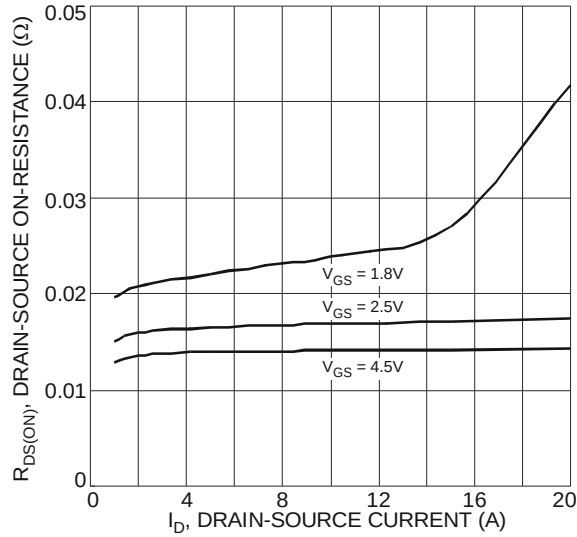


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

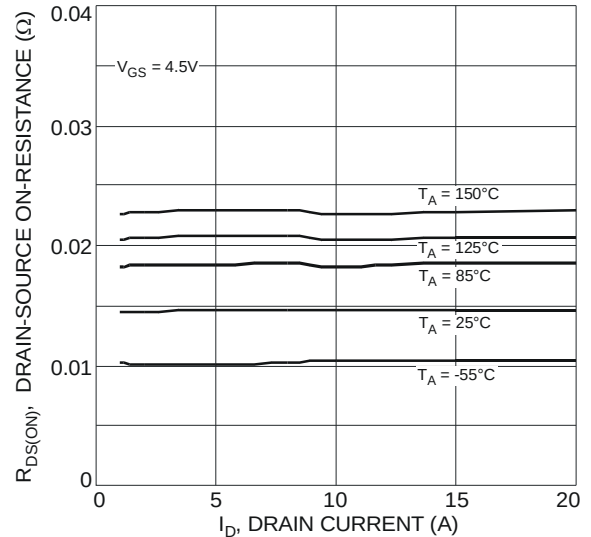


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

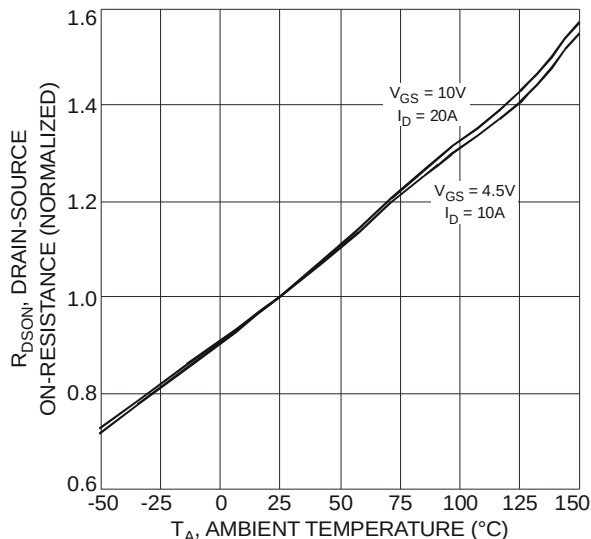


Fig. 5 On-Resistance Variation with Temperature

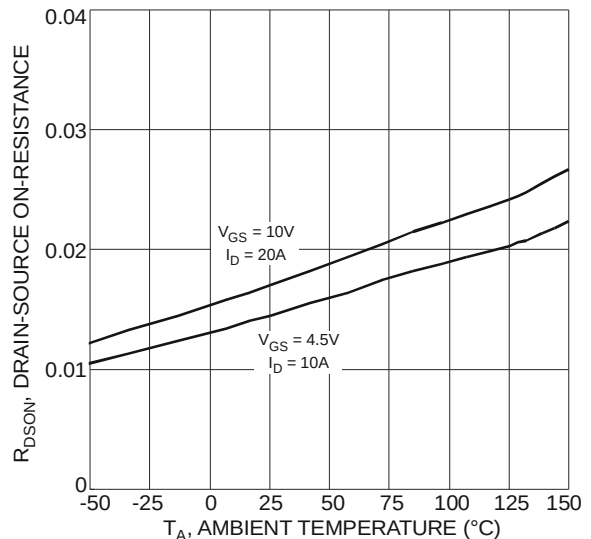
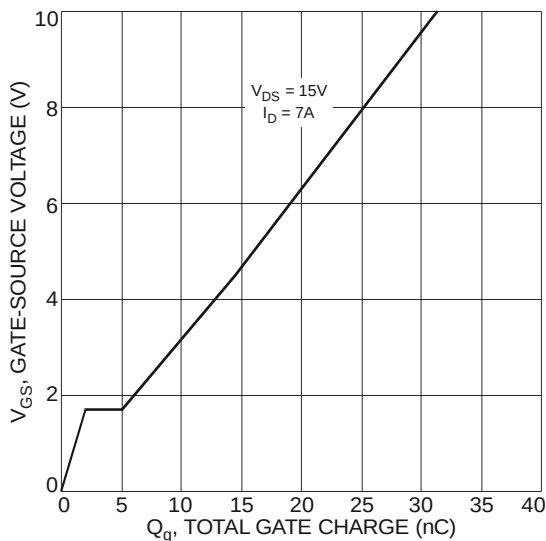
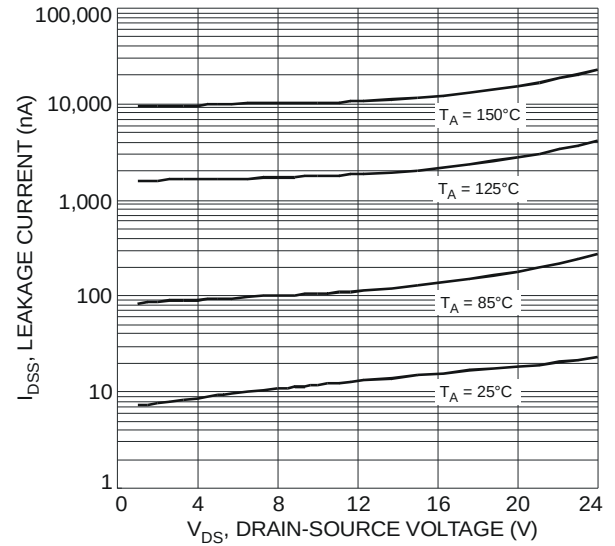
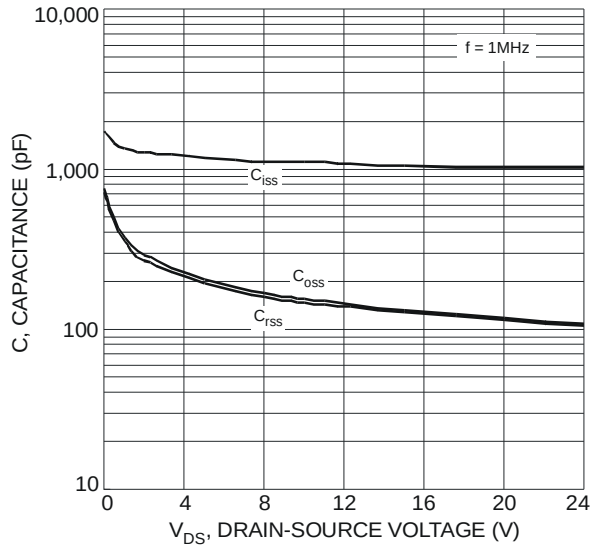
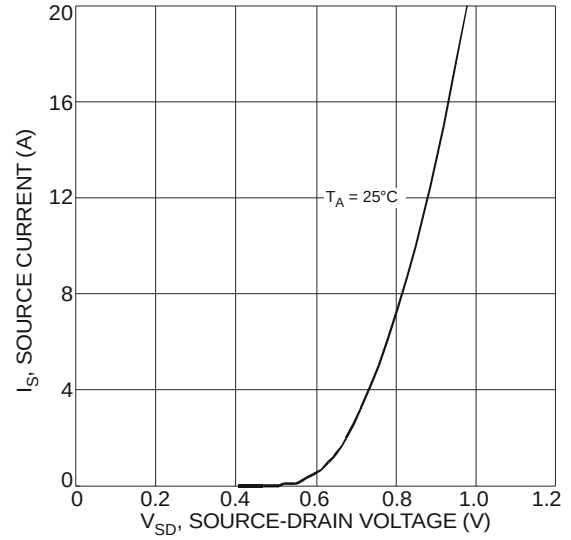
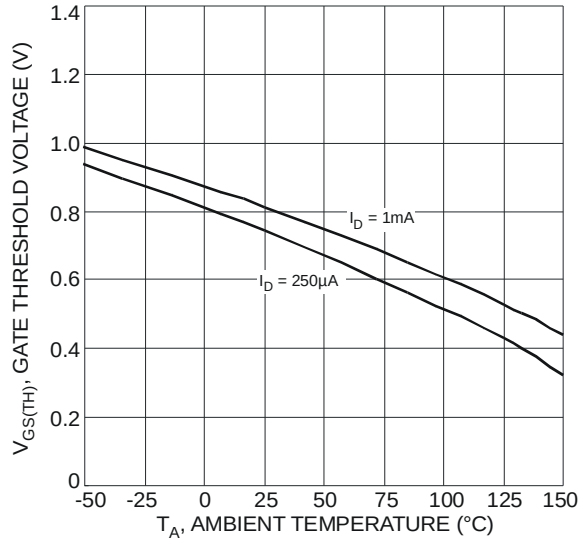


Fig. 6 On-Resistance Variation with Temperature



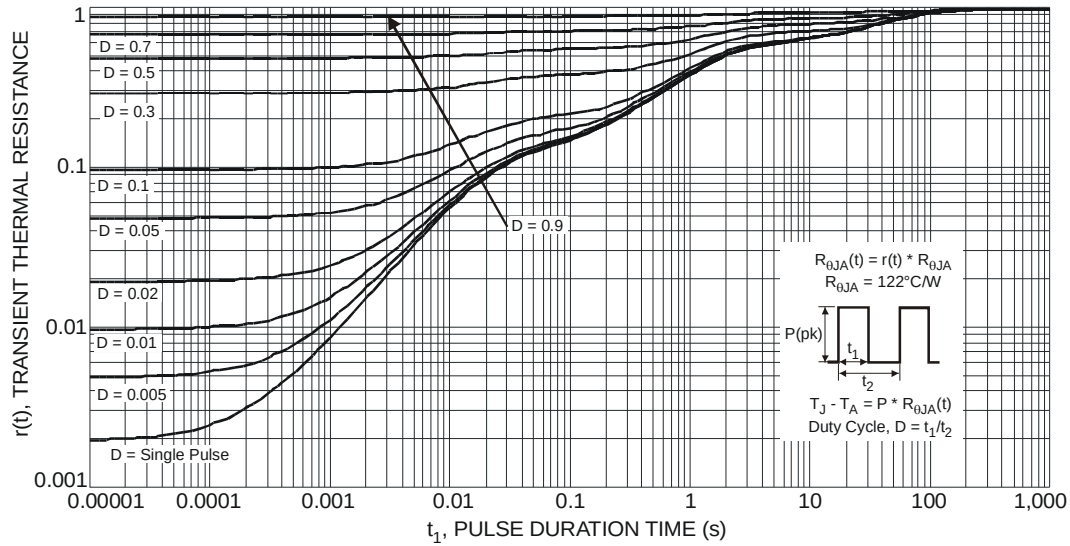
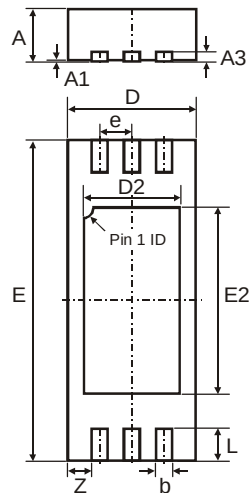


Fig. 12 Transient Thermal Response

Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.

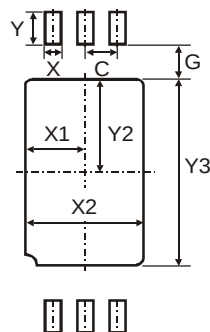


W-DFN5020-6			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0	0.05	0.02
A3	—	—	0.15
b	0.20	0.30	0.25
D	1.90	2.10	2.00
D2	1.40	1.60	1.50
e	—	—	0.50
E	4.90	5.10	5.00
E2	2.80	3.00	2.90
L	0.35	0.65	0.50
Z	—	—	0.375

All Dimensions in mm

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.50
G	0.35
X	0.35
X1	0.90
X2	1.80
Y	0.70
Y2	1.60
Y3	3.20

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