

P1TX6B-SX51-02

Product Specification Sheet

ORIGINATOR:	C. ENG	DATE:	9/19/2012
	P1TX6B-SX51-02 Module Product Specification	DOCUMENT NO. DOC002260	REV B
		SHEET 1 OF 8	

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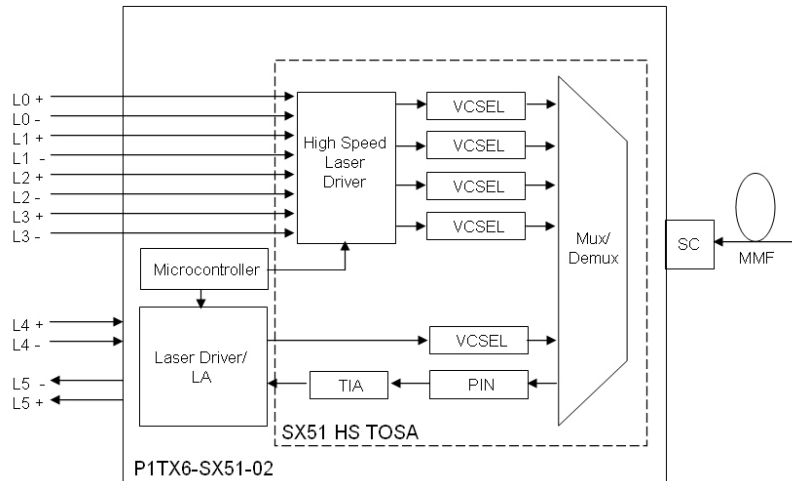
1.0 Description

The P1TX6B-SX51x-02 (TX Data Module) is an optical communication module that transmits over 14Gbps of data. The TX Data Module transmits up to four 3.4 Gbps data channels PLUS two side-band channels (one each direction), all on a single multimode fiber. With integrated drivers and amplifiers, the TX Data Module eliminates the need for in-house optical design expertise.



2.0 Features

- 5 transmit lanes and 1 return lane over a single multimode fiber
- Low power consumption (1W)
- Up to 1000M
- Mechanical enclosure serves as heat sink while allowing for FCC part 15 Class A compliance
- No manipulating or compressing the data
- Small footprint
- High-speed CML inputs

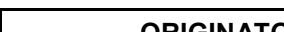


This device is **EXTREMELY SENSITIVE** to Electrostatic Discharge (ESD). At a minimum, all handling must be performed in accordance with an ANSI-compliant ESD Control Program (ANSI/ESD S20.20-2007) to mitigate possible ESD-induced damage. Reliability and life of the device will be adversely affected if these precautions are not met.



This device is a Class 3R Laser device and can cause damage to eye sight if used improperly. Refer to ANSI Z136 for proper handling and usage of Class 3R devices.



ORIGINATOR:		C. ENG	DATE:	9/19/2012	
	P1TX6B-SX51-02 Module Product Specification			DOCUMENT NO. DOC002260	REV B
				SHEET 2 OF 8	

CONFIDENTIAL INFORMATION

3.0 Absolute Maximum Ratings

Parameter	Symbol	Min	Typ	Max	Units
Storage Temperature ¹	Tst	-40		85	°C
Supply Voltage ^{2 3}	Vcc	-0.3		3.6	V
Operating Surface Temperature ⁴	Ta	0		65	°C
Operating Humidity ⁵	RH			80	%
Durability – SC Connector			200		cycles
Durability – Plug-down Connector			50		cycles
Input Pin Voltage ⁶	V _{IN}	-0.5		Vcc + 0.5	V

4.0 Optical Characteristics – High-speed Lanes

Parameter (per lane)	Symbol	Min	Typ	Max	Units
Average Optical Power, per Lane ⁷	P _{out}		-1.5		dBm
Optical Modulation Amplitude		-6.25	0.0		dBm
Center Wavelength – Lane 0			778		nm
Center Wavelength – Lane 1			800		nm
Center Wavelength – Lane 2			825		nm
Center Wavelength – Lane 3			850		nm
Optical Rise/Fall Time ⁸					
P1TX6B-SX51V			200		
P1TX6B-SX51D			100		Ps

¹ Stresses listed may be applied without causing damage. Functionality at or above the values listed is not implied. Exposure to these values for extended periods may affect reliability.

² Supply voltage must be present before input signal may be applied

³ Module must be powered down (OFF) before installation/removal.

⁴ See outline drawing for measurement point.

⁵ Non condensing. Do not operate device if wet.

⁶ Supply voltage must be present before input signal may be applied. Driving the device in a power OFF state may result in permanent damage to the input pins.

⁷ I = 6mA, T=25°C.

⁸ Rise and fall times measured from 20 - 80% . Tested with OMRON reference circuits.

ORIGINATOR:		C. ENG	DATE:	9/19/2012
	P1TX6B-SX51-02 Module Product Specification		DOCUMENT NO. DOC002260	REV B
			SHEET 3 OF 8	

5.0 Electrical Specifications – High-speed Lanes

Parameter	Symbol	Min	Typ	Max	Units
Data Rate per Lane ⁹ P1TX6B-SX51V P1TX6B-SX51D				1.65 3.40	Gb/s
Total Jitter (RMS), per lane ¹⁰	T _{J1}		10		ps
Input Differential Impedance			100		ohm
Differential Input Voltage ^{2,11}		320		2000	mVp-p
Single-ended Input Voltage ²		160		1000	mVp-p
Common mode input voltage ² (AC-coupled input)		0.85	2.6	VCC	V V
Operating Supply Voltage	Vcc-Vee	3.15	3.30	3.45	V
Operating Supply Current	Icc			300	mA
Input Data Pattern		DC-balanced			

6.0 Optical Characteristics – Bi-Directional Lanes

Transmit Parameter	Symbol	Min	Typ	Max	Units
Average Optical Power - Lane 4	Pavg		-1.5		dBm
Optical Modulation Amplitude		-6.25	0.0		dBm
Wavelength - Lane 4			911		nm
Optical Rise/Fall Time			2000		Ps

Receive Parameter	Symbol	Min	Typ	Max	Units
Wavelength - Lane 5			980		nm
Data Rate -02A -02B -02C				155 622 1250	Mb/s
Peak Optical Input Power	Pin			3.0	dBm
Peak Optical Modulation Amplitude	Pin			2.5	dBm
OMA Sensitivity ¹²		-13.25	-15.00		dBm
Input Data Pattern		DC-balanced			

⁹ Measured with input signals conforming to HDMI rev 1.4a

¹⁰ Based on a jitter-free source

¹¹ Differential CML compatible inputs

¹² Optical Modulation Amplitude. Based on an unstressed input signal.

ORIGINATOR:		C. ENG	DATE:	9/19/2012
	P1TX6B-SX51-02 Module Product Specification		DOCUMENT NO. DOC002260	REV B
			SHEET 4 OF 8	

7.0 Electrical Specifications – Bi-Directional Lanes

Transmit Parameter	Symbol	Min	Typ	Max	Units
Data Rate per Lane -02A -02B -02C				155 622 1250	Mb/s
Total Jitter (RMS) ¹³	T _{J1}		25		ps
Input Differential Impedance			100		ohm
Differential Input Voltage – Lane 4 ^{2,14}		320		2000	mVp-p
Input Data Pattern		DC-balanced			

Receive Parameter	Symbol	Min	Typ	Max	Units
Low Frequency Cutoff	F _{CUTOFF}		35		kHz
Differential Output Voltage ¹⁵	V _{OD}		835		mVp-p
Loss of Signal Assert Sensitivity	LOS _{SEN-ON}		-15.5		dBm
Loss of Signal De-Assert Sensitivity	LOS _{SEN-OFF}		-13.5		dBm
Loss of Signal Output Low ¹⁶	V _{LOS}			0.7	V
Loss of Signal Output High	V _{LOS}	2.0			V

8.0 Laser Safety

The P1TX6-SX51-02 meets Class-3R requirements.¹⁷ Use proper eye protection and handling practices per ANSI Z136.

¹³ Based on a jitter-free source

¹⁴ Differential CML compatible inputs

¹⁵ Differential back-terminated CML outputs

¹⁶ This output is asserted low when a loss of signal is detected on all lanes

¹⁷ Lane 4 data input with 50% duty cycle

ORIGINATOR:		C. ENG	DATE:	9/19/2012
	P1TX6B-SX51-02 Module Product Specification		DOCUMENT NO. DOC002260	REV B
			SHEET 5 OF 8	

9.0 Environmental Standards

Omron Network Products designs and manufactures its products to minimize the negative impact on our environment. As such, the P1TX6B-SX51-02 conforms to a variety of environmental and safety standards

Standard	Compliant	Certificate Available
RoHS	Yes	Yes

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10.0 Pin Numbers and Descriptions¹⁸

The TX Data Module contains a 30 pin connector (DF12-30DS-0.5V(86)). For information on the specifications of the mating connector (DF12(4.0)-30DP-0.5V(86)), contact Hirose.

Pin #	Signal	Name	Description
1	GND	Ground	
2	NC	No connect	Reserved for future use
3	+ IN0	Ch 0 + Data Input	Positive differential input for 778nm lane
4	LOS _{BI}	Ch 5 LOS	Loss of Signal – Bi-directional Channel
5	- IN0	Ch 0 - Data Input	Negative differential input for 778nm lane
6	Reset	Reset	Microcontroller Reset ¹⁹
7	+ IN1	Ch 1 + Data Input	Positive differential input for 800nm lane
8	UART	UART_TX	Reserved for future use
9	- IN1	Ch 1 - Data Input	Negative differential input for 800nm lane
10	UART	UART_RX	Reserved for future use
11	+ IN2	Ch 2 + Data Input	Positive differential input for 825nm lane
12	NC	No connect	Reserved for future use
13	- IN2	Ch 2 - Data Input	Negative differential input for 825nm lane
14	NC	No connect	Reserved for future use
15	+ IN3	Ch 3 + Data Input	Positive differential input for 850nm lane
16	EN _{BI}	Enable	Enable ²⁰ – Bi-directional laser
17	- IN3	Ch 3 - Data Input	Negative differential input for 850nm lane
18	EN _{HS}	Enable	Enable ²⁰ – High speed lasers
19	GND	Ground	
20	NC	No connect	Reserved for future use
21	- IN4	Ch 4 - Data Input	Negative differential input for 911nm lane
22	NC	No connect	Reserved for future use
23	+ IN4	Ch 4 - Data Input	Positive differential input for 911nm lane
24	NC	No connect	Reserved for future use
25	+ TD5	Ch 5 - Data Output	Positive differential output for 980nm lane
26	NC	No connect	Reserved for future use
27	- TD5	Ch 5 - Data Output	Negative differential output for 980nm lane
28	VCC ²	Voltage Input	+3.3 volt input
29	GND	Ground	
30	VCC ²	Voltage Input	+3.3 volt input

¹⁸ Verify pin assignments and polarity before powering on device

¹⁹ Reset to be pulled high for normal operation

²⁰ Enable to be pulled up to VCC for normal operation

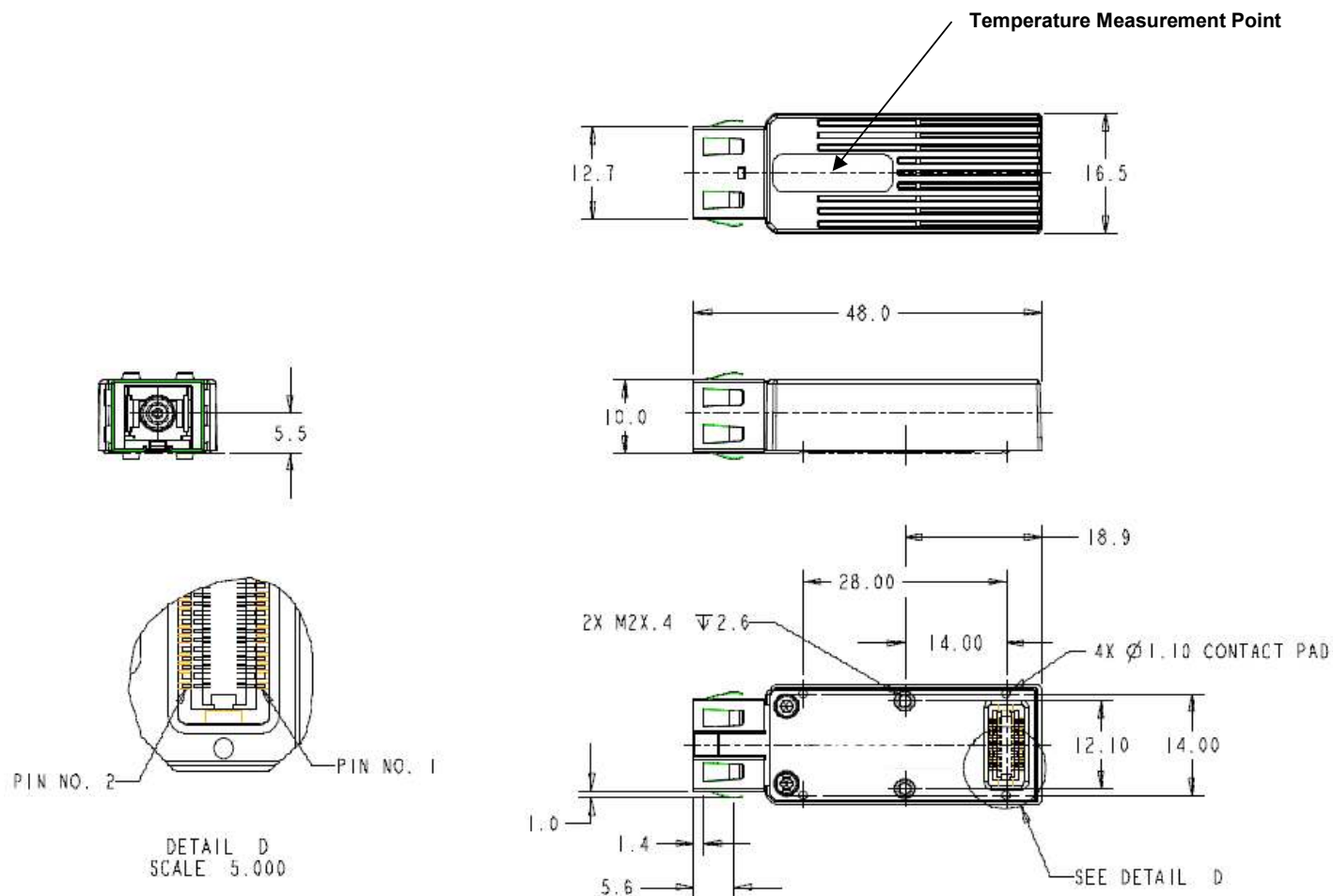
ORIGINATOR:		C. ENG	DATE:	9/19/2012
	P1TX6B-SX51-02 Module Product Specification		DOCUMENT NO. DOC002260	REV B
			SHEET 7 OF 8	

CONFIDENTIAL INFORMATION

11.0 Dimensions

The SX51-02 data module is designed to work with a standard SC ferrule only. Insertion of any other type may result in damage.

Dimensions and orientation are for reference only. Customers can request final, detailed dimensions, or a CAD drawing, through your Omron sales representative.



Dimensions are in mm

ORIGINATOR:		C. ENG		DATE:		9/19/2012	
		P1TX6B-SX51-02 Module Product Specification			DOCUMENT NO. DOC002260		REV B
					SHEET 8 OF 8		

CONFIDENTIAL INFORMATION