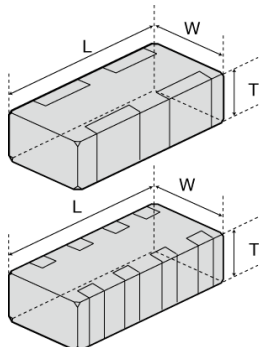


Array Type Multilayer Ceramic Capacitors

E4K212B7104KD-T



■ Features

- Item Summary
16V, X7R, 0.1 μ F, $\pm 10\%$, 0805
- Lifecycle Stage
Non-preferred
- Standard packaging quantity (minimum)
Taping paper 4000pcs

■ Products characteristics table

CaseSize (EIA/JIS)	0805/2012
Rated Voltage (max)	16V
Capacitance	0.1 μ F
Capacitance Tolerance	$\pm 10\%$
Resistance	500 M Ω μ F
Dielectric Loss Tangent	5%MAX
Temperature Characteristics (EIA/JIS)	X7R/-
Temperature Range (EIA/JIS)	-55 to +125°C/-
Capacitance Change (EIA/JIS)	$\pm 15\%$ -%
RoHS Compliance	Yes
Halogen Free	Yes
Soldering Method	Reflow

■ External Dimensions

L	2.0mm ± 0.10
W	1.25mm ± 0.10
T	0.85mm ± 0.10
e	0.25mm ± 0.10

2013.11.29

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
 TAIYO YUDEN reserves the right to make change to the Date at any time without notice.
 Before making final selection, please check product specification.