

FCA36N60NF

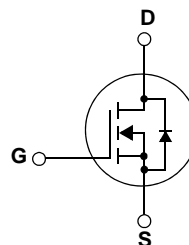
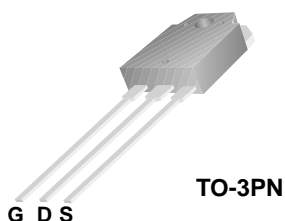
N-Channel SupreMOS®, FRFET®, MOSFET 600V, 36A, 95mΩ

Features

- $R_{DS(on)} = 80m\Omega$ (Typ.) @ $V_{GS} = 10V$, $I_D = 18A$
- Ultra Low Gate Charge (Typ. $Q_g = 86nC$)
- Low Effective Output Capacitance
- 100% Avalanche Tested
- RoHS Compliant

Description

The SupreMOS® MOSFET, Fairchild's next generation of high voltage super-junction MOSFETs, employs a deep trench filling process that differentiates it from preceding multi-epi based technologies. By utilizing this advanced technology and precise process control, SupreMOS® provides world class R_{sp} , superior switching performance and ruggedness. This SupreMOS® MOSFET fits the industry's AC-DC SMPS requirements for PFC, server/telecom power, FPD TV power, ATX power, and industrial power applications.



MOSFET Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted*

Symbol	Parameter	FCA36N60NF	Units
V_{DSS}	Drain to Source Voltage	600	V
V_{GSS}	Gate to Source Voltage	± 30	V
I_D	Drain Current	Continuous ($T_C = 25^\circ C$)	A
		Continuous ($T_C = 100^\circ C$)	
I_{DM}	Drain Current	Pulsed (Note 1)	A
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	mJ
I_{AR}	Avalanche Current	12	A
E_{AR}	Repetitive Avalanche Energy	3.12	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	V/ns
	MOSFET dv/dt Ruggedness	100	
P_D	Power Dissipation	($T_C = 25^\circ C$)	W
		Derate above $25^\circ C$	$W/^\circ C$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ C$
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	$^\circ C$

*Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FCA36N60NF	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.40	$^\circ C/W$
$R_{\theta CS}$	Thermal Resistance, Case to Heat Sink (Typical)	0.24	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	

Package Marking and Ordering Information $T_C = 25^\circ\text{C}$ unless otherwise noted

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCA36N60NF	FCA36N60NF	TO-3PN	-	-	30

Electrical Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
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Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 1\text{mA}$, $V_{GS} = 0\text{V}$, $T_J = 25^\circ\text{C}$	600	-	-	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 1\text{mA}$, Referenced to 25°C	-	0.60	-	V/ $^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 480\text{V}$, $V_{GS} = 0\text{V}$ $T_J = 125^\circ\text{C}$	-	-	10 100	μA
I_{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250\mu\text{A}$	3.0	3.7	5.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{V}$, $I_D = 18\text{A}$	-	80	95	m Ω
g_{FS}	Forward Transconductance	$V_{DS} = 20\text{V}$, $I_D = 18\text{A}$	-	39	-	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 100\text{V}$, $V_{GS} = 0\text{V}$ $f = 1\text{MHz}$	-	3191	4245	pF
C_{oss}	Output Capacitance		-	145	195	pF
C_{rss}	Reverse Transfer Capacitance		-	5	8	pF
C_{oss}	Output Capacitance	$V_{DS} = 380\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	-	81	-	pF
$C_{oss\text{eff}}$	Effective Output Capacitance	$V_{DS} = 0\text{V}$ to 480V , $V_{GS} = 0\text{V}$	-	338	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10V	$V_{DS} = 380\text{V}$, $I_D = 18\text{A}$, $V_{GS} = 10\text{V}$ (Note 4)	-	86	112	nC
Q_{gs}	Gate to Source Gate Charge		-	16	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	36	-	nC
ESR	Equivalent Series Resistance (G-S)	Drain Open, $f = 1\text{MHz}$	-	1.2	-	Ω

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 380\text{V}$, $I_D = 18\text{A}$ $R_G = 4.7\Omega$ (Note 4)	-	27	64	ns
t_r	Turn-On Rise Time		-	17	44	ns
$t_{d(off)}$	Turn-Off Delay Time		-	92	194	ns
t_f	Turn-Off Fall Time		-	4	18	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	36	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	108	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 18A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 18A dI _F /dt = 100A/μs	-	166	-	ns
Q _{rr}	Reverse Recovery Charge		-	1.3	-	μC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{AS} = 12\text{A}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 36\text{A}$, $di/dt \leq 1200\text{A}/\mu\text{s}$, $V_{DD} \leq 380\text{V}$, Starting $T_J = 25^\circ\text{C}$
4. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

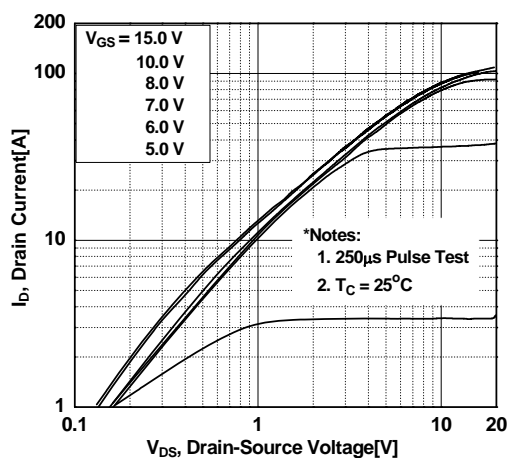


Figure 2. Transfer Characteristics

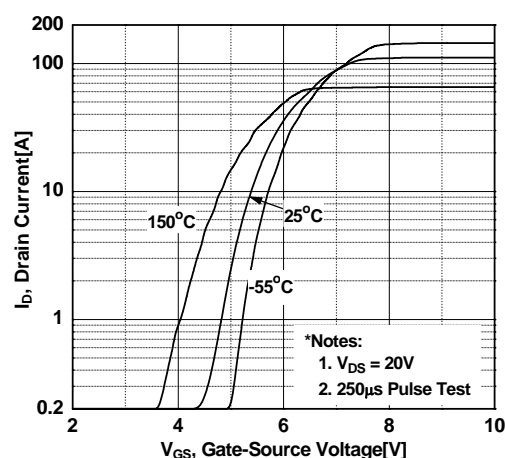


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

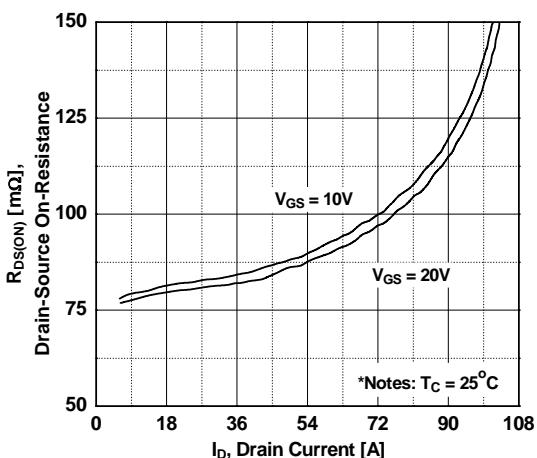


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

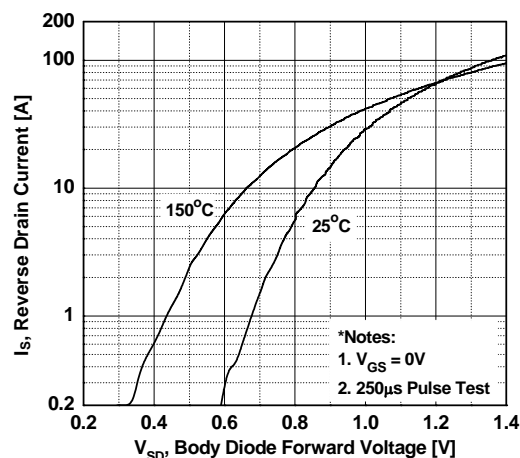


Figure 5. Capacitance Characteristics

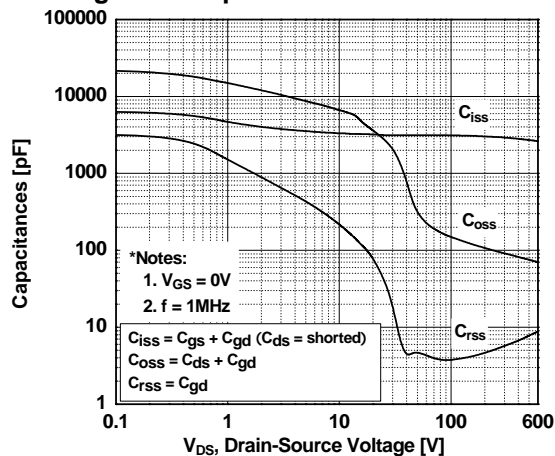
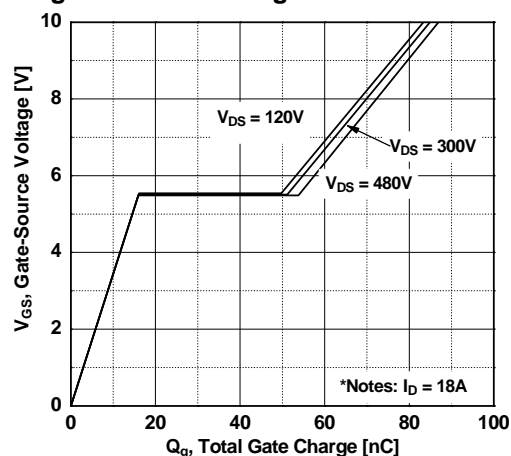


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

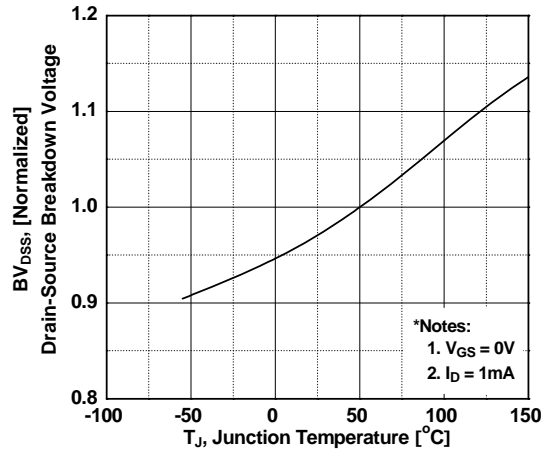


Figure 8. On-Resistance Variation vs. Temperature

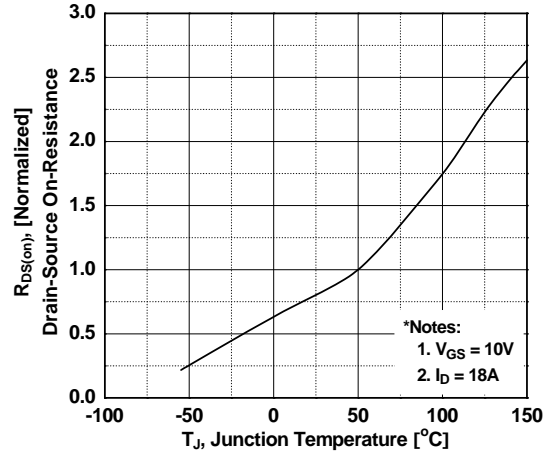


Figure 9. Maximum Safe Operating Area

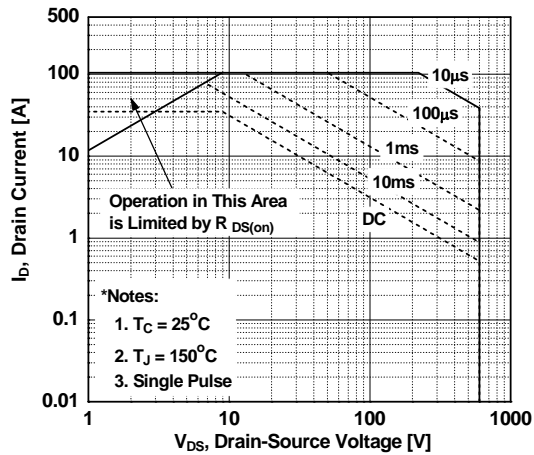


Figure 10. Maximum Drain Current vs. Case Temperature

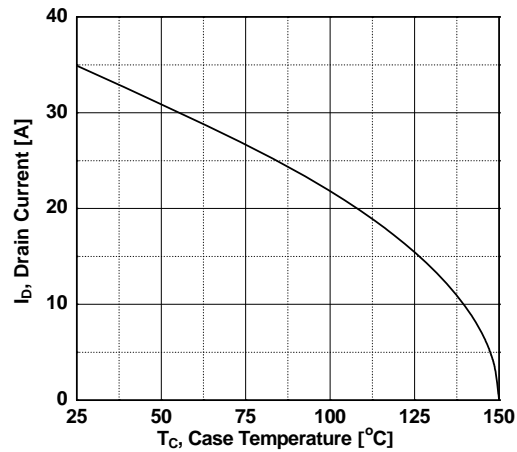
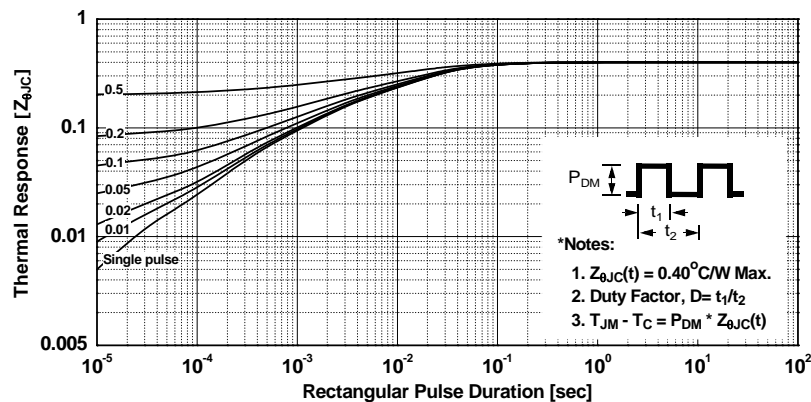
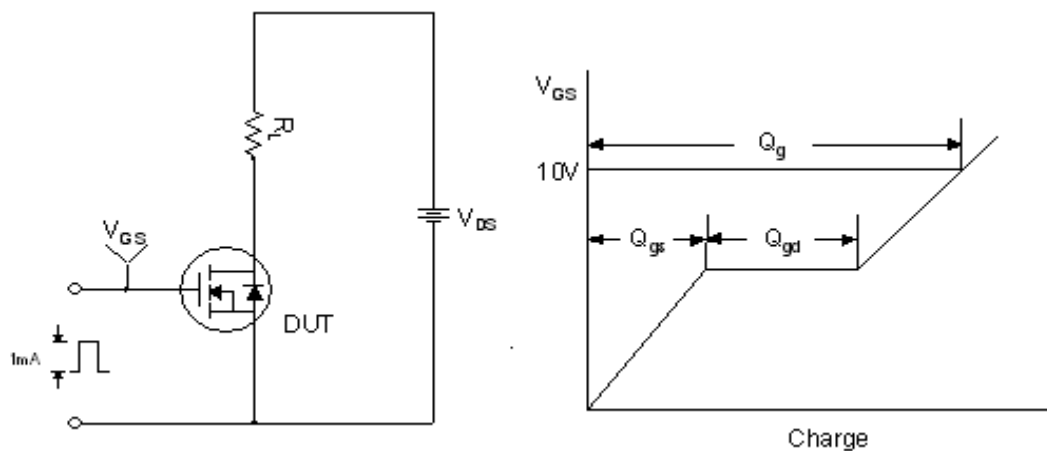


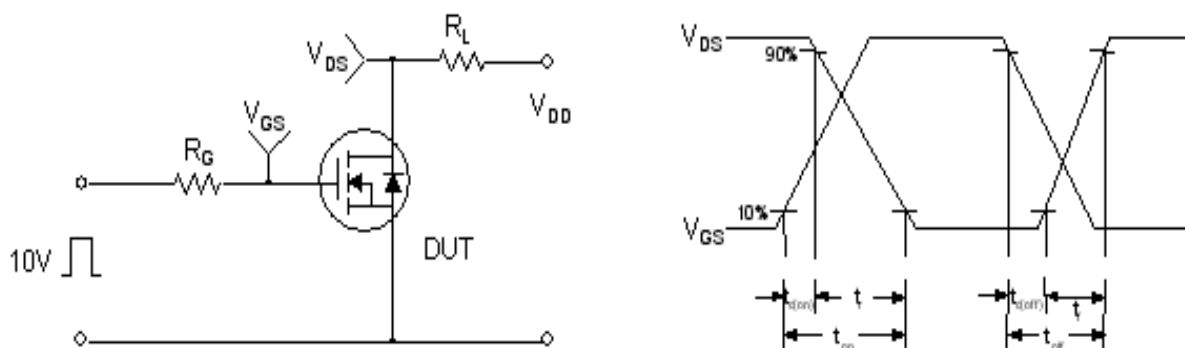
Figure 11. Transient Thermal Response Curve



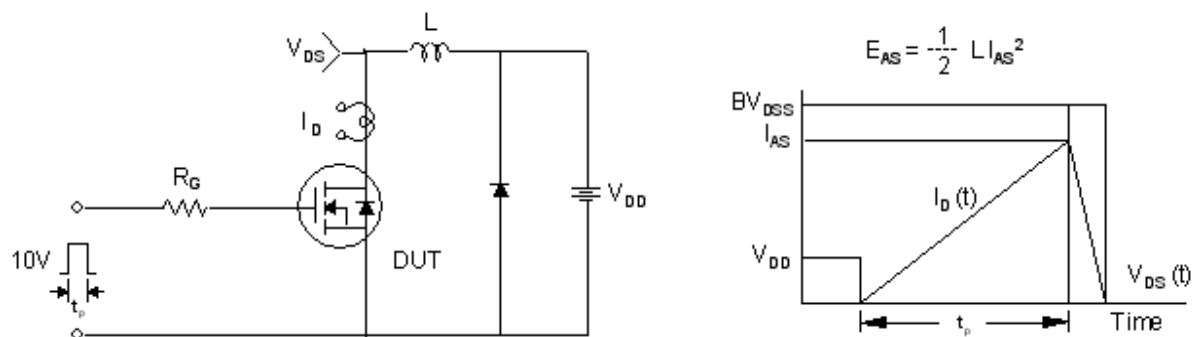
Gate Charge Test Circuit & Waveform



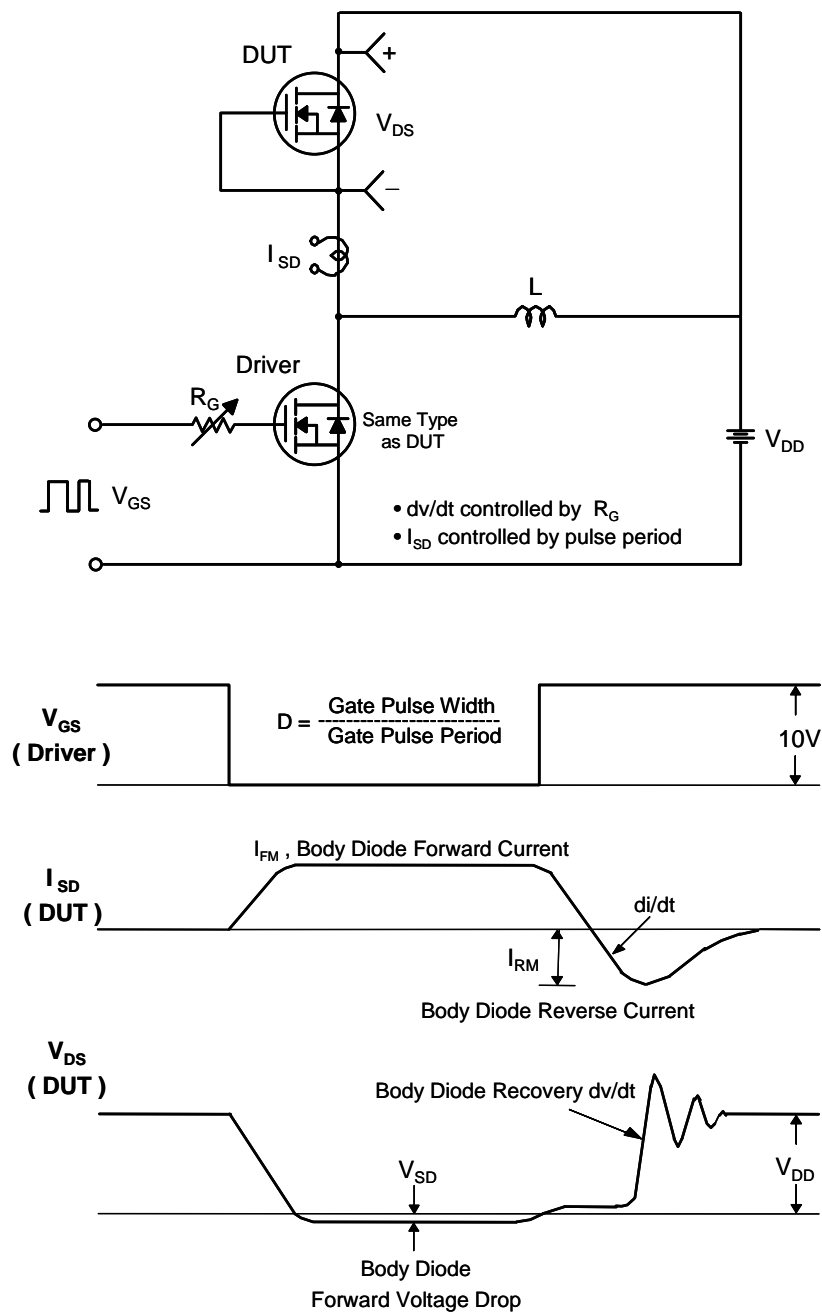
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms



Technical drawing of a 3-pin connector with dimensions in mm. The drawing includes three views: a front view on the left, a side view in the middle, and a rear view on the right. Dimensions are provided for various parts of the connector, including pin diameters, pin lengths, and body dimensions. A note at the bottom left specifies a hole diameter of 0.55 mm with a maximum tolerance of 0.05 mm (M).

Front View Dimensions:

- Top flange width: 15.80 / 15.40
- Top flange height: 5.20 / 4.80
- Top flange inner width: 18.90 / 18.50
- Top flange hole diameter: $\phi 0.55$ (M)
- Pin 1 diameter: 1.85
- Pin 2 diameter: 2.20 / 1.80
- Pin 3 diameter: 3.70 / 3.30
- Pin 1 length: 1.20 / 0.80
- Pin 2 length: 3.20 / 2.80
- Pin 3 length: 5.45

Side View Dimensions:

- Top flange thickness: 5.00 / 4.60
- Top flange hole diameter: $\phi 0.55$ (M)
- Top flange hole position: 1.65 / 1.45
- Top flange hole radius: (R0.50)
- Top flange hole angle: 3°
- Pin 1 angle: 3°
- Pin 2 angle: 4°
- Pin 3 angle: 3°
- Pin 1 length: 2.90 / 1.90
- Pin 2 length: 0.75 / 0.55
- Pin 3 length: 5.45

Rear View Dimensions:

- Top flange width: 13.80 / 13.40
- Top flange height: 16.96 / 16.56
- Top flange inner width: 7.20 / 6.80
- Top flange hole diameter: $\phi 0.55$ (M)
- Pin 1 diameter: 3.30 / 3.10
- Pin 2 diameter: 2.20 / 1.80
- Pin 3 diameter: 3.70 / 3.30
- Pin 1 length: 20.30 / 19.70
- Pin 2 length: 5.45
- Pin 3 length: 5.45

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