FAIRCHILD SEMICONDUCTOR®

June 2013

FCP11N60F / FCPF11N60F

N-Channel SuperFET® MOSFET

600 V, 7 A, 380 mΩ

Features

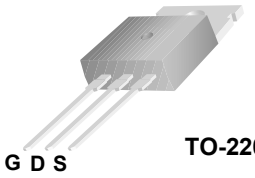
- 650V @T_J = 150°C
- Typ. $R_{DS(on)}$ = 320 mΩ
- Fast Recovery Type (t_{rr} = 120ns)
- Ultra Low Gate Charge (Typ. Q_g = 40 nC)
- Low Effective Output Capacitance (Typ. C_{oss}.eff = 95 pF)
- 100% Avalanche Tested
- RoHS compliant

Description

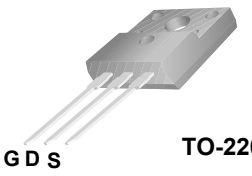
SuperFET® MOSFET is Fairchild Semiconductor®'s first generation of high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.

Application

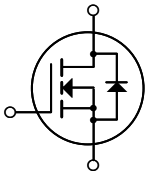
- LCD/LED/PDP TV
- Lighting
- Solar Inverter
- AC-DC Power Supply



TO-220



TO-220F



MOSFET Maximum Ratings T_C = 25°C unless otherwise noted*

Symbol	Parameter		FCP11N60F	FCPF11N60F	Unit
V_{DSS}	Drain to Source Voltage		600		V
I_D	Drain Current	-Continuous (T_C = 25°C)	11	11 *	A
		-Continuous (T_C = 100°C)	7	7*	
I_{DM}	Drain Current	- Pulsed (Note 1)	33	33 *	A
V_{GSS}	Gate to Source Voltage		±30		V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)		340		mJ
I_{AR}	Avalanche Current (Note 1)		11		A
E_{AR}	Repetitive Avalanche Energy (Note 1)		12.5		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5		V/ns
P_D	Power Dissipation	(T_C = 25°C)	125	36 *	W
		- Derate above 25°C	1.0	0.29 *	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150		°C
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300		°C

*Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FCP11N60F	FCPF11N60F	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	1.0	3.5	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink	0.5	-	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max	62.5	62.5	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCP11N60F	FCP11N60F	TO-220	-	-	50
FCPF11N60F	FCPF11N60F	TO-220F	-	-	50

Electrical Characteristics $T_C = 25^oC$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{DSS}	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 μA, $T_C = 25^oC$	600	-	-	V
		V_{GS} = 0 V, I_D = 250 μA, $T_C = 150^oC$	-	650	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25^oC	-	0.6	-	$V/^oC$
BV_{DS}	Drain-Source Avalanche Breakdown Voltage	V_{GS} = 0 V, I_D = 11 A	-	700	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 600 V, V_{GS} = 0 V	-	-	1	μA
		V_{DS} = 480 V, $T_C = 125^oC$	-	-	10	
I_{GSS}	Gate to Body Leakage Current	V_{GS} = ±30 V, V_{DS} = 0 V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 250 μA	3.0	-	5.0	V
$R_{DS(on)}$	Static Drain to Source On Resistance	V_{GS} = 10 V, I_D = 5.5 A	-	0.32	0.38	Ω
g_{FS}	Forward Transconductance	V_{DS} = 40 V, I_D = 5.5 A (Note 4)	-	6	-	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz	-	1148	1490	pF
C_{oss}	Output Capacitance		-	671	870	pF
C_{rss}	Reverse Transfer Capacitance		-	63	82	pF
C_{oss}	Output Capacitance	V_{DS} = 480 V, V_{GS} = 0 V, f = 1.0 MHz	-	35	-	pF
C_{oss}eff.	Effective Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	-	95	-	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 300 V, I_D = 11 A, R_G = 25 Ω (Note 4, 5)	-	34	80	ns
t_r	Turn-On Rise Time		-	98	205	ns
$t_{d(off)}$	Turn-Off Delay Time		-	119	250	ns
t_f	Turn-Off Fall Time		-	56	120	ns
$Q_{g(tot)}$	Total Gate Charge at 10V	V_{DS} = 480 V, I_D = 11 A, V_{GS} = 10 V (Note 4, 5)	-	40	52	nC
Q_{gs}	Gate to Source Gate Charge		-	7.2	-	nC
Q_{gd}	Gate to Drain “Miller” Charge		-	21	-	nC
Drain-Source Diode Characteristics						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	11	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	33	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0 V, I_{SD} = 11 A	-	-	1.4	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_{SD} = 11 A, dI_F/dt = 100 A/μs (Note 4)	-	120	-	ns
Q_{rr}	Reverse Recovery Charge		-	0.8	-	μC

Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. I_{AS} = 5.5 A, V_{DD} = 50 V, R_G = 25 Ω, Starting T_J = 25°C
3. $I_{SD} \leq$ 11 A, di/dt ≤ 200 A/μs, $V_{DD} \leq BV_{DSS}$, Starting T_J = 25°C
4. Pulse Test : Pulse width ≤ 300 μs, Duty cycle ≤ 2%
5. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. On-Region Characteristics

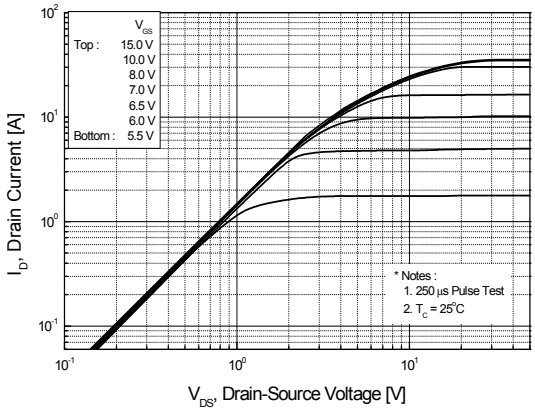


Figure 2. Transfer Characteristics

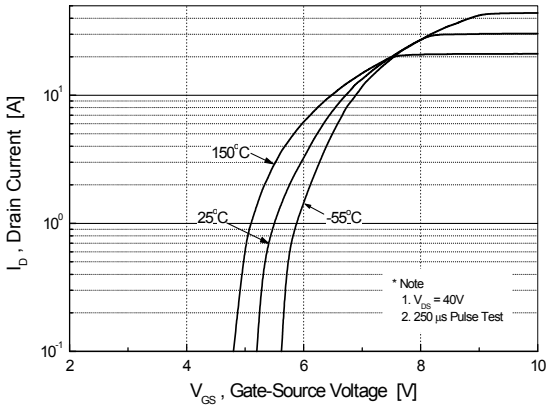


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

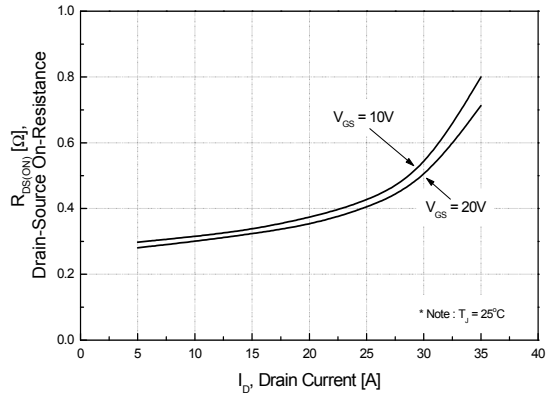


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

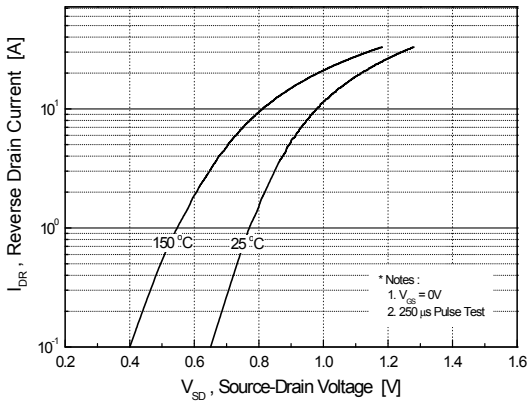


Figure 5. Capacitance Characteristics

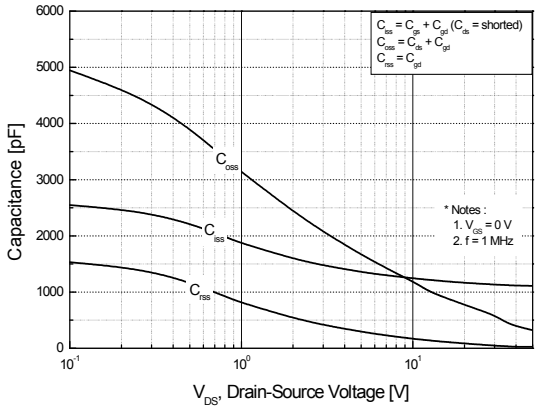
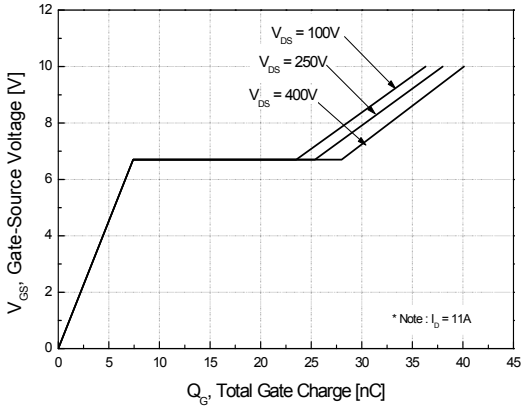


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

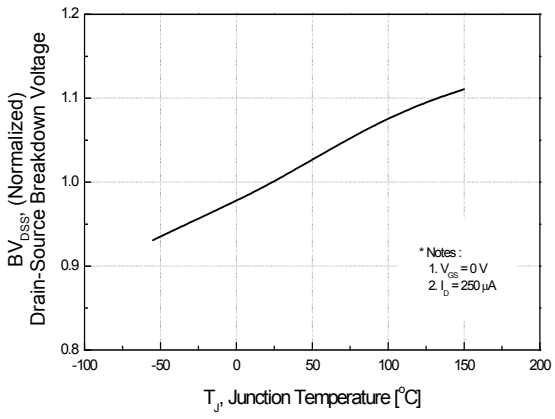


Figure 8. On-Resistance Variation vs. Temperature

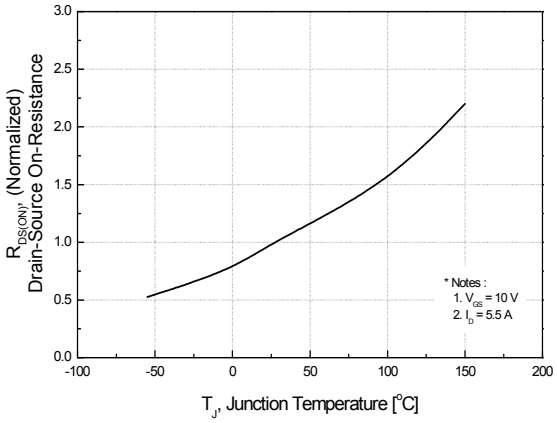


Figure 9-1. Safe Operating Area for FCP11N60F

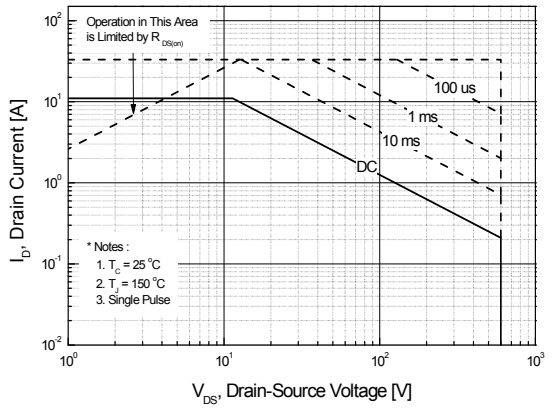


Figure 9-2. Safe Operating Area for FCPF11N60F

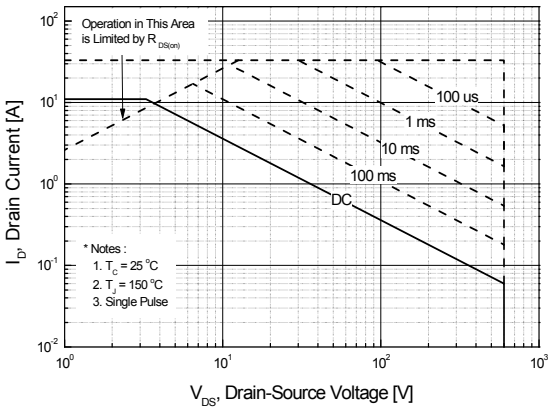
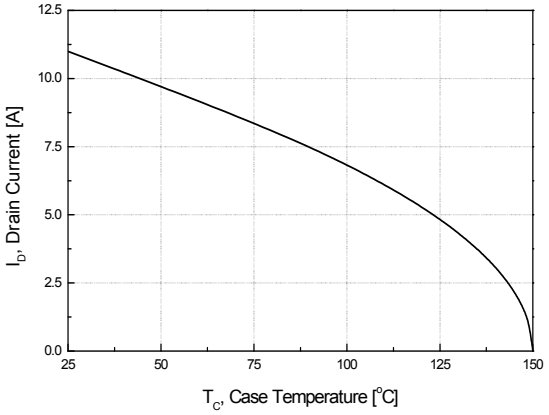


Figure 10. Maximum Drain Current vs. Case Temperature



Typical Performance Characteristics (Continued)

Figure 11-1. Transient Thermal Response Curve for FCP11N60F

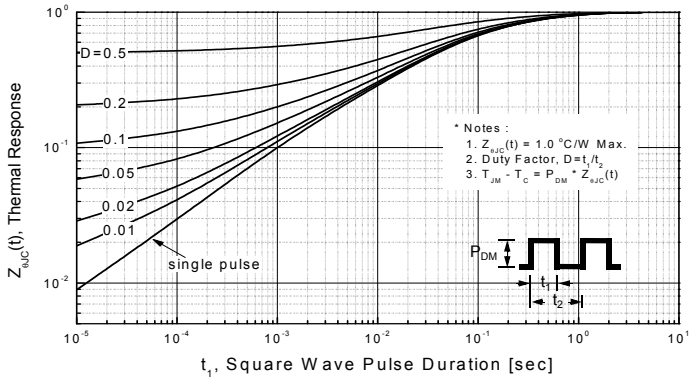
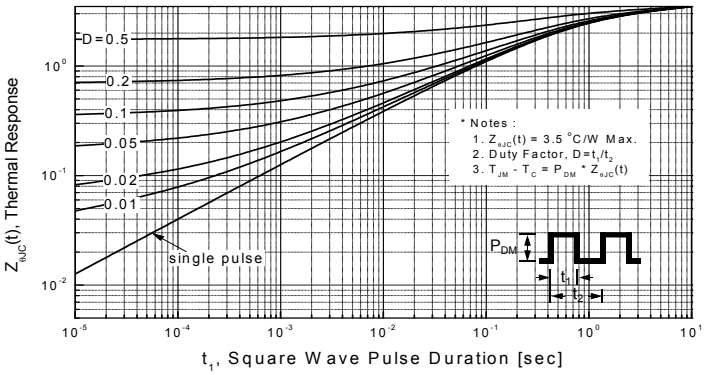
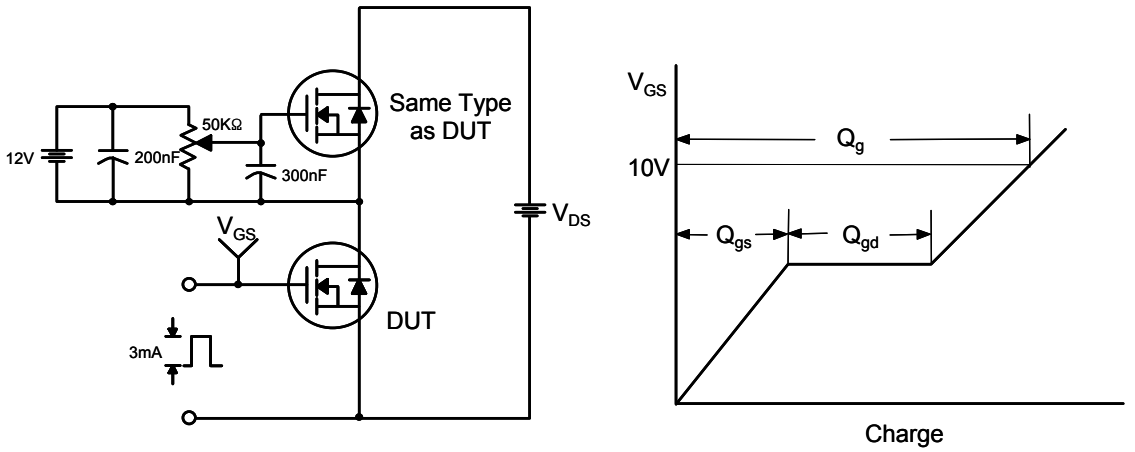


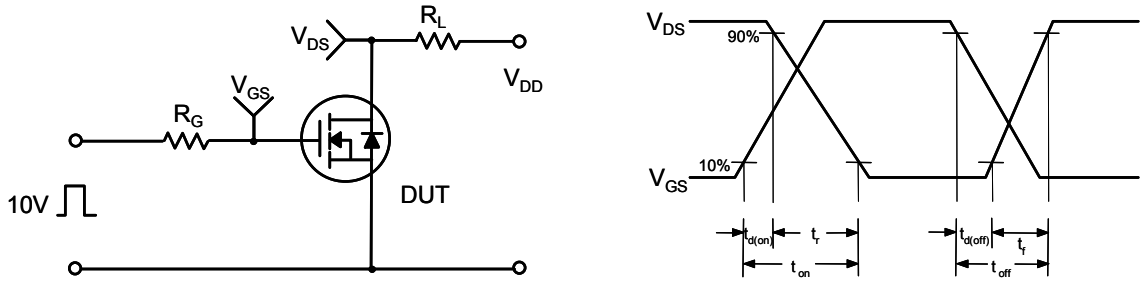
Figure 11-2. Transient Thermal Response Curve for FCPF11N60F



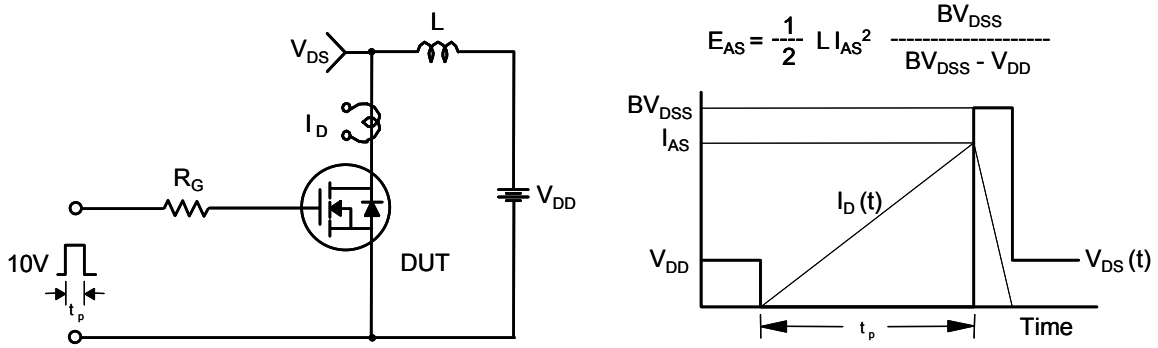
Gate Charge Test Circuit & Waveform



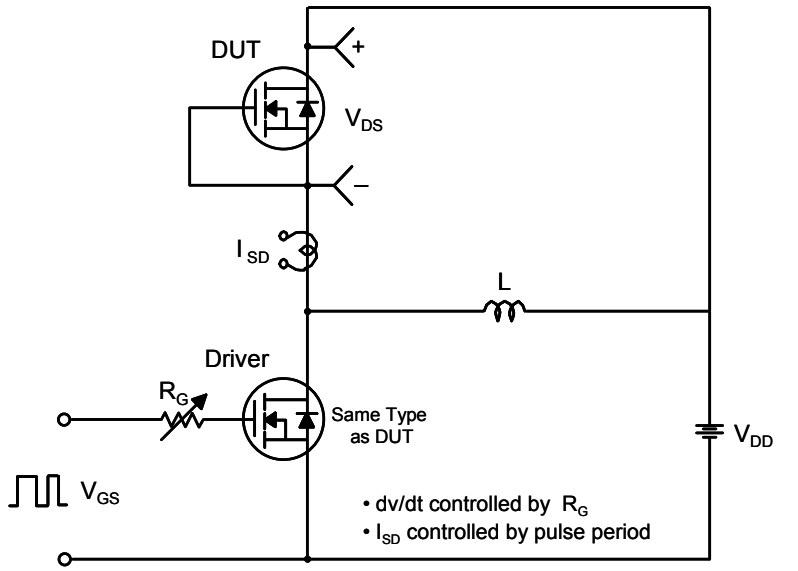
Resistive Switching Test Circuit & Waveforms

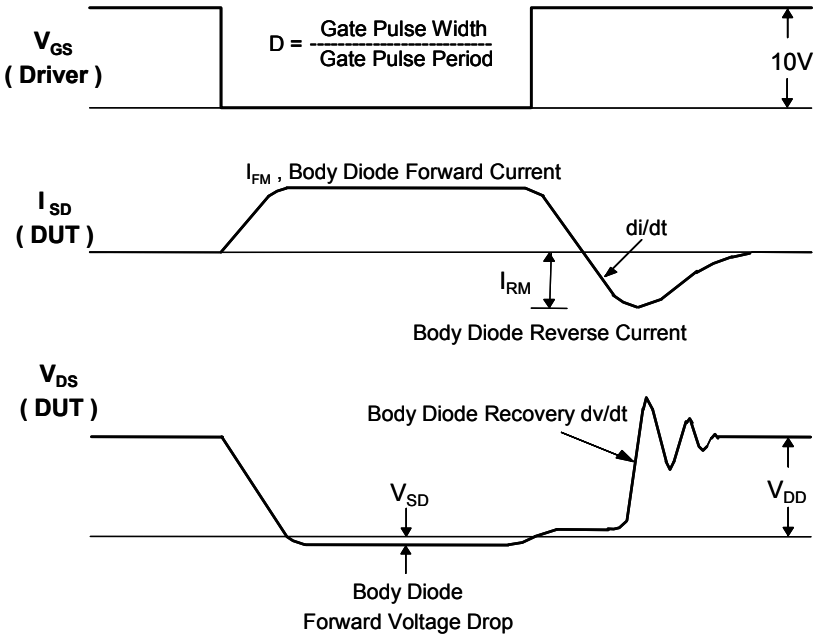


Unclamped Inductive Switching Test Circuit & Waveforms



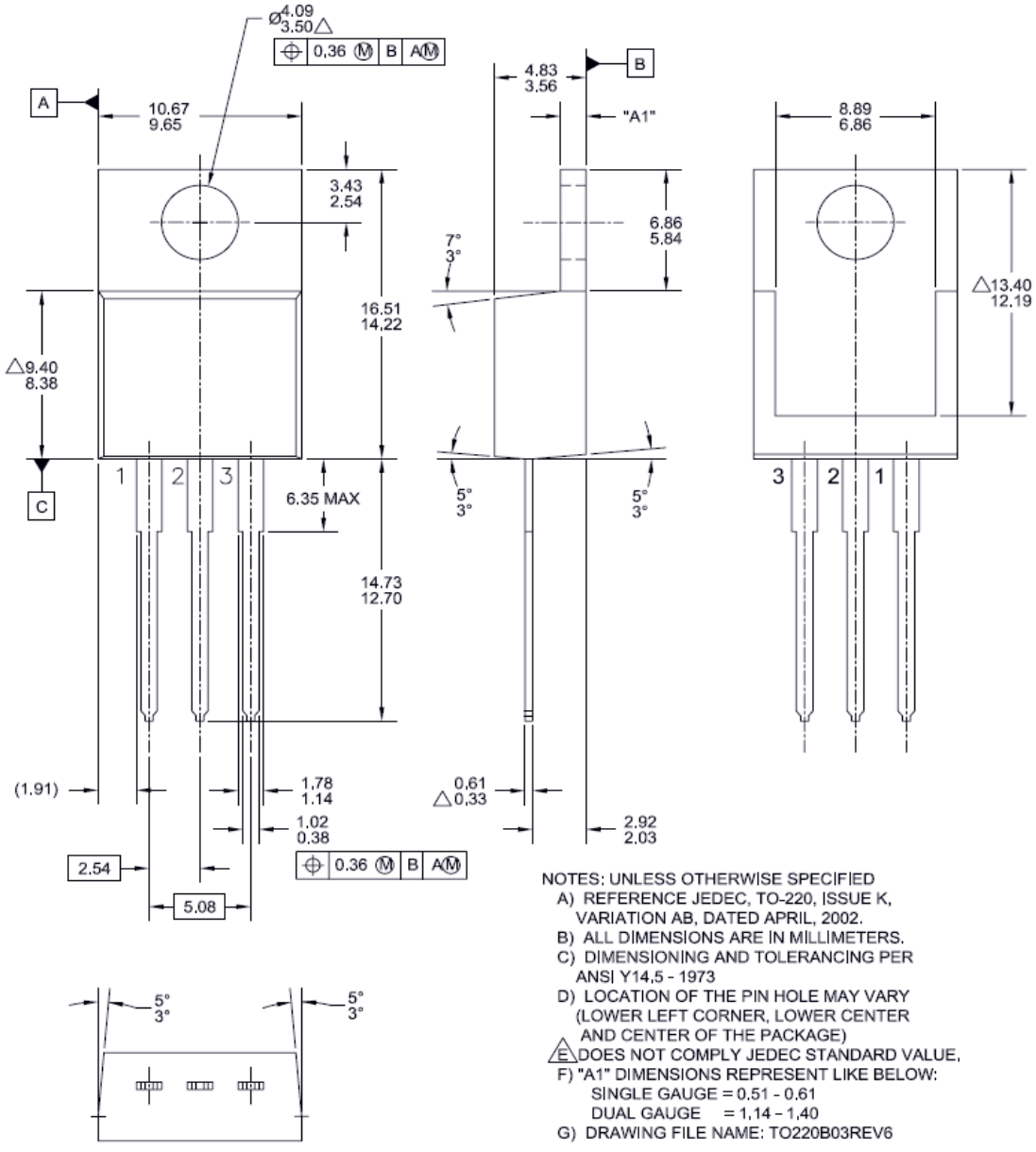
Peak Diode Recovery dv/dt Test Circuit & Waveforms





Mechanical Dimensions

TO-220

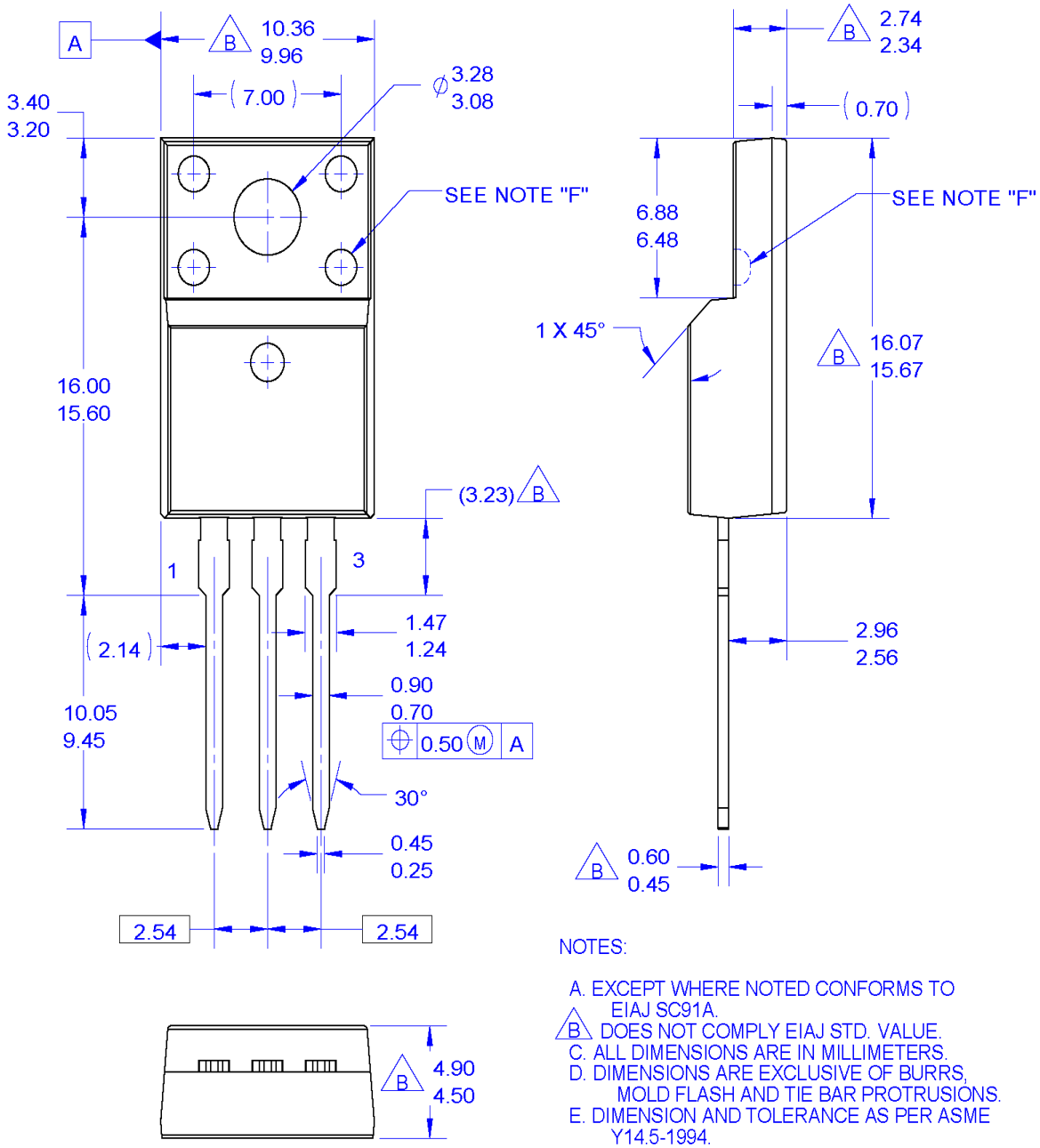


NOTES: UNLESS OTHERWISE SPECIFIED
A) REFERENCE JEDEC, TO-220, ISSUE K, VARIATION AB, DATED APRIL, 2002.
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONING AND TOLERANCING PER ANSI Y14.5 - 1973
D) LOCATION OF THE PIN HOLE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
△E DOES NOT COMPLY JEDEC STANDARD VALUE.
F) "A1" DIMENSIONS REPRESENT LIKE BELOW:
SINGLE GAUGE = 0.51 - 0.61
DUAL GAUGE = 1,14 – 1,40
G) DRAWING FILE NAME: TO220B03REV6

Dimensions in Millimeters

Mechanical Dimensions

TO-220F



NOTES:

A. EXCEPT WHERE NOTED CONFORMS TO EIAJ SC91A.
B DOES NOT COMPLY EIAJ STD. VALUE.
C. ALL DIMENSIONS ARE IN MILLIMETERS.
D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
E. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994.
F. OPTION 1 - WITH SUPPORT PIN HOLE.
OPTION 2 - NO SUPPORT PIN HOLE.
G. DRAWING FILE NAME: TO220M03REV3

Dimensions in Millimeters

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2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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