FAIRCHILD SEMICONDUCTOR®

March 2013

FDP054N10

N-Channel PowerTrench® MOSFET

100 V, 144 A, 5.5 mΩ

Features

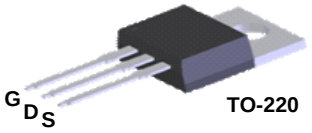
- $R_{DS(on)}$ = 4.6 mΩ (Typ.)@ V_{GS} = 10 V, I_D = 75 A
- Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(on)}$
- High Power and Current Handling Capability
- RoHS Compliant

Description

This N-Channel MOSFET is produced using Fairchild Semiconductor®'s advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Micro Solar Inverter



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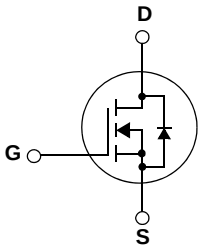


MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		FDP054N10	Unit
V_{DSS}	Drain to Source Voltage		100	V
V_{GSS}	Gate to Source Voltage		±20	V
I_D	Drain Current	- Continuous (T_C = 25°C, Silicon Limited)	144*	A
		- Continuous (T_C = 100°C, Silicon Limited)	102	
		- Continuous (T_C = 25°C, Package Limited)	120	
I_{DM}	Drain Current	- Pulsed (Note 1)	576	A
E_{AS}	Single Pulsed Avalanche Energy (Note 2)		1153	mJ
dv/dt	Peak Diode Avalanche Energy (Note 3)		6	V/ns
P_D	Power Dissipation	(T_C = 25°C)	263	W
		- Derate above 25°C	1.75	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	°C
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	°C

*Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 120A.

Thermal Characteristics

Symbol	Parameter	FDP054N10	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.57	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP054N10	FDP054N10	TO-220	-	-	50

Electrical Characteristics $T_C = 25^oC$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250\mu A$, $V_{GS} = 0V$, $T_C = 25^oC$	100	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu A$, Referenced to 25^oC	-	0.01	-	$V/^oC$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 100V$, $V_{GS} = 0V$	-	-	1	μA
		$V_{DS} = 100V$, $V_{GS} = 0V$, $T_C = 150^oC$	-	-	500	
I_{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250\mu A$	2.5	3.5	4.5	V
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10V$, $I_D = 75A$	-	4.6	5.5	mΩ
g_{FS}	Forward Transconductance	$V_{GS} = 10V$, $I_D = 75A$	-	192	-	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1MHz$	-	9985	13280	pF
C_{oss}	Output Capacitance		-	935	1245	pF
C_{rss}	Reverse Transfer Capacitance		-	390	585	pF
$Q_{g(tot)}$	Total Gate Charge at 10V	$V_{DS} = 80V$, $I_D = 75A$, $V_{GS} = 10V$ (Note 4)	-	156	203	nC
Q_{gs}	Gate to Source Gate Charge		-	53	-	nC
Q_{gd}	Gate to Drain “Miller” Charge		-	48	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 50V$, $I_D = 75A$, $V_{GS} = 10V$, $R_{GEN} = 4.7\Omega$ (Note 4)	-	44	98	ns
t_r	Turn-On Rise Time		-	92	194	ns
$t_{d(off)}$	Turn-Off Delay Time		-	80	170	ns
t_f	Turn-Off Fall Time		-	39	88	ns
Drain-Source Diode Characteristics						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	144	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	576	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V$, $I_{SD} = 75A$	-	-	1.3	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0V$, $I_{SD} = 75A$, $dI_F/dt = 100A/\mu s$	-	57	-	ns
Q_{rr}	Reverse Recovery Charge		-	121	-	nC

Notes:
1: Repetitive Rating: Pulse width limited by maximum junction temperature
2: L = 0.41mH, $I_{AS} = 75A$, $V_{DD} = 50V$, $R_G = 25\Omega$, Starting $T_J = 25°C$
3: $I_{SD} \le 75A$, $di/dt \le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25°C$
4: Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

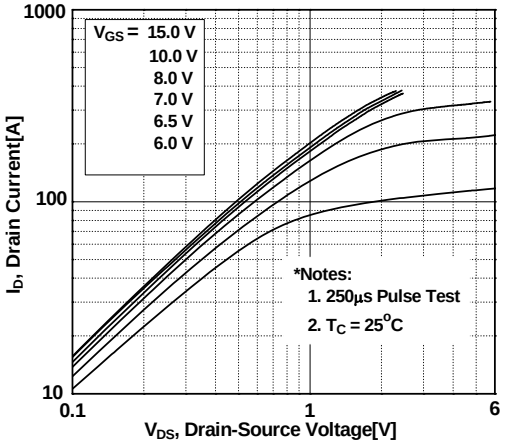


Figure 2. Transfer Characteristics

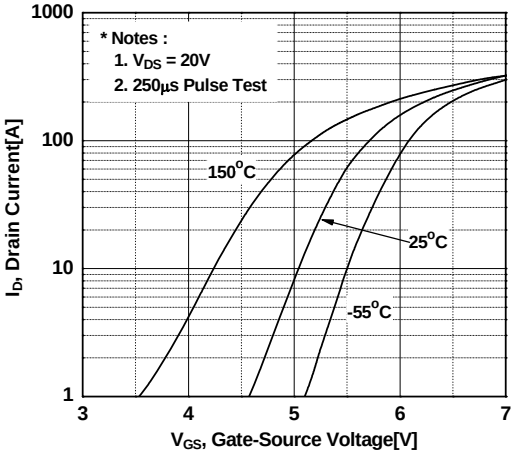


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

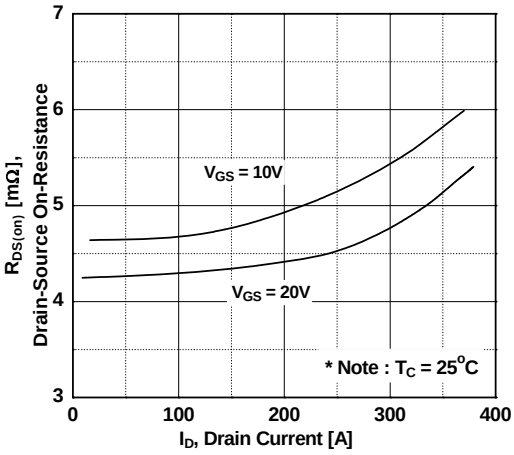


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

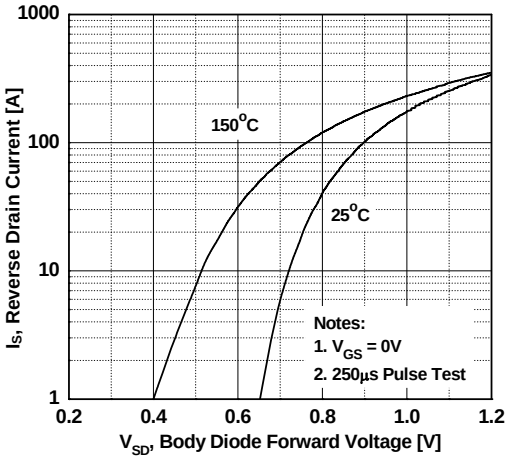


Figure 5. Capacitance Characteristics

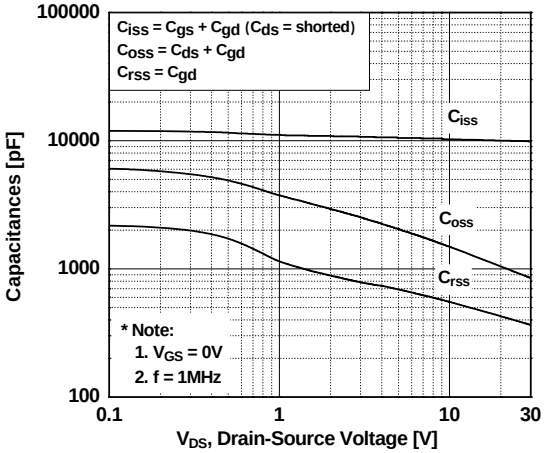
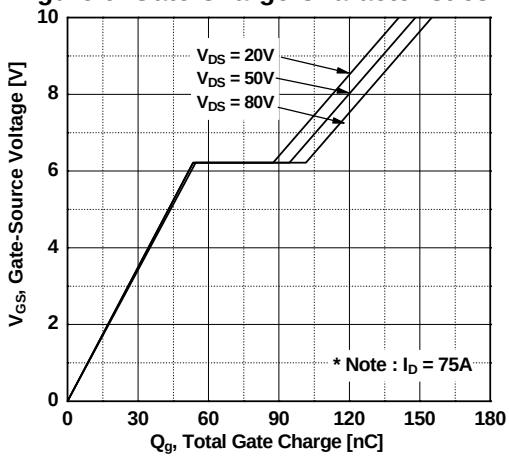


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

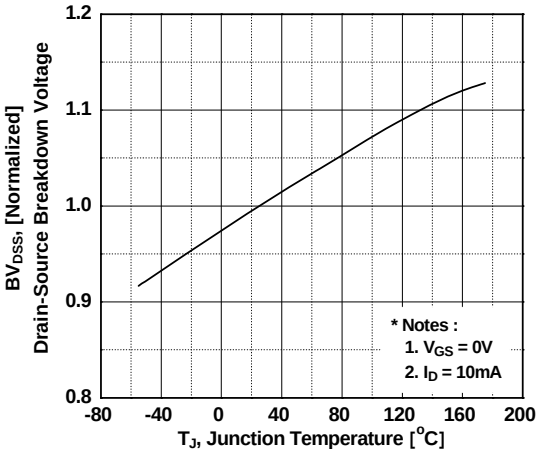


Figure 8. On-Resistance Variation vs. Temperature

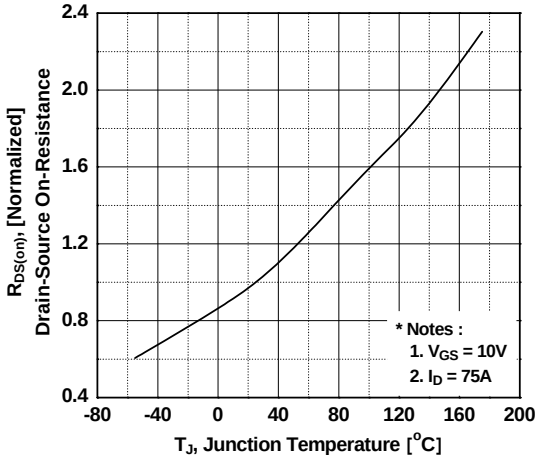


Figure 9. Maximum Safe Operating Area

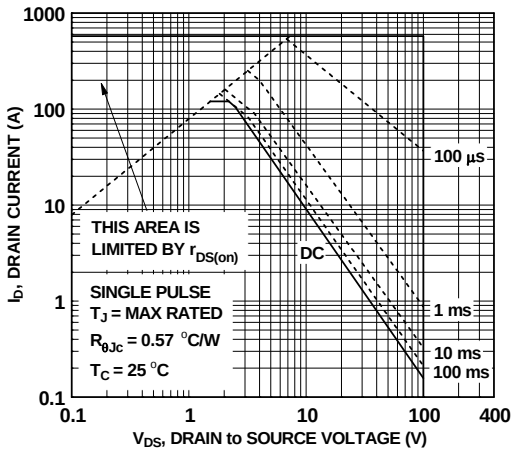


Figure 10. Maximum Drain Current vs. Case Temperature

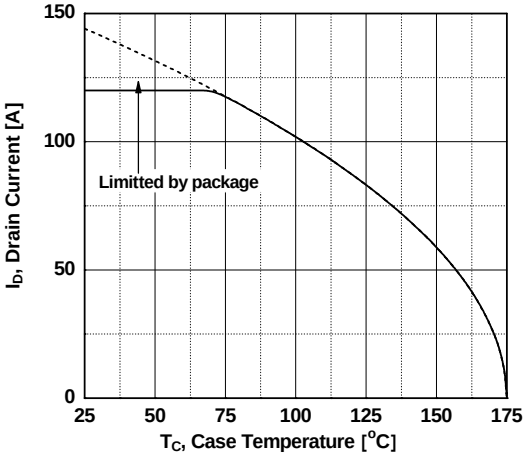
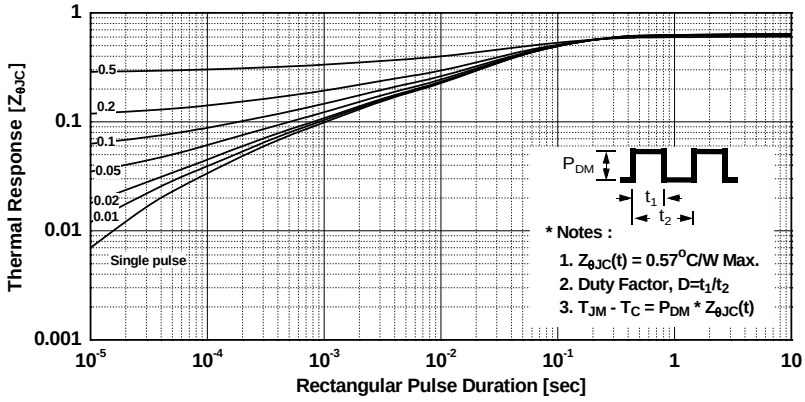
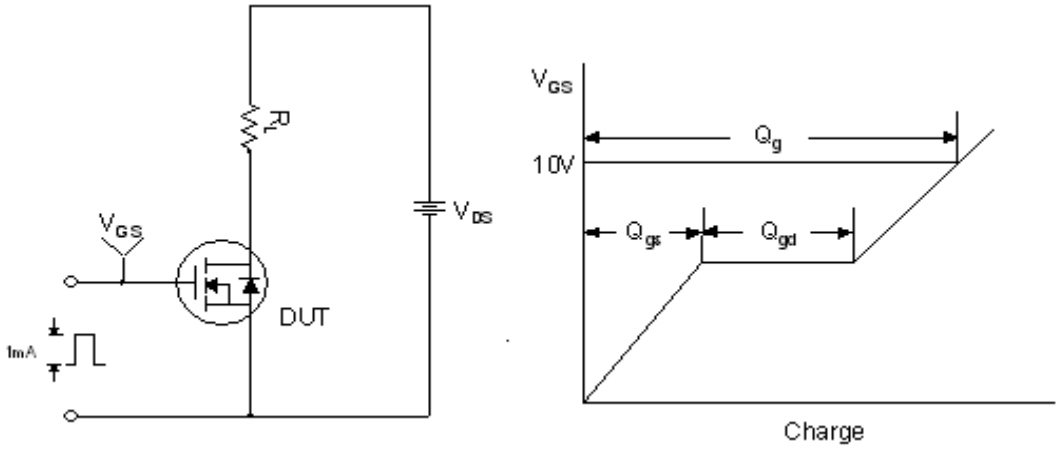


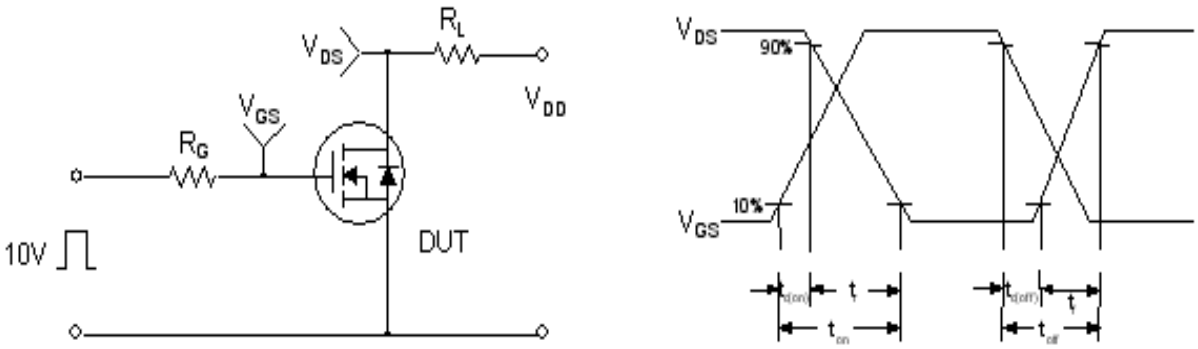
Figure 11. Transient Thermal Response Curve



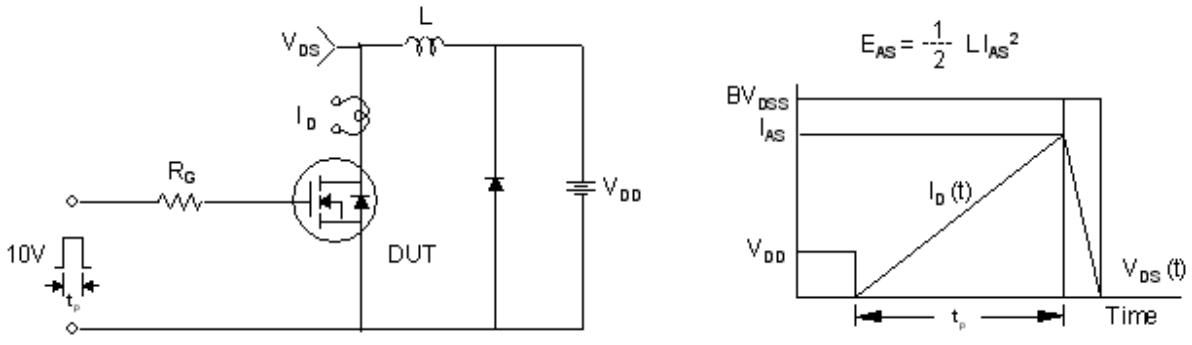
Gate Charge Test Circuit & Waveform



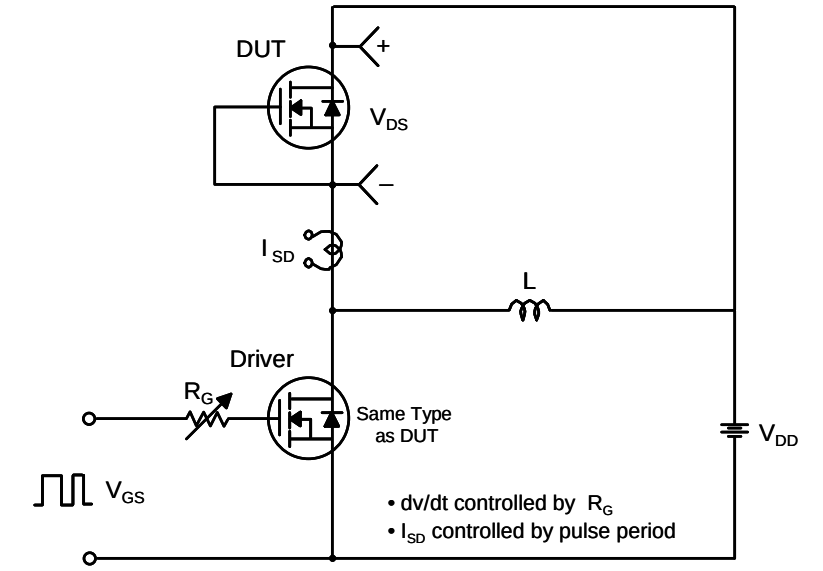
Resistive Switching Test Circuit & Waveforms

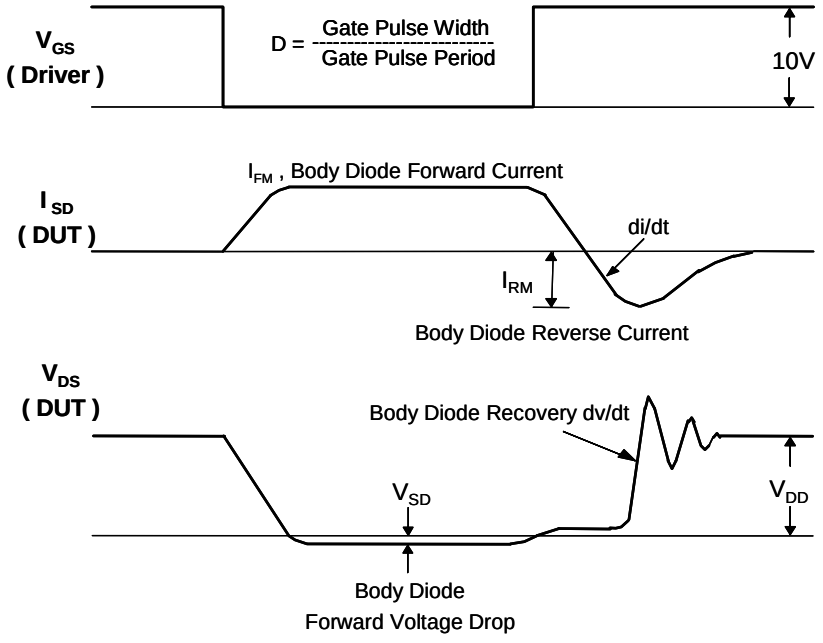


Unclamped Inductive Switching Test Circuit & Waveforms



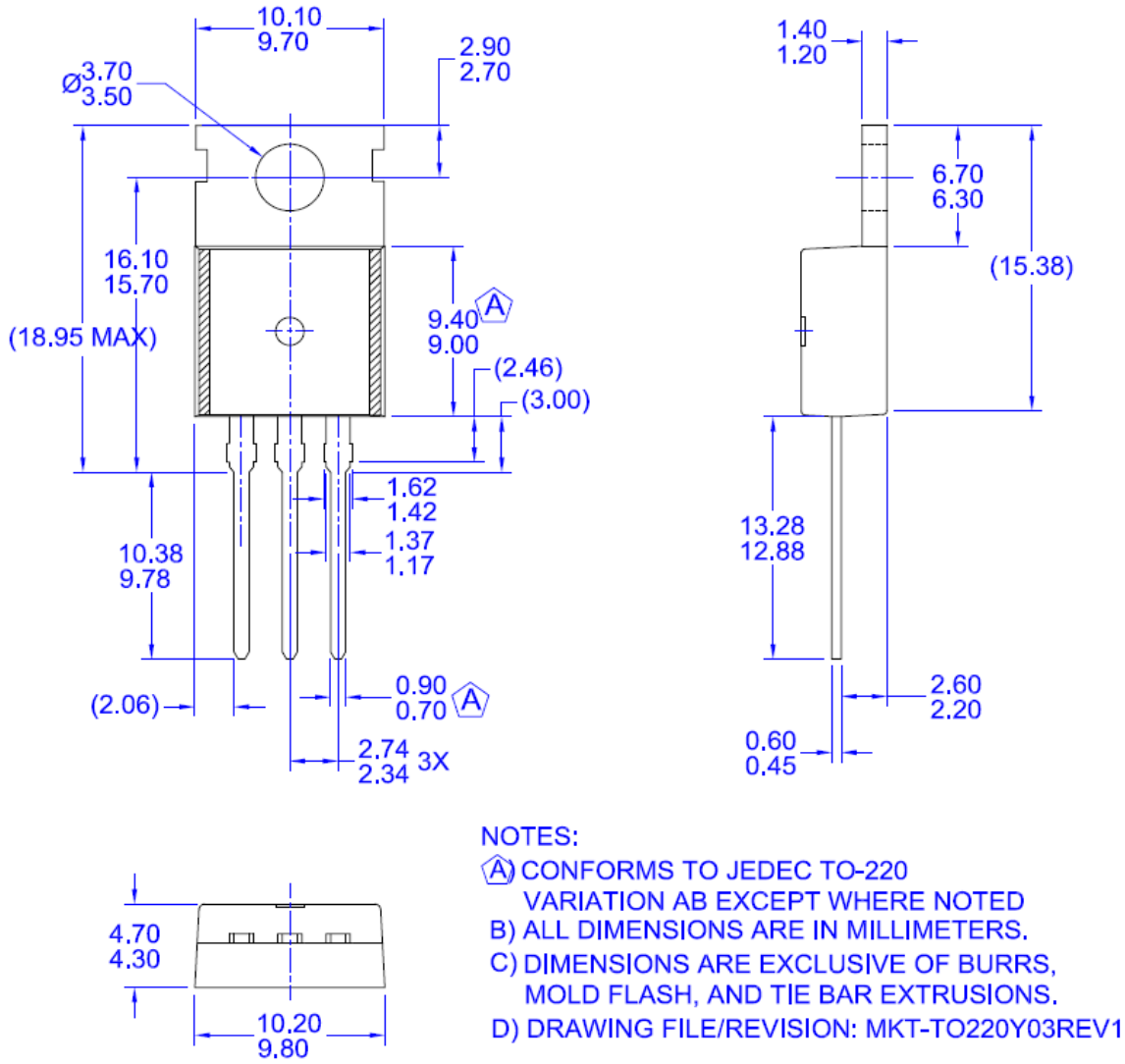
Peak Diode Recovery dv/dt Test Circuit & Waveforms





Package Dimensions

TO-220



NOTES:

A) CONFORMS TO JEDEC TO-220 VARIATION AB EXCEPT WHERE NOTED

B) ALL DIMENSIONS ARE IN MILLIMETERS.

C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

D) DRAWING FILE/REVISION: MKT-TO220Y03REV1

Dimensions in Millimeters

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Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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