



Target Applications

- GPS receivers
- Blood glucose meters
- Bike computers
- Currency counters

Kinetis K40 Family

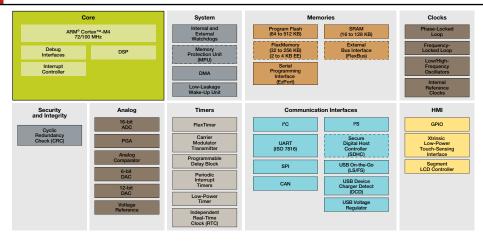
Low-power MCUs with USB and LCD

Overview

The Kinetis MCU portfolio consists of multiple pin-, peripheral- and software-compatible MCU families based on the ARM® Cortex™-M4 core. Families are built from innovative 90 nm thin-film storage (TFS) flash technology with unique FlexMemory (EEPROM) capability, and offer industry-leading low power and mixed signal analog integration.

The K40 MCU family adds Full-Speed USB 2.0 On-The-Go with device charger detect capability and a flexible, low-power segment LCD controller with support for up to 320 segments. Devices start from 64 KB of flash in 64 LQFN packages extending up to 512 KB in a 144 MAPBGA package with a rich suite of analog, communication, timing and control peripherals.

Kinetis K40 Family



Standard Optional



One-Stop Enablement Offering-MCU + IDE + RTOS

Freescale Tower System hardware development environment:

- Integrated development environments
 - Eclipse-based CodeWarrior V10.x IDE and Processor Expert
 - o IAR Embedded Workbench
 - Keil MDK
 - o CodeSourcery Sourcery G++ (GNU)
- · Runtime software and RTOS
 - Math, DSP and encryption libraries
 - Motor control libraries
 - Complimentary bootloaders (USB, Ethernet, RF, serial)
 - Complimentary Freescale embedded GUI
 - Complimentary Freescale MQX™
 - Cost-effective Nano™ SSL/Nano™ SSH for Freescale MQX RTOS
 - o Micrium uC/OS-III
 - Express Logic ThreadX
 - SEGGER embOS
 - o freeRTOS
 - Mocana (security)
- Full ARM ecosystem

Features Benefits

- ARM® CortexTM-M4 core with DSP instruction support
- Up to 16-channel DMA. Crossbar switch
- Up to 100 MHz core supporting a broad range of processing bandwidth needs
- Peripheral and memory servicing with reduced CPU loading. Concurrent multi-master bus accesses for increased bus bandwidth
- USB On-The-Go (Full Speed) with device charger detect
 Optimized charging current/time for portable USB devices, enabling longer battery life. USB low-voltage regulator supplies up to 120 mA off chip at 3.3 V to power external components from 5 V input
 - LCD blink mode enables low average power while remaining in low-
- Flexible, low-power LCD controller with support for up to 320 segments (40 x 8 or 44 x 4)
- Segment fail detect guards against erroneous readouts and reduces LCD test costs
 Frontplane/backplane reassignment provides pin-out flexibility, easing
- PCB design and allows LCD configuration changes via firmware with no hardware re-work

 Supports multiple 3 V and 5 V LCD panel sizes with fewer segments
- (pins) than competitive controllers and no external components

 Unused LCD pins can be configured as other GPIO functions

Enables the connection of external memories and peripherals

- FlexBus external bus interface Secure digital host controller
 - (e.g., graphics displays)
 Connection to SD, SDIO, MMC or CE-ATA cards for in-application software upgrades, file systems or adding Wi-Fi® or Bluetooth® support
- Up to three FlexTimers with up to 12 channels
- General-purpose timers with hardware dead-time insertion and quadrature decoding for motor control
- Carrier modulator transmitter
- Infrared waveform generation for remote control applications
- 4-channel, 32-bit interrupt
- Time base generation for RTOS task scheduler or trigger source for ADC conversion and programmable delay block
- 64–512 KB flash. Up to 128 KB of SRAM
 32–256 KB FlexMemory
- High reliability, fast access program memory with 4-level security protection. Independent flash banks allow concurrent code execution and firmware updating
- FlexMemory provides 32 byte-4 KB of user-segmentable byte write/ erase EEPROM. In addition, Flex NVM 32-256 KB for extra program code, data or EEPROM backup

K40 Family Options

		Mer	nory		Feature Options								Packages					
					Unit		Controller	90		L.			LH	LK	LL	мс	LQ	MD
Part Number	CPU (MHz)	Flash (KB)	FIex NVM (KB)	SRAM (KB)	Memory Protection U	CAN	Secure Digital Host Con	External Bus Interface	12-bit DAC	Prog. Gain Amplifier	5 V Tolerant I/O	Other	64 LQFP (10 x 10)	80 LQFP (12×12)	100 LQFP (14 x 14)	121 BGA (8 × 8)	144 LQFP (20 x 20)	144 BGA (13×13)
MK40DN512Vyy10	100	512		128	1	V	J	*	J	V	V	USB OTG (FS), Segment LCD (up to 40 x 8/44 x 4)		1	1	1	1	1
MK40DX64Vyy7	72	64	32	16		J			J	J	J	USB OTG (FS), Segment LCD (up to 24 x 8/28 x 4)	1	1		1		
MK40DX128Vyy7	72	128	32	32		1			J	1	1	USB OTG (FS), Segment LCD (up to 38 x 8/42 x 4)	1	1	1	1		
MK40DX256Vyy7	72	256	32	64		1			J	1	1	USB OTG (FS), Segment LCD (up to 38 x 8/42 x 4)		1	1	1		
MK40DX128yy10	100	128	128	32	1	1	J	J	J	J	1	USB OTG (FS), Segment LCD (up to 40 x 8/44 x 4)					1	1
MK40DX256yy10	100	256	256	64	1	V	J	J	J	J	V	USB OTG (FS), Segment LCD (up to 40 x 8/44 x 4)					1	1

yy = Package designator *144pin only



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