

40MHz to 4GHz Linear Broadband Amplifiers

General Description

The MAX2612-MAX2616 is a family of high-performance broadband gain blocks designed for use as a PA predriver, low-noise amplifier, or as a cascadable 50Ω amplifier with up to +19.5dBm output power. These devices are suited for many applications that include cellular infrastructure, private or commercial microwave radios, and CATV or cable modems. The operating frequency range extends from 40MHz to 4000MHz. The amplifier operates on a +3V to a +5.25V supply with input and output ports internally matched to 50Ω . The device family is available in a pin-to-pin compatible, compact 2mm x 3mm TDFN lead-free package.

Applications

Cellular Infrastructure Microwave Radio Wireless LAN Test and Measurement

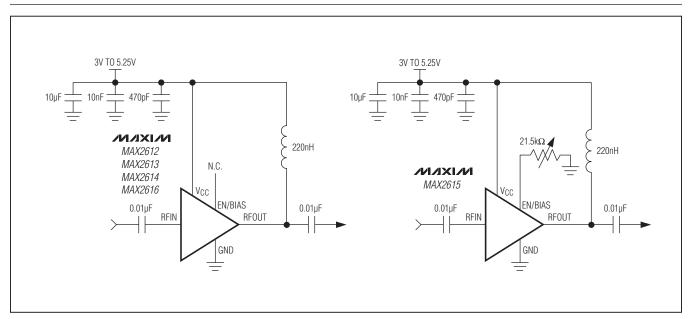
Ordering Information appears at end of data sheet.

For related parts and recommended products to use with this part, refer to www.maxim-ic.com/MAX2612.related.

Features

- Extremely Flat Frequency Response
- Low Noise Figure: 2.0dB at f_{RFIN} = 2.0GHz
- 40MHz to 4000MHz Frequency Range
- Industry's Highest Max P_{IN} Rating
- Large OIP3 Ranges
 - ♦ MAX2615/MAX2616: +37dBm
 - ♦ MAX2612: +35.2dBm
 - ♦ MAX2613: +31.2dBm
 - ♦ MAX2614: +30dBm
- Output P1dB: +19.5dBm (MAX2615/MAX2616)
- High Gain: 18.6dB
- Shutdown Mode (MAX2612/MAX2613/ MAX2614/MAX2616)
- Adjustable Bias Current for Improved OIP3 (MAX2615)
- 3.0V to 5.25V Supply Range
- Compact 2mm x 3mm TDFN Package
- Industry-High ESD Rating: 2.5kV HBM

Typical Application Circuits



MIXIM

Maxim Integrated Products 1

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ABSOLUTE MAXIMUM RATINGS

V _{CC} ,EN/RBIAS, RFOUT to GND0.3V to +6.0V Maximum Input Power (RFIN)+20dBm	Junction Temperature+150°C Storage Temperature Range65°C to +160°C
Continuous Power Dissipation ($T_A = +70^{\circ}C$)	Lead Temperature (soldering, 10s)+300°C
TDFN (derates 16.7mW/°C above +70°C)1333.3mW	Soldering Temperature (reflow)+260°C
Operating Temperature Range40°C to +85°C	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

PACKAGE THERMAL CHARACTERISTICS (Note 1)

TDFN

Junction-to-Ambient Thermal Resistance (θ_{JA}).......60°C/W Junction-to-Case Thermal Resistance (θ_{JC})......11°C/W

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a fourlayer board. For detailed information on package thermal considerations, refer to www.maxim-ic.com/thermal-tutorial.

DC ELECTRICAL CHARACTERISTICS

 $(MAX2612/MAX2613/MAX2614/MAX2615/MAX2616~EV~Kit,~V_{CC}=+5.0V,~no~RF~input~signals~at~RFIN,~T_{A}=-40^{\circ}C~to~+85^{\circ}C,~unless$ otherwise noted. Typical values are at V_{RFOUT} = +5V, T_A = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS	
Supply Voltage	DC voltage at RFOUT	3	5	5.25	V	
	MAX2612		69			
	MAX2613		51.2			
Supply Current	MAX2614		40.6		mA	
	MAX2615, $R_{BIAS} = 21.5$ kΩ		81.5			
	MAX2616		80.6			
Shutdown Supply Current	EN logic-low		7		μΑ	
RBIAS Minimum	MAX2615		10		kΩ	

AC ELECTRICAL CHARACTERISTICS

 $(MAX2612/MAX2613/MAX2614/MAX2615/MAX2616~EV~Kit,~V_{CC}=+5V,~T_{A}=-40^{\circ}C~to~+85^{\circ}C,~unless~otherwise~noted.~Typical~values~the value of the v$ are at V_{RFOUT} = +5V, T_A = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	CC	NDITIONS	MIN	TYP	MAX	UNITS
RFIN Frequency Range			40		4000	MHz
		MAX2612		18.3		
		MAX2613		18.6		
	$f_{RFIN} = 1000MHz$	MAX2614 18.6			1	
	(Note 3)	MAX2615		18.5		1
D 0:		MAX2616		18.4		1
Power Gain		MAX2612		17.5		dB
		MAX2613		18.1		1
		f _{RFIN} = 4000MHz MAX2614 17.5	17.5		1	
	(Note 3)	MAX2615		18.0		1
		MAX2616		18.0		

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AC ELECTRICAL CHARACTERISTICS (continued)

(MAX2612/MAX2613/MAX2614/MAX2615/MAX2616 EV Kit, $V_{CC} = +5V$, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted. Typical values are at $V_{RFOUT} = +5V$, $T_A = +25^{\circ}C$, unless otherwise noted.) (Note 2)

PARAMETER	CONE	DITIONS	MIN	TYP	MAX	UNITS
		MAX2612		0.2		
	f _{RFIN} = 1000MHz <	MAX2613		0.1		1
	f _{RFOUT} < 3000MHz	MAX2614		0.15		1
	(Note 3)	MAX2615		0.15		
Gain Flatness Across Band		MAX2616		0.1		dB
Gain Flathess Across Band		MAX2612		0.8] UB
	f _{RFIN} = 1000MHz <	MAX2613		0.5		
	f _{RFOUT} < 4000MHz	MAX2614		1.1		
	(Note 3)	MAX2615		0.5		
		MAX2616		0.4		
		MAX2612		2.1	2.65	
	f 2000MI	MAX2613		2	2.42	
Noise Figure	f _{RFIN} = 2000MHz (Note 3)	MAX2614		2	2.35	dB
	(Note 3)	MAX2615		2.2	2.95	1
		MAX2616		2.2	2.85	
	Input tones at 1000MHz and 1001MHz at	MAX2612		35.2		
		MAX2613		31.2		dBm
OIP3		MAX2614		29.7		
	-15dBm/tone	MAX2615		37.6		
		MAX2616		37.2		
		MAX2612		18.2		dBm
	f 1000MI	MAX2613		15.5		
Output P1dB	f _{RFIN} = 1000MHz (Note 3)	MAX2614		13.6		
	(Note 5)	MAX2615		19.5		
		MAX2616		19.5		
Reverse Isolation	40MHz < f _{RFOUT} < 4000ľ	ИНz		20		dB
		MAX2612		15		
	400 41 1 6	MAX2613		15		
	40MHz < f _{RFOUT} < 1000MHz	MAX2614		12		
	10001/11/12	MAX2615		15		
DEIN Input Datum I aga		MAX2616		15		dB
RFIN Input Return Loss		MAX2612		12] UD
	1000M1-	MAX2613		8		
	1000MHz < f _{RFOUT} < 4000MHz	MAX2614		8		
	40001811 12	MAX2615		12		
		MAX2616		12		

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AC ELECTRICAL CHARACTERISTICS (continued)

 $(MAX2612/MAX2613/MAX2614/MAX2615/MAX2616~EV~Kit,~V_{CC}=+5V,~T_{A}=-40^{\circ}C~to~+85^{\circ}C,~unless~otherwise~noted.~Typical~values~the value of the v$ are at $V_{RFOUT} = +5V$, $T_A = +25^{\circ}C$, unless otherwise noted.) (Note 2)

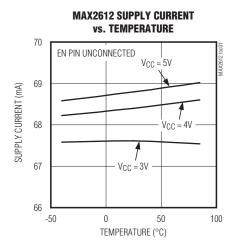
PARAMETER	COND	OITIONS	MIN	TYP	MAX	UNITS
		MAX2612		20		
	AONALL C	MAX2613		15		
	40MHz < f _{RFOUT} < 1000MHz	MAX2614		12		
	10001/11/12	MAX2615 20				
DEOLIT Output Datum Loop	N	MAX2616		20		dB
RFOUT Output Return Loss		MAX2612		12		ub
	1000011	MAX2613		10		
	1000MHz < f _{RFOUT} < MAX2614 10	10				
	40001011 12	MAX2615		12		
		MAX2616		12		

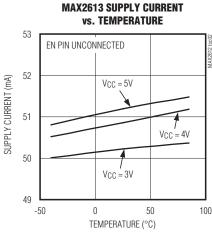
Note 2: Min and max values are production tested at $T_A = +85^{\circ}C$. Min and max limits at $T_A = +25^{\circ}C$ and $T_A = -40^{\circ}C$ are guaranteed by design and characterization.

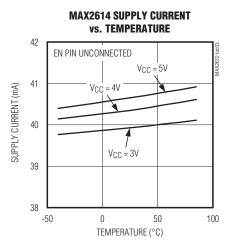
Note 3: Min and max values are guaranteed by design and characterization at $T_A = +25$ °C.

Typical Operating Characteristics

 $(MAX2612/MAX2613/MAX2614/MAX2615/MAX2616 EV Kit, V_{RFOUT} = +5V, T_A = +25^{\circ}C.)$



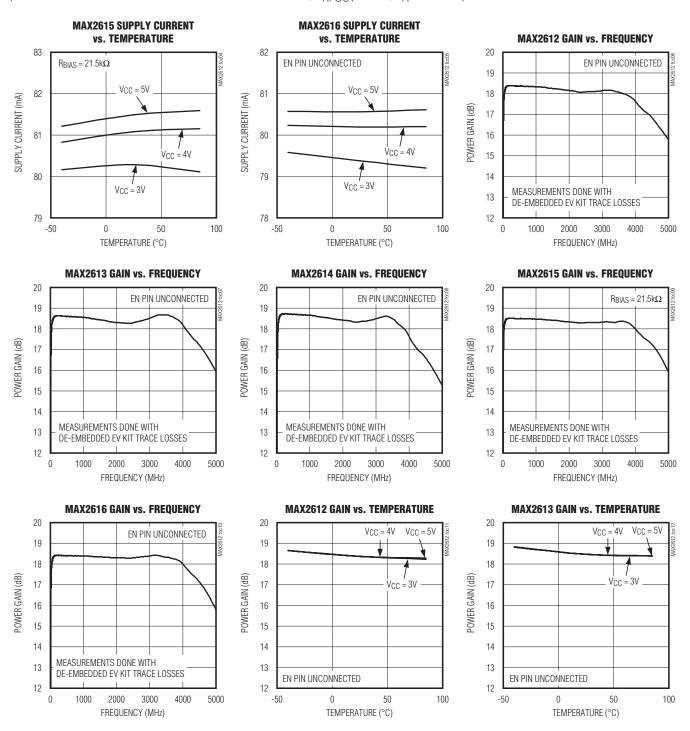




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Typical Operating Characteristics (continued)

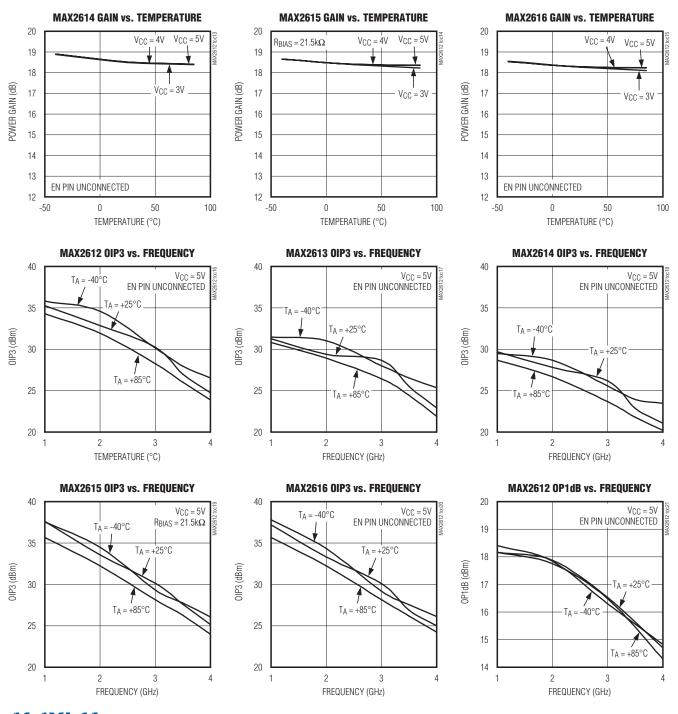
 $(MAX2612/MAX2613/MAX2614/MAX2615/MAX2616 EV Kit, V_{RFOUT} = +5V, T_{A} = +25^{\circ}C.)$



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Typical Operating Characteristics (continued)

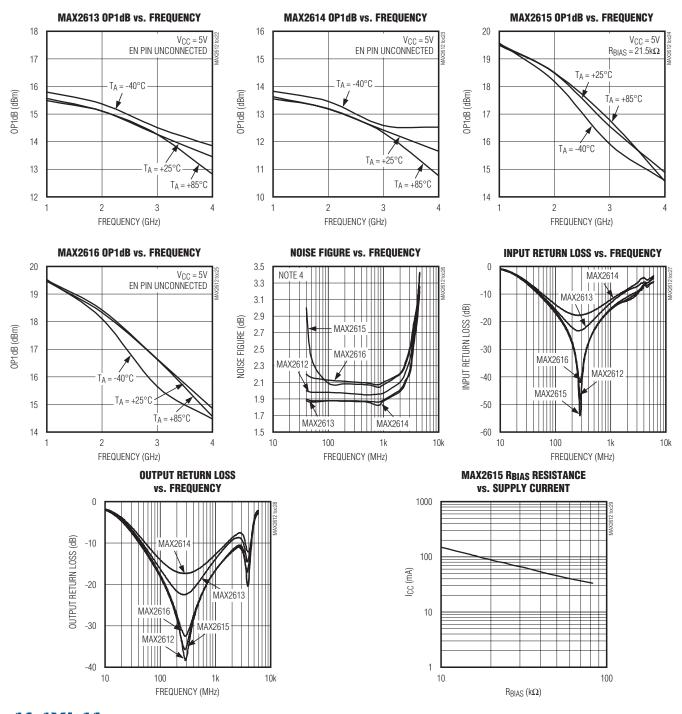
 $(MAX2612/MAX2613/MAX2614/MAX2615/MAX2616 EV Kit, V_{RFOUT} = +5V, T_{A} = +25^{\circ}C.)$



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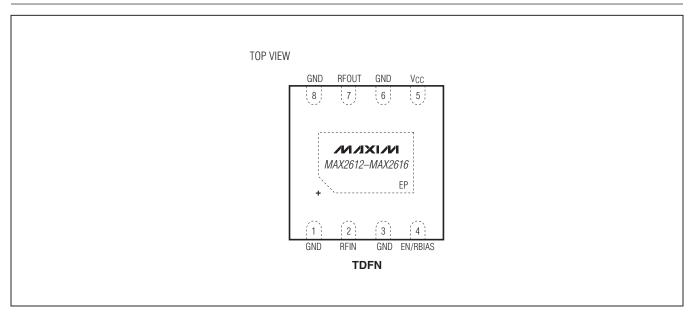
Typical Operating Characteristics (continued)

 $(MAX2612/MAX2613/MAX2614/MAX2615/MAX2616 EV Kit, V_{RFOUT} = +5V, T_{A} = +25^{\circ}C.)$



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Pin Configuration



Pin Description

PIN	NAME	FUNCTION
1, 3, 6, 8	GND	Ground. Connect to PCB ground plane.
2	RFIN	RF Input. Connect to an RF source through a 0.01 μ F DC-blocking capacitor. Internally matched to 50 Ω .
4	EN/RBIAS	Enable (MAX2612/MAX2613/MAX2614/MAX2616). Leave unconnected for normal operation or logic-low for disable mode operation. For applications that use the disable mode, it is recommended that the logic-high signal be derived from a high-impedance source such as an unterminated open-collector output or three-state (high-Z) output. Logic-low should be a low-impedance source or a switch to ground capable of sinking $10\mu A$. RBIAS (MAX2615). Connect to a $21.5k\Omega$ bias resistor to ground. The value can be adjusted to trade off supply current for OIP3. See the <i>Applications Information</i> section for further detail.
5	Vcc	DC Supply Input. Place 470pF and 10nF decoupling capacitors as close to pin as possible. Also place a $10\mu\text{F}$ bulk capacitor on V_{CC} ; this must be a tantalum capacitor with ESR > 2nF and can be placed further away.
7	RFOUT	RF Output and DC Feed. Connect to DC supply through a 220nH inductor. Connect to output load through a 0.01µF DC-blocking capacitor.
_	EP	Exposed Pad. Connect to PCB ground plane by a 3 x 3 array of vias. Connect to ground lead (1, 3, 6, 8) land patterns and to layer 1 ground plane with thermal relief traces.

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Detailed Description

Adjustable Bias Control for the MAX2615

While the MAX2612/MAX2613/MAX2614/MAX2616 are fixed biased for ease of use, the MAX2615 allows the current to be controlled by an external bias resistor connected from RBIAS (pin 4) to ground. In this configuration, the MAX2615 can be used over a range of current settings with an upper limit of ~150mA for an R_{BIAS} of $10k\Omega$ and a lower limit of 37.5mA for an R_{BIAS} of $69k\Omega$. Values within this range allow optimized performance and power consumption for customer requirements.

Applications Information

Wideband Designs

For LTE designs, the MAX261x family is ideally suited to minimize gain compensation over frequency while providing low noise and high OIP3 in a small (2mm x 3mm TDFN) but thermally efficient package. The same device can be used for multiple frequency bands without adjusting for gain slope degradation, a common artifact among pHEMT, InGaP, and GaAs gain blocks.

Input Overload Handling

As a result of its simple Darlington architecture and rugged bipolar process, the MAX261x family provides an industry-leading +20dBm maximum input power rating. This inherently reduces the need for input protection circuitry while greatly minimizing the potential for damage to the device from intermittent RF surges.

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE
MAX2612 ETA+	-40°C to +85°C	8 TDFN-EP*
MAX2613ETA+	-40°C to +85°C	8 TDFN-EP*
MAX2614ETA+	-40°C to +85°C	8 TDFN-EP*
MAX2615ETA+	-40°C to +85°C	8 TDFN-EP*
MAX2616 ETA+	-40°C to +85°C	8 TDFN-EP*

⁺Denotes a lead(Pb)-free/RoHS-compliant package.

Package Information

For the latest package outline information and land patterns (footprints), go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE	PACKAGE	OUTLINE	LAND
TYPE	CODE	NO.	PATTERN NO.
8 TDFN-EP	T823+1	20-0174	

^{*}EP = Exposed Pad.

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	
0	5/12	Initial release	_

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time. The parametric values (min and max limits) shown in the Electrical Characteristics table are guaranteed. Other parametric values quoted in this data sheet are provided for guidance.

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