

SANYO Semiconductors DATA SHEET

N-Channel Silicon MOSFET

MCH3427 — General-Purpose Switching Device **Applications**

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- 1.8V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±12	V
Drain Current (DC)	ID		4	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	16	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)	1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Linit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	20			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =2A	2.9	4.9		S
Static Drain-to-Source On-State Resistance	RDS(on)1	I _D =2A, V _G S=4V		40	52	mΩ
	RDS(on)2	ID=1A, VGS=2.5V		54	76	mΩ
	RDS(on)3	I _D =0.5A, V _G S=1.8V		72	110	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		400		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		92		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		85		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		11		ns
Rise Time	t _r	See specified Test Circuit.		75		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		54		ns
Fall Time	t _f	See specified Test Circuit.		60		ns

Marking: ZC Continued on next page.

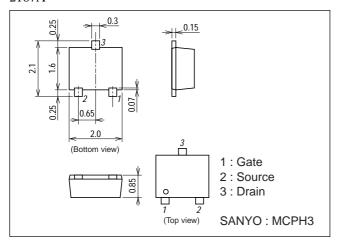
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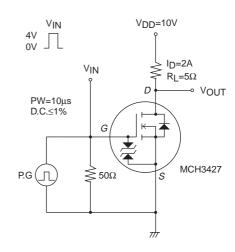
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =4V, I _D =4A		6		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =4V, I _D =4A		0.8		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =4V, I _D =4A		2.2		nC
Diode Forward Voltage	VSD	IS=4A, VGS=0		0.87	1.2	V

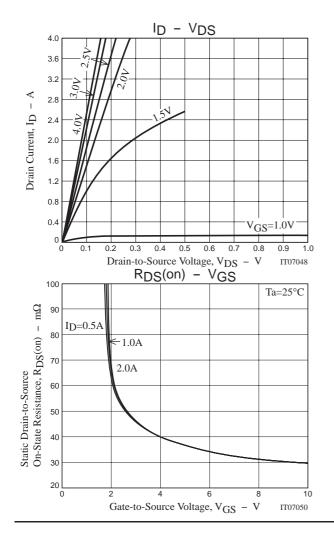
Package Dimensions

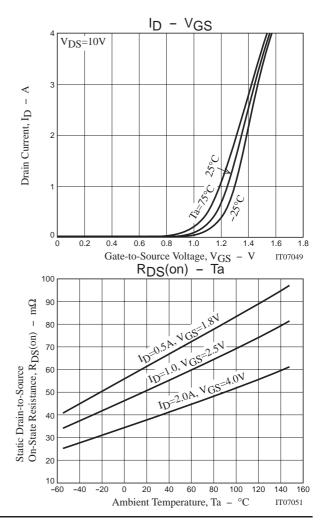
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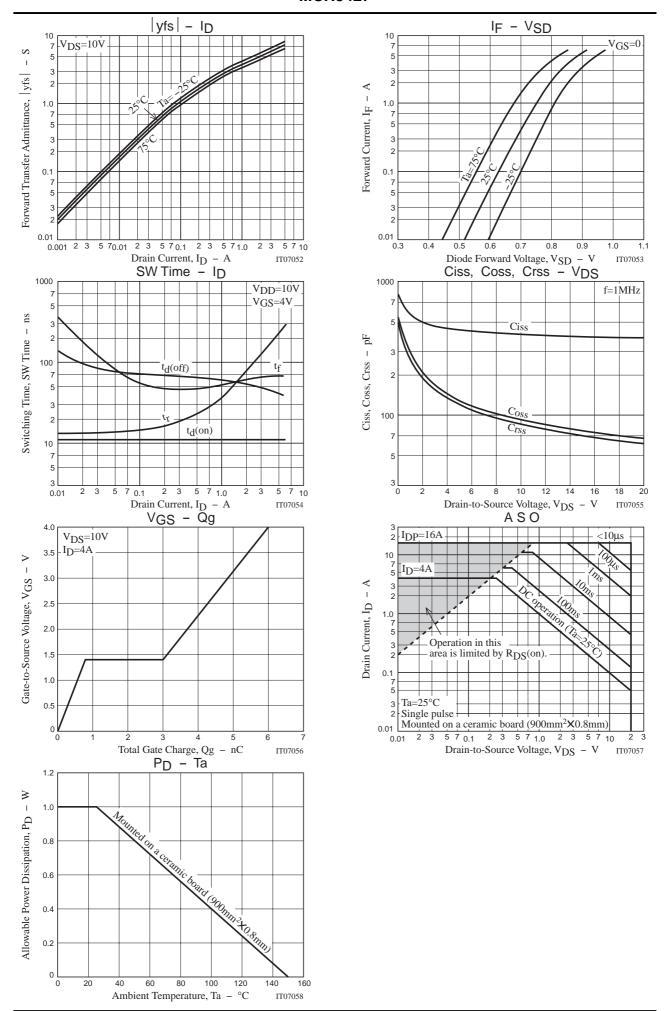


Switching Time Test Circuit









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