Standard Rectifier Module

MDMA110P1200TG

preliminary

= 2x 1200 V

110A

1.11V

Phase leg

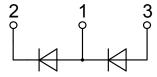
Part number

MDMA110P1200TG



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Reduced weight
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For single and three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
 Field supply for DC motors

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- Reduced weight
- · Advanced power cycling



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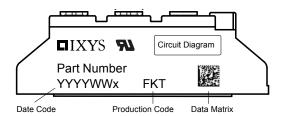
Rectifie	•				Ratings	3	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse block	ing voltage	$T_{VJ} = 25^{\circ}C$			1300	V
V _{RRM}	max. repetitive reverse blocking v	roltage	$T_{VJ} = 25^{\circ}C$			1200	V
I _R	reverse current, drain current	V _R = 1200 V	$T_{VJ} = 25^{\circ}C$			200	μΑ
		V _R = 1200 V	$T_{VJ} = 150^{\circ}C$			3	mΑ
V _F	forward voltage drop	I _F = 110 A	$T_{VJ} = 25^{\circ}C$			1.18	V
		I _F = 220 A				1.42	V
		I _F = 110 A	T _{VJ} = 125°C			1.11	V
		I _F = 220 A				1.40	V
I _{FAV}	average forward current	T _C = 100°C	T _{VJ} = 150°C			110	Α
		rectangular d = 0.5					
V _{F0}	threshold voltage		T _{VJ} = 150°C			0.79	V
r _F	slope resistance	oss calculation only				2.7	mΩ
R _{thJC}	thermal resistance junction to cas	e				0.3	K/W
R _{thCH}	thermal resistance case to heatsi	nk			0.20		K/W
P _{tot}	total power dissipation		$T_{c} = 25^{\circ}C$			415	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			2.00	kA
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			2.16	kA
		t = 10 ms; (50 Hz), sine	T _{VJ} = 150°C			1.70	kA
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			1.84	kA
I²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			20.0	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			19.4	kA²s
		t = 10 ms; (50 Hz), sine	T _{VJ} = 150°C			14.5	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			14.0	kA²s
C,	junction capacitance	V _R = 400 V; f = 1 MHz	$T_{VJ} = 25^{\circ}C$		73		pF



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Package TO-240AA				Ratings				
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					200	Α
T _{stg}	storage temperature				-40		125	°C
T _{VJ}	virtual junction temperature				-40		150	°C
Weight						90		g
M _D	mounting torque				2.5		4	Nm
\mathbf{M}_{T}	terminal torque				2.5		4	Nm
d _{Spp/App}	creenage distance on surfac	e striking distance through air	terminal to terminal	13.0	9.7			mm
d _{Spb/Apb}	creepage distance on sanae	o striking distance through an	terminal to backside	16.0	16.0			mm
V _{ISOL}	isolation voltage	t = 1 second			4800			V
		t = 1 minute	50/60 Hz, RMS; I _{ISOL} ≤ 1 mA		4000			V



Part number

M = Module

D = Diode

M = Standard Rectifier

A = (up to 1800V) 110 = Current Rating [A] P = Phase leg

1200 = Reverse Voltage [V] TG = TO-240AA

Ordering	Part Number	Marking on Product	Delivery Mode	Quantity	Code No.	
Standard	MDMA110P1200TG	MDMA110P1200TG	Box	6	514304	

Equiva	alent Circuits for	Simulation	* on die level	T _{VJ} = 150 °C
$I \rightarrow V_0$)—[R ₀]—	Rectifier		
V _{0 max}	threshold voltage	0.79		V
R_{0max}	slope resistance *	1.5		$m\Omega$

preliminary

Outlines TO-240AA

