

PI5C3253C

# **Dual 4:1 Mux/DeMux Bus Switch** with -2V Undershoot Protection

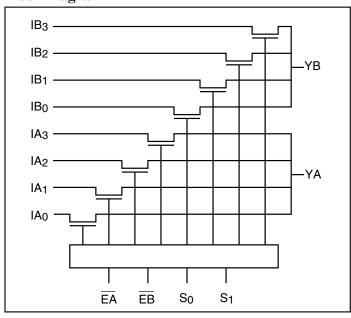
#### **Features**

- Near-Zero propagation delay
- 4-ohm switches connect inputs to outputs
- · Direct bus connection when switches are ON
- Undershoot protection to -2V (Data Inputs Only)
- Pin compatible with 74 series 253 logic devices
- Packaging (Pb-free & Green available):
  - −16-pin 173-mil wide plastic TSSOP (L)

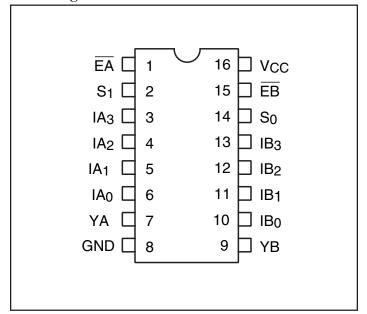
# **Description**

Pericom Semiconductor's PI5C3253C is a Dual 4:1 multiplexer/demultiplexer with three-state outputs that is pinout compatible with the PI74FCT253T. Inputs can be connected to outputs with low On-Resistance (5-ohm) with no additional ground bounce noise or propagation delay.

## **Block Diagram**



# **Pin Configuration**



#### **Pin Description**

Pin Name	Description
$\overline{\mathrm{EA}}, \overline{\mathrm{EB}}$	Enable
S0-1	Select Inputs
1AN,1BN	Data Inputs
YA, YB	Data Outputs
VCC	Power Supply Pin
GND	Ground

## Truth Table<sup>(1)</sup>

Ena	ble	Sel	ect	Channel		
EA	EB	S1	S0	YA	YB	Function
Н	X	X	X	Hi-Z	X	Disable A
X	Н	X	X	X	Hi-Z	Disable B
L	L	L	L	IA0	IB0	S1-0 = 0
L	L	L	Н	IA1	IB1	S1-0 = 1
L	L	Н	L	IA2	IB2	S1-0 = 2
L	L	Н	Н	IA3	IB3	S1-0=2

**Note:** 1. H = High Logic Voltage Level

L = Low Logic Voltage Level

Hi-Z=High Impedance



## **Maximum Ratings**

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Ambient Temperature with Power Applied	-40°Cto+85°C
Supply Voltage to Ground Potential	-0.5Vto+7.0V
DC Input Voltage	-0.5Vto+7.0V
DC Output Current	
Power Dissipation	0.5W

#### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

#### **DC Electrical Characteristics** (Over the Operating Range, T<sub>A</sub> = -40°C to +85°C, V<sub>CC</sub> = 5V ±5%)

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	<b>Typ.</b> <sup>(2)</sup>	Max.	Units
V <sub>IH</sub>	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V <sub>IL</sub>	Input LOW Voltage	Guaranteed Logic LOW Level -			0.8	V
$I_{\mathrm{IH}}$	Input HIGH Current	V <sub>CC</sub> =Max., V <sub>IN</sub> =V <sub>CC</sub>			±1	μΑ
$I_{\mathrm{IL}}$	Input LOW Current	V <sub>CC</sub> =Max., V <sub>IN</sub> =GND			±1	μΑ
I <sub>OZH</sub>	High Impedance Output Current	$0 \le I_N, Y_N \le V_{CC}$			±1	μΑ
V <sub>IK</sub>	Clamp Diode Voltage	V <sub>CC</sub> =Min., I <sub>IN</sub> =-18mA		-0.7	-1.8	V
V <sub>UP</sub>	Undershoot Protection Voltage <sup>(3)</sup>	Max Inputs, Undershoot Pulse Duration <25ns			-2.0	V
RON	Switch On Resistance <sup>(4)</sup>	V <sub>CC</sub> =Min., V <sub>IN</sub> =0.0V, I <sub>ON</sub> =48mA		4	7	ohm
		$V_{CC}$ =Min., $V_{IN}$ =2.4V, $I_{ON}$ =15mA		8	15	

#### Capacitance ( $T_A = 25$ °C, f = 1 MHz)

Parameters <sup>(5)</sup>	Description	Test Conditions	Тур.	Max.	Units
C <sub>IN</sub>	Input Capacitance	$V_{\rm IN}$ =0 $V$		6	pF
Coff	I <sub>A</sub> /I <sub>B</sub> Capacitance, Switch Off	V <sub>IN</sub> =0V		6	pF
Con	I <sub>A</sub> /I <sub>B</sub> Capacitance, Switch On	V <sub>IN</sub> =0V		14	pF

#### **Notes:**

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at  $V_{CC} = 5.0V$ ,  $T_A = 25$ °C ambient and maximum loading.
- 3. On 1AN & 1BN (Data Inut) pins only.
- 4. Measured by the voltage drop between I and Y pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (I,Y) pins.
- 5. This parameter is determined by device characterization but is not production tested.

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## **Power Supply Characteristics**

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	<b>Typ</b> <sup>(2)</sup>	Max.	Units
$I_{CC}$	Quiescent Power Supply Current	V - M	$V_{IN} = GND \text{ or } V_{CC}$	_	_	100	μA
$\Delta I_{CC}$	Supply Current per Input @ TTL HIGH	$V_{CC} = Max.$	$V_{IN} = 3.4V^{(3)}$	_	_	2.5	mA

#### Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at  $V_{CC} = 5.0V$ ,  $+25^{\circ}C$  ambient.
- 3. Per TTL driven input ( $V_{IN} = 3.4V$ , control inputs only); I and Y pins do not contribute to  $I_{CC}$ .

## **Switching Characteristics over Operating Range**

			PI5C3	3253C	
			Co	m.	
Parameters	Description	Conditions <sup>(1)</sup>	Min.	Max.	Units
t <sub>IY</sub>	Propagation Delay <sup>(2,3)</sup> In to Yn			0.25	
$t_{\mathrm{SY}}$	Bus Select time Sn to Yn	$C_L = 50 pF$	0.5	4.5	, ma
t <sub>PZL</sub>	Bus Enable Time En to Yn	$R_L = 500 \text{ ohms}$	0.5	4.0	ns
t <sub>PHZ</sub> t <sub>PLZ</sub>	Bus Disable Time En to Yn		0.5	4.5	

## Notes:

- 1. See test circuit and waveforms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

# **Application Information**

# **Logic Inputs**

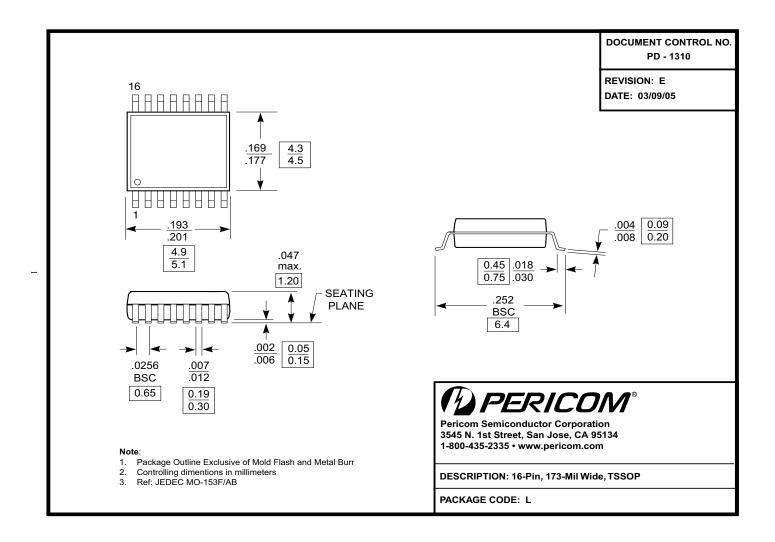
Logic control inputs can be driven up to +5.5V regardless of the supply voltage. For example, given a 5.0V supply, the control or select pins may be driven low to 0V and high to 5.5V. Driving the control or select pins Rail-toRail® minimizes power consumption.

## **Power -Supply Sequencing**

Proper power-supply sequencing is recommended for all CMOS devices. Always apply  $V_{CC}$  before applying signals to the input/output or control pins.

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#### **Notes:**

For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php

## **Ordering Information**

Ordering Code	Package Code	Package Type
PI5C3253CLEX	L	Pb-free & Green, 16-pin 150-mil wide plastic TSSOP

#### **Notes:**

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding an X suffix = Tape/Reel

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