

High Efficiency Snubber Diode

Features and Benefits

- High Peak Reverse Voltage, V_{RM}: 800 V
- Low Forward Voltage, V_F : 0.92 V (max.) at I_F = 1.5 A
- Peak Forward Surge Current, I_{FSM}: 100 A
- Average Forward Current, I_{F(AV)}: 1.2 A
- Flammability rating UL94V-0 (Equivalent)
- Pins Pb (lead) free

Package: Axial



Description

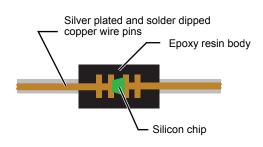
The SARS02 is an 800 V silicon diode designed especially for use in high-efficiency snubber circuits. This diode can sustain a high voltage with low loss, with low-noise rectification.

To suppress surge voltage, conduct the surge voltage and noise into a capacitor via a series resistor, $R_{\rm S}$. Then allow the capacitor to discharge the energy into power supply line with the regenerative circuit operation, shown below in the typical application circuit schematic.

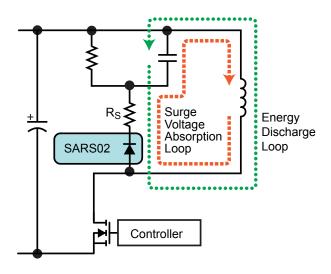
Applications

- White goods appliances
- Audio-visual equipment
- Light fixtures
- Communication equipment
- Factory automation

Product Structure



Typical Application



Selection Guide

Part Number	Packing*		
SARS02	1000 pieces per box, bulk		
SARS02V	3000 pieces per reel, 52 mm pitch axial taping		
SARS02V1	1000 pieces per box, 52 mm pitch axial taping		

^{*}See the Packing Options page for details on the packing orientation.

Absolute Maximum Ratings

Characteristic	Symbol	Conditions	Rating	Unit
Peak Reverse Surge Voltage	V _{RSM}		800	V
Peak Reverse Voltage	V _{RM}		800	V
Average Forward Current	I _{F(AV)}	Refer to figure 1	1.2	А
Peak Forward Surge Current	I _{FSM}	10 ms, half sine wave, one shot	100	А
I²t Limiting Value	I²t	1 ms < t < 10 ms	50	A ² ·s
Junction Temperature	T _j		-40 to 150	°C
Storage Temperature	T _{stg}		-40 to 150	°C

Design Notes

Use a series resistor (R_S in the typical application circuit schematic), and choose a value for the resistor such that the SARS02 diode saturates at junction temperature, $T_j \le 150$ °C.

Electrical Characteristics valid at T_A = 25°C, unless otherwise specified

Characteristic	Symbol	Test Conditions	Value	Unit
Forward Voltage	V _F	I _F = 1.5 A	0.92 (max)	V
Reverse Current	I _R	$V_R = V_{RM}$	10 (max)	μΑ
Reverse Current (High Temperature)	I _{R(H)}	$V_R = V_{RM}, T_j = 100^{\circ}C$	50 (max)	μΑ
Reverse Recovery Time	t _{rr}	$I_F = I_{RP} = 10 \text{ mA}, 90\% \text{ recovery point}, T_J = 25^{\circ}\text{C}$	2.0 to 18	μs
Thermal Resistance, Junction to Lead	$R_{\theta JL}$	Between junction and pin	15 (max)	°C/W

Forward Current Derating versus Ambient Temperature

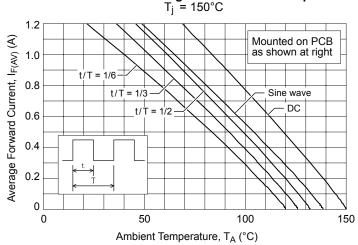
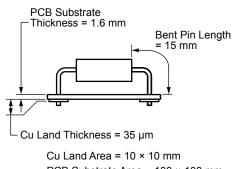


Figure 1. Derating Characteristics and Mounting Conditions



PCB Substrate Area = 180 × 100 mm

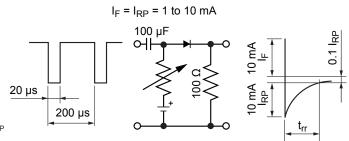
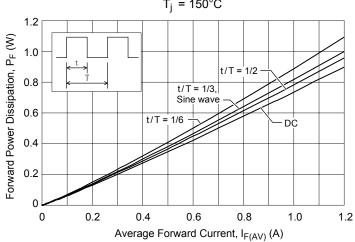


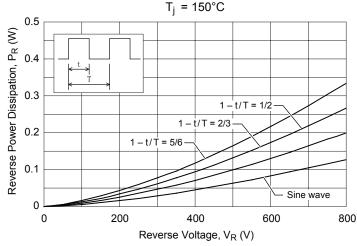
Figure 2. Definition of Peak Reverse Current, I_{RP}

Characteristic Performance

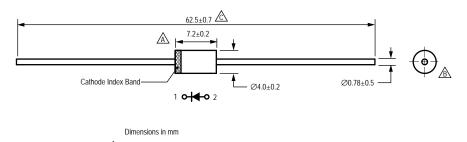
Forward Power Dissipation versus Average Forward Current T_j = 150°C



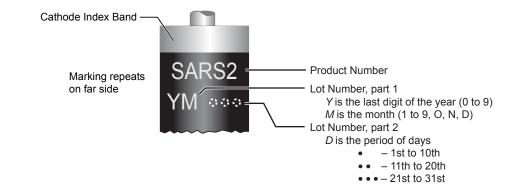
Reverse Power Dissipation versus Reverse Voltage



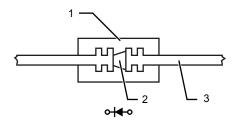
Package Outline



Package Marking



Material Composition and Internal Structure



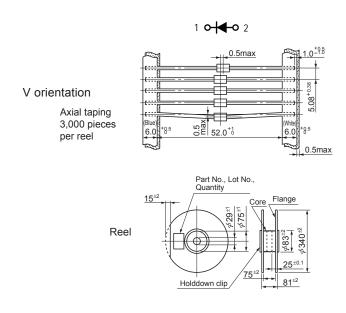
- 1. Body: Plastic, epoxy resin
- 2. Chip: Si
- 3. Pins: Cu wire
 Ag plating
 Solder dipped

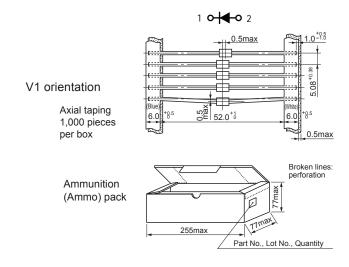
Weight: Approximately 0.4 g



Pin treatment Pb-free. Device composition compliant with the RoHS directive.

Packing Options





Dimensions in mm

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- In addition, it should be noted that since power devices or IC's including power devices have large self-heating value, the degree of derating of junction temperature affects the reliability significantly.
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