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SiM3L1xx Revision C Errata

Hardware Errata

Erratum #	Title	Impact
H1	Disabled VDD Monitor does not prevent flash write or erase operations	Minor

Impact definition: Each erratum is marked with an impact, as defined below:

- Minor – Workaround exists.
- Major – Errata that do not conform to the data sheet or standard.
- Information – The device behavior is not ideal but acceptable. Typically, the data sheet will be changed to match the device behavior.

Hardware Errata Details

H1. **Disabled VBAT Monitor does not prevent flash write or erase operations**

Description:
The SiM3L1xx Reference Manual states (revision 0.5, page 422):

Firmware cannot modify flash through the interface when the supply monitor in the VMONn module is disabled or disabled as a reset source (device reset source module). Any write or erase operations initiated while the supply monitor is disabled or disabled as a reset source will be ignored.

For devices with a trace code before 1312, flash modification may occur even if the VMON0 supply monitor is disabled or is disabled as a reset source.

Impacts:
Systems containing flash modification code running with the VMON0 supply monitor disabled or disabled as a reset source may experience inadvertent flash modification if the VBAT supply drops below the minimum Operating Supply Voltage on VBAT specified by the data sheet.

Workaround:
Devices with firmware modifying flash with write or erase operations using the FLASHCTRL module must ensure that the VMON0 supply monitor is enabled and is enabled as a reset source prior to executing the flash modification operation.

Resolution:
Devices with date codes later than 1312 will behave as described in revision 0.5 of the SiM3L1xx Reference Manual. These devices will ignore flash write and erase operations if the Supply Voltage Monitor (VMON0) is not enabled or is not enabled as a reset source.