TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS)

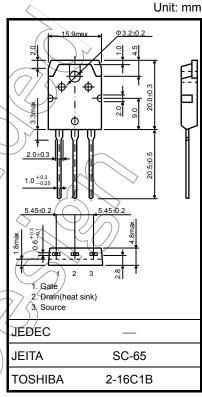
TK40J60T

Switching Regulator Applications

- Low drain-source ON-resistance: RDS (ON) = 0.068Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 25 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 100 \mu A (V_{DS} = 600 V)$
- Enhancement-mode: $V_{th} = 3.0 \text{ to } 5.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit | |
|--|------------------------------|-------------------|------------|-------------------|--|
| Drain-source voltage | | V_{DSS} | 600 | (y) | |
| Gate-source voltage | | V_{GSS} | ±30 | $(\wedge \wedge)$ | |
| Drain current | DC (Note 1) | I _D | 40 | // | |
| | Pulse (t = 1 ms) (Note 1) | I _{DP} | 80 | A | |
| Drain power dissipation (Tc = 25°C) | | P_{D} | 400 | W | |
| Single pulse avalanche energy (Note 2) | | EAS | 576 | mJ | |
| Avalanche current (Note 3) | | I _{AR} | 40 | << | |
| Repetitive avalanche energy | | EAR | 40 | μγ | |
| Channel temperature | | T _{ch} | 150 | °C | |
| Storage temperature range | | (T _{stg} | -55 to 150 | ∕\°C | |



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

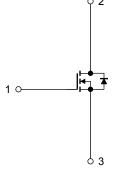
| Characteristics | Symbol | Max | Unit |
|--|------------------------|-------|------|
| Thermal resistance, channel to case | Rth (ch-c) | 0.313 | °C/W |
| Thermal resistance, channel to ambient | R _{th (ch-a)} | 50 | °C/W |

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25 °C (initial), L = 0.63 mH, R_G = 25 Ω , I_{AR} = 40 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



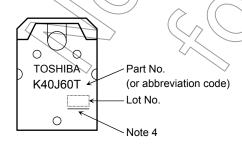
Electrical Characteristics (Ta = 25°C)

| Char | acteristics | Symbol | Test Condition | Min | Тур. | Max | Unit | |
|-----------------------------------|----------------|----------------------|---|-----------------|-------|------|------|--|
| Gate leakage cu | rrent | I _{GSS} | $V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | _ | ±1 | μА | |
| Drain cut-off curr | rent | I _{DSS} | V _{DS} = 600 V, V _{GS} = 0 V | _ | _ | 100 | μА | |
| Drain-source bre | akdown voltage | V (BR) DSS | I _D = 10 mA, V _{GS} = 0 V | 600 | _ | _ | ٧ | |
| Gate threshold v | oltage | V _{th} | V _{DS} = 10 V, I _D = 1 mA | 3.0 | _ | 5.0 | V | |
| Drain-source ON | l-resistance | R _{DS} (ON) | V _{GS} = 10 V, I _D = 20 A | (| 0:068 | 0.08 | Ω | |
| Forward transfer | admittance | Y _{fS} | V _{DS} = 10 V, I _D = 20 A | 6 | 25 | _ | S | |
| Input capacitance | | C _{iss} | | ()) | 3900 | _ | | |
| Reverse transfer | capacitance | C _{rss} | V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz | _ | 280 | _ | pF | |
| Output capacitance | | C _{oss} | | _ | 9200 | _ | | |
| Switching time | Rise time | t _r | 10 V ID = 20A VOUT VGS | _ | 60 | | | |
| | Turn-on time | t _{on} | R _L = 15Ω | | 120 | > — | ns | |
| | Fall time | t _f | V _{DD} ≈ 300 V | | 15 |) _ | 113 | |
| | Turn-off time | t _{off} | Duty ≨ 1%, t _W ≥ 10 μs | (\mathcal{I}) | 200 | _ | | |
| Total gate charge | e | Qg | | \sim | 67 | _ | | |
| Gate-source cha | rge | Q _{gs} | $V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$ |) — | 45 | _ | nC | |
| Gate-drain charge Q _{gd} | | | _ | 22 | _ | | | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

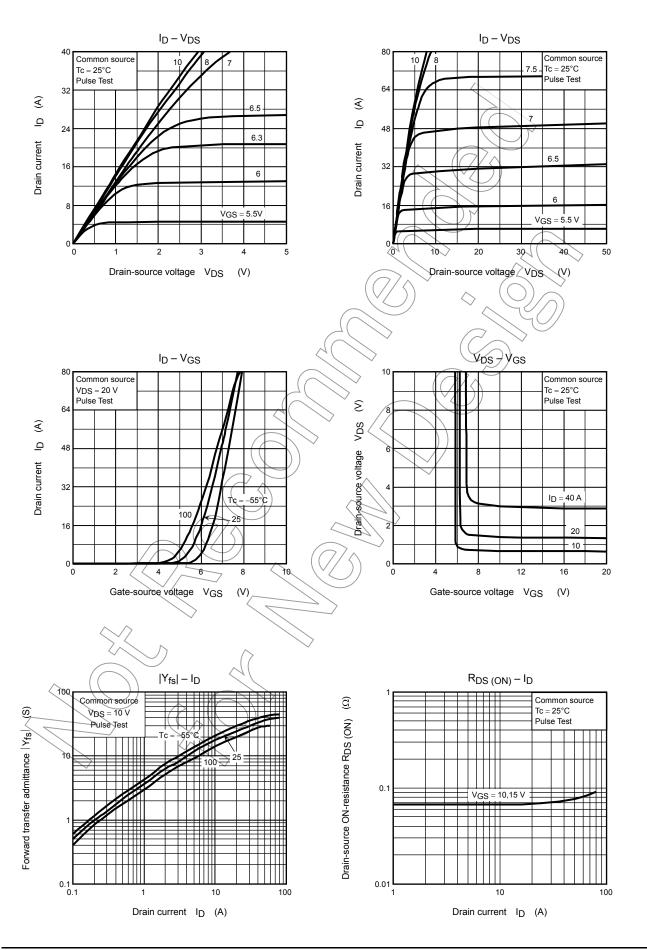
| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|------------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | | _ | _ | 40 | Α |
| Pulse drain reverse current (Note 1) | I _{DRP} | <u> </u> | _ | _ | 80 | Α |
| Forward voltage (diode) | VDSF | $1_{DR} = 40 \text{ A}, V_{GS} = 0 \text{ V}$ | _ | _ | -1.7 | V |
| Reverse recovery time | tìr | I _{DR} = 40 A, V _{GS} = 0 V, | _ | 550 | _ | ns |
| Reverse recovery charge | Q _{rr} | dl _{DR} /dt = 100 A/μs | | 14 | _ | μС |

Marking

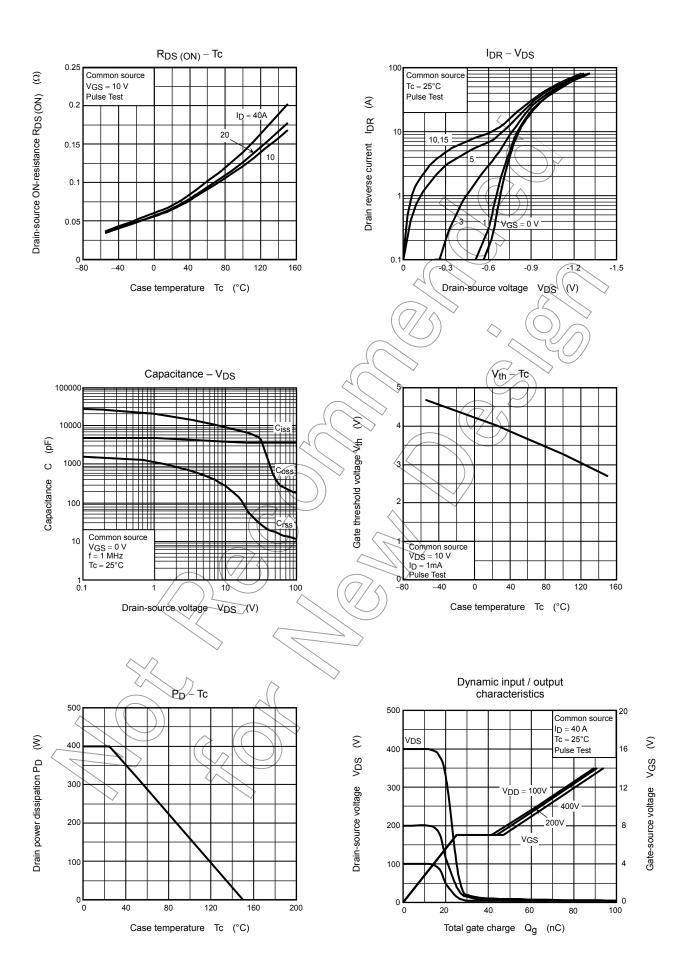


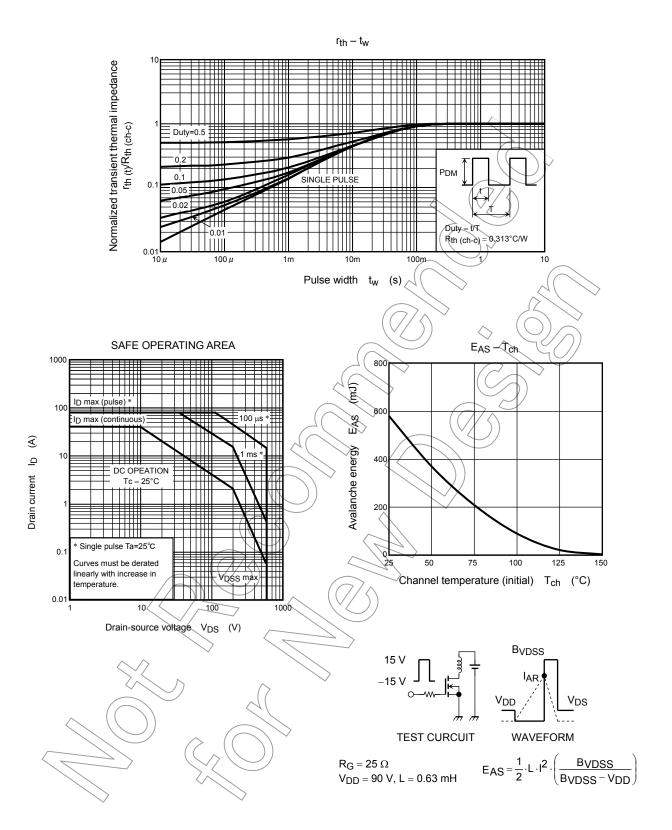
Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



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