

DS96F173M/DS96F175C/DS96F175M EIA-485/EIA-422 Quad Differential Receivers

Check for Samples: [DS96F173M/DS96F175C/DS96F175M](#)

FEATURES

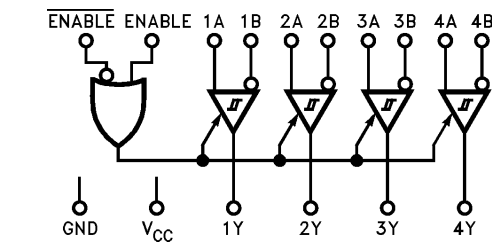
- Meets EIA-485, EIA-422A, EIA-423A Standards
- Designed for Multipoint Bus Applications
- Tri-State Outputs
- Common Mode Input Voltage Range: -7V to +12V
- Operates from Single +5.0V Supply
- Reduced Power Consumption ($I_{CC} = 50$ mA max)
- Input Sensitivity of ± 200 mV over Common Mode Range
- Input Hysteresis of 50 mV Typical
- High Input Impedance
- Military Temperature Range Available
- Qualified for MIL STD 883C
- Available to Standard Military Drawings (SMD)
- Available in CDIP(NFE), LCCC (NAJ), and CLGA (NAD) Packages
- DS96F173 and DS96F175 are Lead and Function Compatible with SN75173/175 or the AM26LS32/MC3486

DESCRIPTION

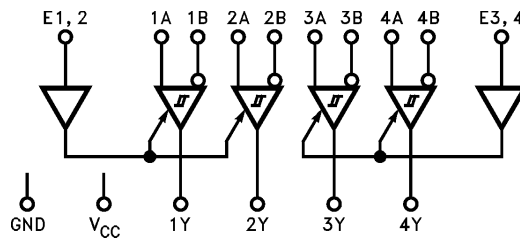
The DS96F173 and the DS96F175 are high speed quad differential line receivers designed to meet the EIA-485 standard. The DS96F173 and the DS96F175 offer improved performance due to the use of L-FAST bipolar technology. The use of LFAST technology allows the DS96F173 and DS96F175 to operate at higher speeds while minimizing power consumption.

The DS96F173 and the DS96F175 have tri-state outputs and are optimized for balanced multipoint data bus transmission at rates up to 15 Mbps. The receivers feature high input impedance, input hysteresis for increased noise immunity, and input sensitivity of 200 mV over a common mode input voltage range of -7V to +12V. The receivers are therefore suitable for multipoint applications in noisy environments. The DS96F173 features an active high and active low Enable, common to all four receivers. The DS96F175 features separate active high Enables for each receiver pair.

Logic Diagrams



DS96F173



DS96F175



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

All trademarks are the property of their respective owners.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of the Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

Copyright © 2000–2013, Texas Instruments Incorporated

Function Tables

Table 1. (Each Receiver) DS96F173⁽¹⁾

Differential Inputs	Enable		Output
A–B	E	$\bar{E}$	Y
$V_{ID} \geq 0.2V$	H	X	H
	X	L	H
$V_{ID} \leq -0.2V$	H	X	L
	X	L	L
X	L	X	Z
X	X	H	Z

- (1) H = High Level
 L = Low Level
 Z = High Impedance (off)
 X = Don't Care

Table 2. (Each Receiver) DS96F175⁽¹⁾

Differential Inputs	Enable	Output
A–B	E	Y
$V_{ID} \geq 0.2V$	H	H
$V_{ID} \leq -0.2V$	H	L
X	L	Z

- (1) H = High Level
 L = Low Level
 Z = High Impedance (off)
 X = Don't Care



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings COMMERCIAL ⁽¹⁾⁽²⁾

Storage Temperature Range (T _{STG})		–65°C to +175°C
Lead Temperature (Soldering, 60 sec.)		300°C
Max. Package Power Dissipation ⁽³⁾ at 25°C	Ceramic CDIP (NFE)	1500 mW
	Supply Voltage	7.0V
	Input Voltage, A or B Inputs	±25V
	Differential Input Voltage	±25V
	Enable Input Voltage	7.0V
	Low Level Output Current	50 mA

- (1) Absolute Maximum Ratings are those values beyond which the safety of the device cannot be ensured. They are not meant to imply that the devices should be operated at these limits. The tables of [Electrical Characteristics](#) provide conditions for actual device operation.
- (2) Specifications for the 883 version of this product are listed separately.
- (3) Derate package 10 mW/°C above 25°C.

Recommended Operating Conditions

	Min	Typ	Max	Units
Supply Voltage (V _{CC}) DS96F175C	4.75	5.0	5.25	V
Common Mode Input Voltage (V _{CM})	–7		+12	V
Differential Input Voltage (V _{ID})			12	V
Output Current HIGH (I _{OH})			–400	µA
Output Current LOW (I _{OL})			11	mA
Operating Temperature (T _A) DS96F175C	0	25	70	°C

Electrical Characteristics ⁽¹⁾⁽²⁾

Over recommended supply voltage and operating temperature ranges, unless otherwise specified

Symbol	Parameter	Conditions		Min	Typ	Max	Units
V _{TH}	Differential-Input High Threshold Voltage	V _O = V _{OH}				0.2	V
V _{TL}	Differential-Input ⁽³⁾ Low Threshold Voltage	V _O = V _{OL}		–0.2			V
V _{TH} – V _{TL}	Hysteresis ⁽⁴⁾	V _{CM} = 0V			50		mV
V _{IH}	Enable Input Voltage HIGH			2.0			V
V _{IL}	Enable Input Voltage LOW					0.8	V
V _{IC}	Enable Input Clamp Voltage	I _I = –18 mA				–1.5	V
V _{OH}	Output Voltage HIGH	V _{ID} = 200 mV I _{OH} = –400 µA	0°C to +70°C	2.8			V
			–55°C to +125°C	2.5			
V _{OL}	Output Voltage LOW	V _{ID} = –200 mV	I _{OL} = 8.0 mA			0.45	V
			I _{OL} = 11 mA			0.50	
I _{OZ}	High-Impedance State Output	V _O = 0.4V to 2.4V				±20	µA
I _I	Line Input Current ⁽⁵⁾	Other Input = 0V	V _I = 12V			1.0	mA
			V _I = –7.0V			–0.8	
I _{IH}	Enable Input Current HIGH	V _{IH} = 2.7V				20	µA

- (1) Unless otherwise specified min/max limits apply across the 0°C to +70°C range for the DS96F175C. All typicals are given for V_{CC} = 5V and T_A = 25°C.
- (2) All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are reference to ground unless otherwise specified.
- (3) The algebraic convention, when the less positive (more negative) limit is designated minimum, is used in this data sheet for common mode input voltage and threshold voltage levels only.
- (4) Hysteresis is the difference between the positive-going input threshold voltage, V_{TH}, and the negative going input threshold voltage, V_{TL}.
- (5) Refer to EIA-485 Standard for exact conditions.

Electrical Characteristics⁽¹⁾⁽²⁾ (continued)

Over recommended supply voltage and operating temperature ranges, unless otherwise specified

Symbol	Parameter	Conditions		Min	Typ	Max	Units
I_{IL}	Enable Input Current LOW	$V_{IL} = 0.4V$				–100	μA
R_I	Input Resistance			14	18	22	$k\Omega$
I_{OS}	Short Circuit Output Current	See ⁽⁶⁾		–15		–85	mA
I_{CC}	Supply Current	No Load	Outputs Enabled			50	mA
I_{CCX}			Outputs Disabled			50	

(6) Only one output at a time should be shorted.

COMMERCIAL Switching Characteristics

 $V_{CC} = 5.0V$, $T_A = 25^{\circ}C$

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t_{PLH}	Propagation Delay Time, Low to High Level Output	$V_{ID} = -2.5V$ to $+2.5V$, $C_L = 15$ pF, See Figure 7	5.0	15	22	ns
t_{PHL}	Propagation Delay Time, High to Low Level Output	$V_{CM} = 0V$	5.0	15	22	ns
t_{ZH}	Output Enable Time to High Level	$C_L = 15$ pF, See Figure 8		12	16	ns
t_{ZL}	Output Enable Time to Low Level	$C_L = 15$ pF, See Figure 9		13	18	ns
t_{HZ}	Output Disable Time from High Level	$C_L = 5.0$ pF, See Figure 8		14	20	ns
t_{LZ}	Output Disable Time from Low Level	$C_L = 5.0$ pF, See Figure 9		14	18	ns
$ t_{PLH} - t_{PHL} $	Pulse Width Distortion (SKEW)	See Figure 7		1.0	3.0	ns

Absolute Maximum Ratings MIL-STD-883C ⁽¹⁾⁽²⁾

Storage Temperature Range (T_{STG})		$-65^{\circ}C$ to $+175^{\circ}C$
Lead Temperature (Soldering, 60 sec.)		$300^{\circ}C$
Max. Package Power Dissipation ⁽³⁾ at $25^{\circ}C$	Ceramic CDIP (NFE)	1500 mW
	Ceramic CLGA (NAD)	1034 mW
	Ceramic LCCC (NAJ)	1500 mW
	Supply Voltage	7.0V
	Input Voltage, A or B Inputs	$\pm 25V$
	Differential Input Voltage	$\pm 25V$
	Enable Input Voltage	7.0V
	Low Level Output Current	50 mA

(1) Absolute Maximum Ratings are those values beyond which the safety of the device cannot be ensured. They are not meant to imply that the devices should be operated at these limits. The tables of [Electrical Characteristics](#) provide conditions for actual device operation.

(2) For complete Military Specifications, refer to the appropriate SMD or MDS.

(3) Above $T_A = 25^{\circ}C$ derate NFE package 10 mW/ $^{\circ}C$, NAD package 6.90 mW/ $^{\circ}C$, NAJ package 11.11 mW/ $^{\circ}C$.

Recommended Operating Conditions

	Min	Typ	Max	Units
Supply Voltage (V_{CC}) DS96F173M/DS96F175M	4.50	5.0	5.50	V
Common Mode Input Voltage (V_{CM})	-7		+12	V
Differential Input Voltage (V_{ID})			12	V
Output Current HIGH (I_{OH})			-400	μA
Output Current LOW (I_{OL})			11	mA
Operating Temperature (T_A) DS96F173M/DS96F175M	-55	25	125	$^{\circ}C$

Electrical Characteristics⁽¹⁾⁽²⁾

Over recommended supply voltage and operating temperature ranges, unless otherwise specified

Symbol	Parameter	Conditions		Min	Max	Units
V _{TH}	Differential-Input High Threshold Voltage	V _{CC} = 4.5V, 5.5V V _{CM} = 0V, 12V, -12V			0.2	V
V _{TL}	Differential-Input Low Threshold Voltage ⁽³⁾	V _{CC} = 4.5V, 5.5V V _{CM} = 0V, 12V, -12V		-0.2		V
V _{IH}	Enable Input Voltage HIGH			2.0		V
V _{IL}	Enable Input Voltage LOW				0.8	V
V _{IC}	Enable Input Clamp Voltage	I _I = -18 mA, V _{CC} = 4.5V			-1.5	V
V _{OH}	Output Voltage HIGH	V _{ID} = 200 mV I _{OH} = -400 μA	-55°C to +125°C	2.5		V
V _{OL}	Output Voltage LOW	V _{ID} = -200 mV	I _{OL} = 8.0 mA		0.45	V
I _{OZ}	High-Impedance State Output	V _O = 0.4V, 2.4V, V _{CC} = 5.5V			±20	μA
I _I	Line Input Current ⁽⁴⁾	Other Input = 0V	V _I = 12V		1.0	mA
			V _I = -7.0V		-0.8	
I _{IH}	Enable Input Current HIGH	V _{IH} = 2.7V, V _{CC} = 5.5V			20	μA
I _{IL}	Enable Input Current LOW	V _{IL} = 0.4V, V _{CC} = 5.5V			-100	μA
R _I	Input Resistance			10		kΩ
I _{OS}	Short Circuit Output Current	See ⁽⁵⁾		-15	-85	mA
I _{CC}	Supply Current	No Load	Outputs Enabled or Disabled		50	mA
I _{CCX}						

- (1) Unless otherwise specified min/max limits apply across the 0°C to +70°C range for the DS96F175C. All typicals are given for V_{CC} = 5V and T_A = 25°C.
- (2) All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are reference to ground unless otherwise specified.
- (3) The algebraic convention, when the less positive (more negative) limit is designated minimum, is used in this data sheet for common mode input voltage and threshold voltage levels only.
- (4) Refer to EIA-485 Standard for exact conditions.
- (5) Only one output at a time should be shorted.

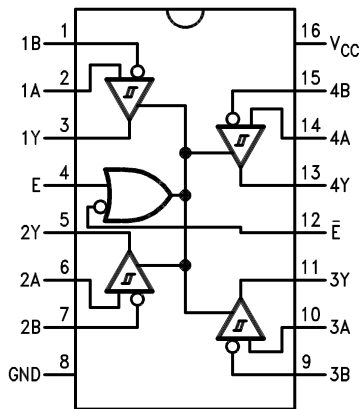
MIL-STD-883C Switching Characteristics

$V_{CC} = 5.0V$

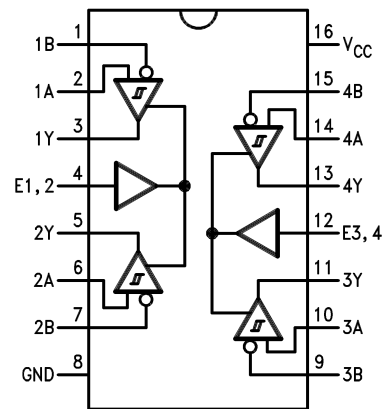
Symbol	Parameter	Conditions	$T_A = 25^\circ C$		$T_A = -55^\circ C$	$T_A = 125^\circ C$	Units
			Typ	Max	Max	Max	
t_{PLH}	Propagation Delay Time, Low to High Level Output	$V_{ID} = -2.5V$ to $+2.5V$, $C_L = 15$ pF, See Figure 7	15	22	30	30	ns
t_{PHL}	Propagation Delay Time, High to Low Level Output	$V_{CM} = 0V$	15	22	30	30	ns
t_{ZH}	Output Enable Time to High Level	$C_L = 15$ pF, See Figure 8	12	16	27	27	ns
t_{ZL}	Output Enable Time to Low Level	$C_L = 15$ pF, See Figure 9	13	18	27	27	ns
t_{HZ}	Output Disable Time from High Level	$C_L = 5.0$ pF, See Figure 8 ⁽¹⁾	14	20	27	27	ns
		$C_L = 20$ pF, See Figure 8 ⁽¹⁾	14	30	37	37	ns
t_{LZ}	Output Disable Time from Low Level	$C_L = 5.0$ pF, See Figure 9	14	18	30	30	ns
$ t_{PLH} - t_{PHL} $	Pulse Width Distortion (SKEW)	See Figure 7	1	3	5.0	5.0	ns

(1) Testing at 20 pF assures conformance to 5 pF specification.

Connection Diagrams

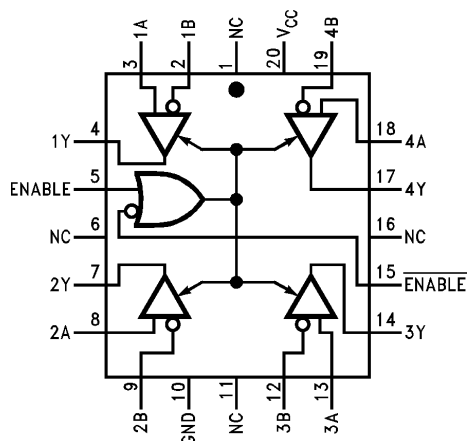


**Figure 1. DS96F173 (Top View)⁽²⁾
16-Lead Ceramic Dual-In-Line CDIP Package
Package Number NFE**



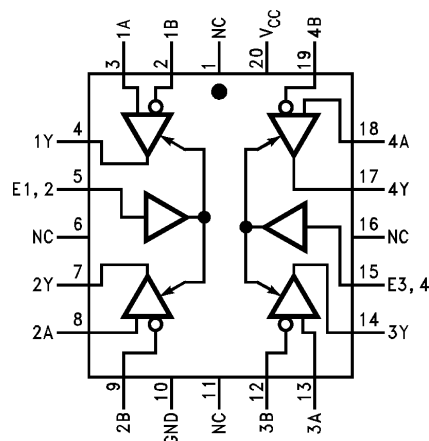
**Figure 2. DS96F175 (Top View)⁽²⁾
16-Lead Ceramic Dual-In-Line CDIP Package
Package Number NFE**

(2) DS96F173 with active high and active low Enables are shown. DS96F175 has active high Enable only.



*NC—No Connection

Figure 3. 20-Lead Ceramic Leadless Chip Carrier - LCCC (Top View)
See Package Number NAJ



*NC—No Connection

Figure 4. 20-Lead Ceramic Leadless Chip Carrier - LCCC (Top View)
See Package Number NAJ

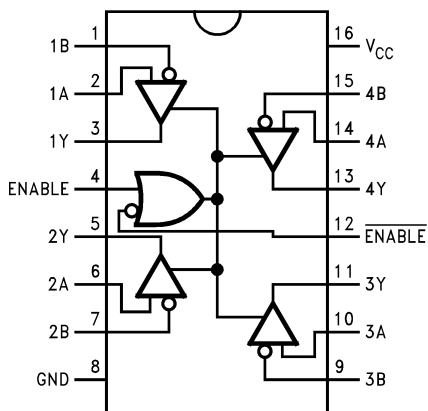


Figure 5. 16-Lead Ceramic Flatpak - CLGA (Top View)
See Package Number NAD

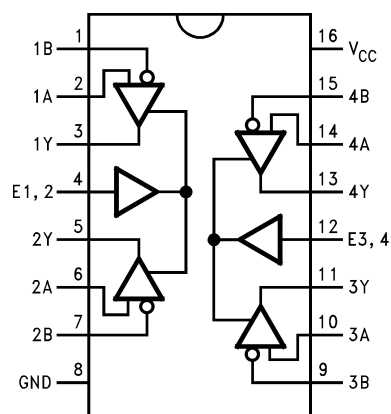


Figure 6. 16-Lead Ceramic Flatpak - CLGA (Top View)
See Package Number - NAD

Parameter Measurement Information

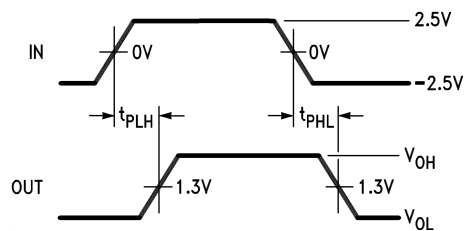
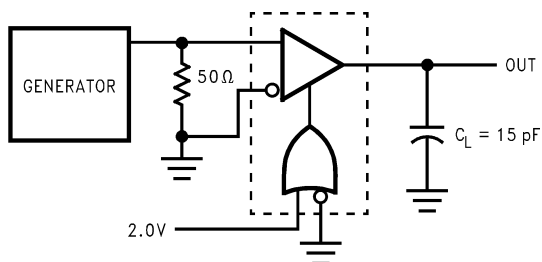


Figure 7. t_{PLH} , t_{PHL} (3)(4)

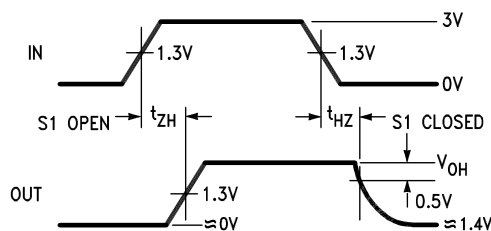
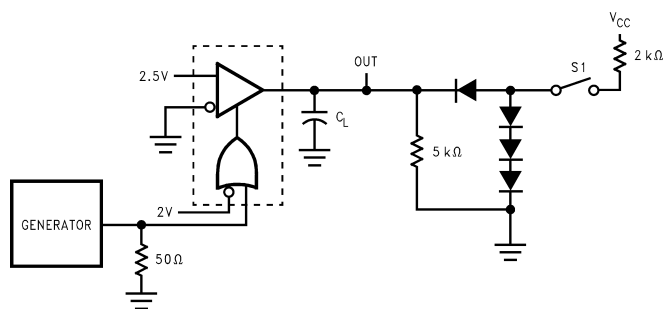


Figure 8. t_{HZ} , t_{ZH} (3)(4)(5)(6)

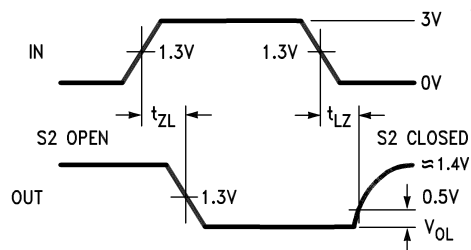
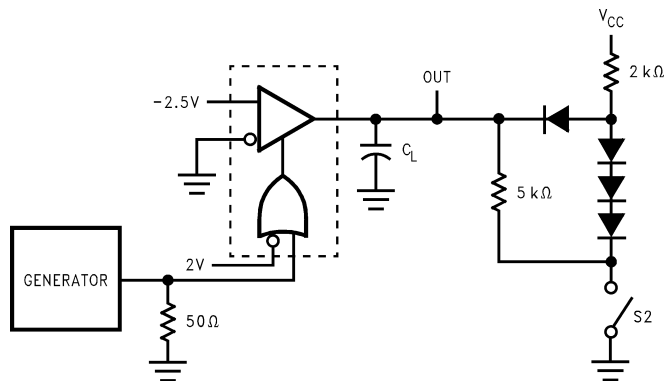
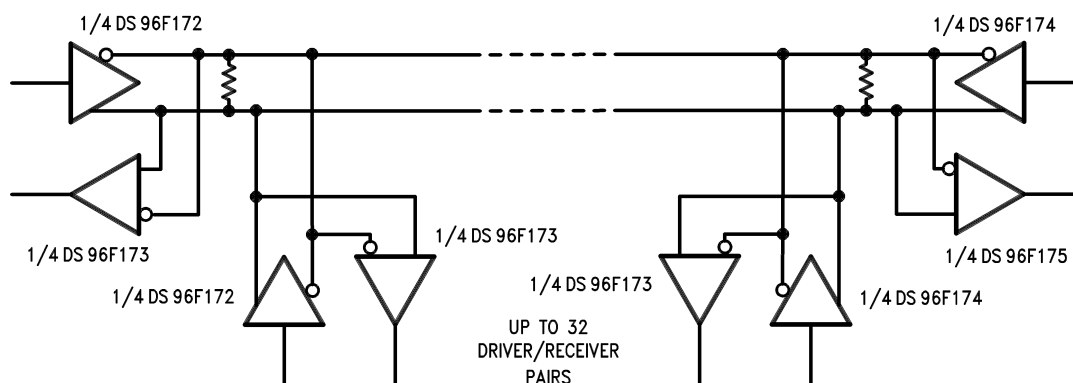


Figure 9. t_{ZL} , t_{LZ} (3)(4)(5)(6)

- (3) The input pulse is supplied by a generator having the following characteristics: $f = 1.0$ MHz, 50% duty cycle, $t_r \leq 6.0$ ns, $t_f \leq 6.0$ ns, $Z_O = 50\Omega$.
- (4) C_L includes probe and stray capacitance
- (5) All diodes are 1N916 or equivalent
- (6) To test the active low Enable E of DS96F173, ground E and apply an inverted input waveform to E. DS96F175 has active high enable only.

Typical Application



NOTE

The line length should be terminated at both ends in its characteristic impedance. Stub lengths off the main line should be kept as short as possible.

REVISION HISTORY

Changes from Revision E (April 2013) to Revision F

Page

- Changed layout of National Data Sheet to TI format [10](#)

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Products

Audio	www.ti.com/audio
Amplifiers	amplifier.ti.com
Data Converters	dataconverter.ti.com
DLP® Products	www.dlp.com
DSP	dsp.ti.com
Clocks and Timers	www.ti.com/clocks
Interface	interface.ti.com
Logic	logic.ti.com
Power Mgmt	power.ti.com
Microcontrollers	microcontroller.ti.com
RFID	www.ti-rfid.com
OMAP Applications Processors	www.ti.com/omap
Wireless Connectivity	www.ti.com/wirelessconnectivity

Applications

Automotive and Transportation	www.ti.com/automotive
Communications and Telecom	www.ti.com/communications
Computers and Peripherals	www.ti.com/computers
Consumer Electronics	www.ti.com/consumer-apps
Energy and Lighting	www.ti.com/energy
Industrial	www.ti.com/industrial
Medical	www.ti.com/medical
Security	www.ti.com/security
Space, Avionics and Defense	www.ti.com/space-avionics-defense
Video and Imaging	www.ti.com/video

TI E2E Community

e2e.ti.com