





DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features

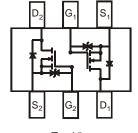
- Dual N-Channel MOSFET
- Low On-Resistance (1.0V max)
- · Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected up to 2kV
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.006 grams (approximate)







Top View

Top View Internal Schematic

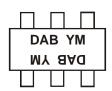
Ordering Information (Note 3)

Part Number	Case	Packaging
DMN5L06DWK-7	SOT363	3000/Tape & Reel

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



DAB = Marking Code YM = Date Code Marking Y = Year ex: T = 2006 M = Month ex: 9 = September

Date Code Key

Year	200	6	2007		2008	20	09	2010		2011	2	2012
Code	T		U		V	V	V	Χ		Υ		Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Char	acteristic	Symbol	Value	Unit
Drain Source Voltage		V_{DSS}	50	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current (Note 4)	Continuous Pulsed (Note 5)	I _D	305 800	mA

Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 4)	P _D	250	mW
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

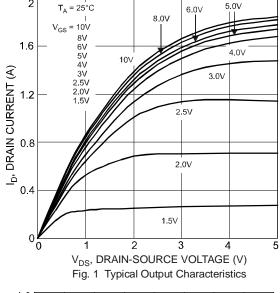
Electrical Characteristics @T_A = 25°C unless otherwise specified

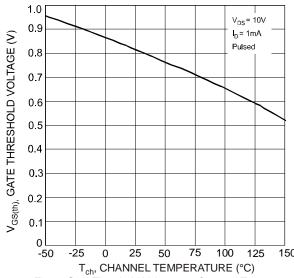
Characteristic			Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		BV _{DSS}	50	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	@ T _C = 25°C	I _{DSS}	_	_	60	nA	$V_{DS} = 50V$, $V_{GS} = 0V$
					1	μΑ	$V_{GS} = \pm 12V, V_{DS} = 0V$
Gate-Body Leakage		I _{GSS}	_	_	500	nA	$V_{GS} = \pm 10V$, $V_{DS} = 0V$
					50	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage		V _{GS(th)}	0.49	_	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
			_	_	3.0		$V_{GS} = 1.8V, I_D = 50mA$
Static Drain-Source On-Resistance		R _{DS (ON)}	_	_	2.5	Ω	$V_{GS} = 2.5V, I_D = 50mA$
			_	_	2.0		$V_{GS} = 5.0V, I_D = 50mA$
On-State Drain Current		I _{D(ON)}	0.5	1.4	_	Α	$V_{GS} = 10V, V_{DS} = 7.5V$
Forward Transconductance		Y _{fs}	200	_	_	mS	$V_{DS} = 10V, I_{D} = 0.2A$
Source-Drain Diode Forward Voltage		V_{SD}	0.5	_	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{iss}	_		50	pF	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Output Capacitance		Coss			25	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance		Crss	_	_	5.0	pF	T = T.UIVII IZ

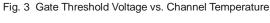
- Notes: 4. Device mounted on FR-4 PCB.

 - 5. Pulse width ≤10µS, Duty Cycle ≤1%.
 6. Short duration pulse test used to minimize self-heating effect.









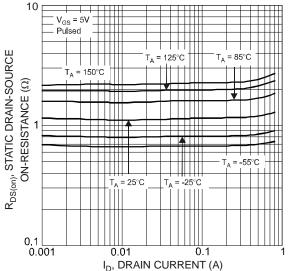
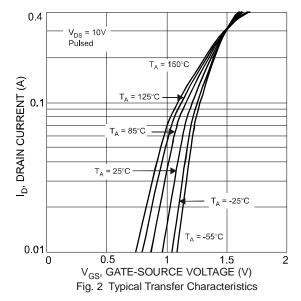


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current



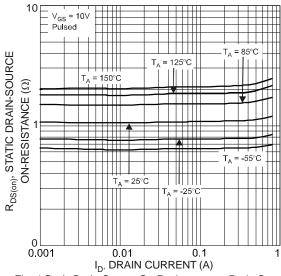


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

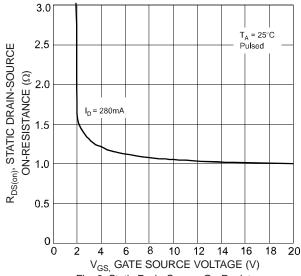


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage



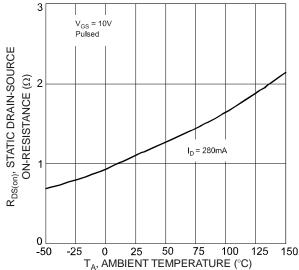


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

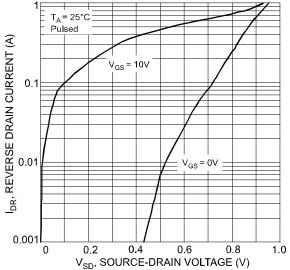
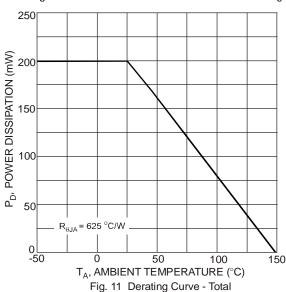


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage



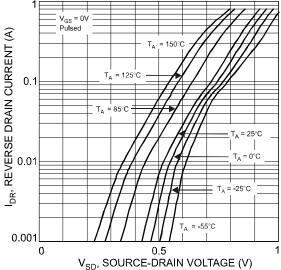


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

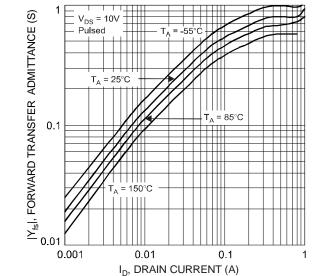
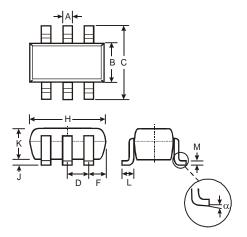


Fig.10 Forward Transfer Admittance vs. Drain Current

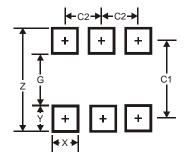


Package Outline Dimensions



SOT363						
Dim	Min	Max				
Α	0.10	0.30				
В	1.15	1.35				
C	2.00	2.20				
D	0.65 Typ					
F	0.40	0.45				
Н	1.80	2.20				
J	0	0.10				
K	0.90	1.00				
L	0.25	0.40				
M	0.10	0.22				
α	0°	8°				
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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