

nichicon

The diagram illustrates the evolution of probe tips. At the top, three icons are shown: a standard tip labeled 'For SMD', a smaller tip labeled 'Smaller', and a tip with a hook-like feature labeled 'Anti-Solvent Feature'. Below these, a large downward arrow labeled 'Smaller' points to a box containing 'ZT'. Another large downward arrow labeled 'Smaller' points from 'ZT' to 'ZG'.

- Chip type with 4.5mm height, operating over wide temperature range of -40 to $+105^{\circ}\text{C}$.
- Designed for surface mounting on high density PC board.
- Applicable to automatic mounting machine fed with carrier tape.
- Compliant to the RoHS directive (2011/65/EU).

[illegible]

105°C Marking
Capacitance
Lot No.

⊕ Positive
⊖ Negative

※Voltage

Plastic platform

0.3 MAX.

C±0.2

0.5 MAX.

⊕ Positive

⊖ Negative

φD±0.5

4.5^{+0.1}_{-0.2}

B±0.2

E

A±0.3

0.5 to 0.8

	(mm)		
φD	4	5	6.3
A	1.8	2.1	2.4
B	4.3	5.3	6.6
C	4.3	5.3	6.6
E	1.0	1.3	2.2

※Voltage mark for 6.3V is 「6V」.

1 2 3 4 5 6 7 8 9 10 11 12 13 14

U Z T 1 C 1 0 0 M C L 1 G B

Type

Series name

Rated voltage (16V)

Rated capacitance (10µF)

Capacitance tolerance (±20%)

Configuration

Taping code

V Cap. (µF)		6.3		10		16		25		35		50	
		0J		1A		1C		1E		1V		1H	
0.1	0R1											4	0.9
0.22	R22											4	2.2
0.33	R33											4	2.8
0.47	R47											4	3.3
1	010											4	5.4
2.2	2R2											4	9.6
3.3	3R3											4	12
4.7	4R7							4	11	4	13	5	16
10	100					4	16	5	20	5	22	6.3	26
22	220	4	19	5	24	5	26	6.3	33	6.3	36		
33	330	5	26	5	30	6.3	35	6.3	42				
47	470	5	32	6.3	40	6.3	44						
100	101	6.3	52									Case size A-D (mm)	Rated temp.

Frequency	50 Hz	120 Hz	300 Hz	1 kHz	10 kHz or more
Coefficient	0.70	1.00	1.17	1.36	1.50

- Taping specifications are given in page 23.
- Recommended land size, soldering by reflow are given in page 18, 19.
- Please select UX(p.158), UJ(p.164) series if high C/V products are required.
- Please refer to page 3 for the minimum order quantity.

CAT.8100C