

## 1. Scope

The present specifications shall apply to an FMB-24.

## 2. Outline

|              |                                |
|--------------|--------------------------------|
| Type         | Silicon Schottky Barrier Diode |
| Structure    | Resin Molded                   |
| Applications | High Frequency Rectification   |

## 3. Flammability

UL94V-0(Equivalent)

## 4. Absolute maximum ratings

| No. | Item                           | Symbol      | Unit        | Rating     | Conditions                 |
|-----|--------------------------------|-------------|-------------|------------|----------------------------|
| 1   | Transient Peak Reverse Voltage | $V_{RSM}$   | V           | 48         |                            |
| 2   | Peak Reverse Voltage           | $V_{RM}$    | V           | 40         |                            |
| 3   | Average Forward Current        | $I_{F(AV)}$ | A           | 4.0        | Refer to Derating of 7     |
| 4   | Peak Surge Forward Current     | $I_{FSM}$   | A           | 50         | Half sinewave, one shot    |
| 5   | $I^2t$ Limiting Value          | $I^2t$      | $A^2s$      | 12.5       | $1msec \leq t \leq 10msec$ |
| 6   | Junction Temperature           | $T_j$       | $^{\circ}C$ | -40 ~ +150 |                            |
| 7   | Storage Temperature            | $T_{stg}$   | $^{\circ}C$ | -40 ~ +150 |                            |

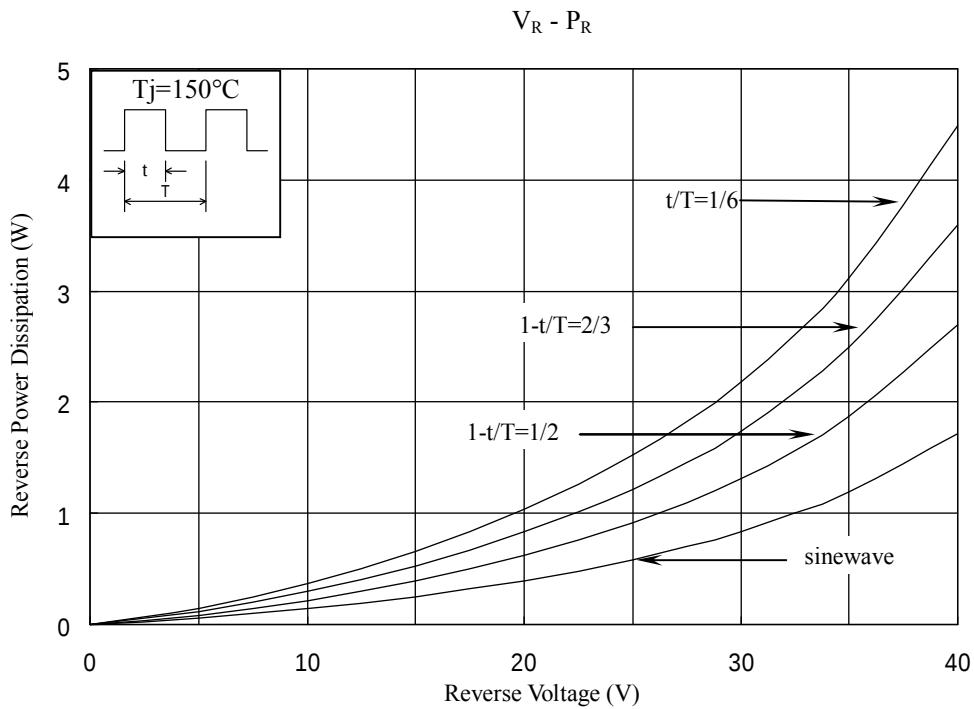
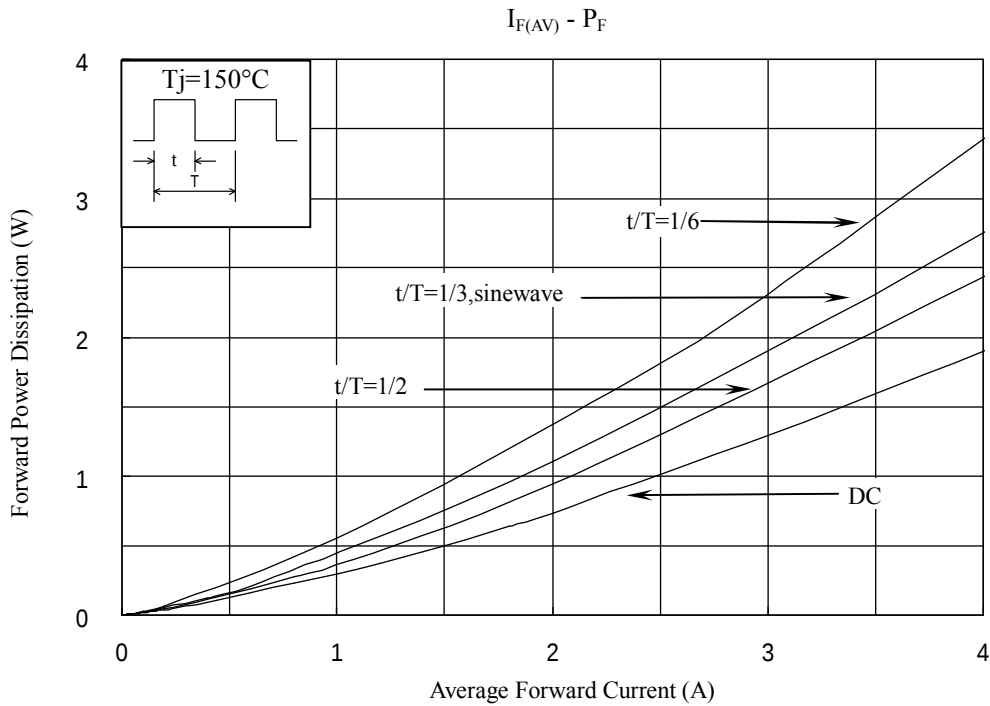
No.1,2,4&5 show ratings per one chip.

5. Electrical characteristics ( $T_a=25^{\circ}C$  , unless otherwise specified)

| No. | Item                                           | Symbol        | Unit          | Value     | Conditions                     |
|-----|------------------------------------------------|---------------|---------------|-----------|--------------------------------|
| 1   | Forward Voltage Drop                           | $V_F$         | V             | 0.55 max. | $I_F=2.0A$                     |
| 2   | Reverse Leakage Current                        | $I_R$         | mA            | 2.0 max.  | $V_R=V_{RM}$                   |
| 3   | Reverse Leakage Current Under High Temperature | $H-I_R$       | mA            | 20 max.   | $V_R=V_{RM}, T_j=125^{\circ}C$ |
|     |                                                |               |               | 70 max.   | $V_R=V_{RM}, T_j=150^{\circ}C$ |
| 4   | Thermal Resistance                             | $R_{th(j-c)}$ | $^{\circ}C/W$ | 4.0 max.  | Between Junction and case      |

No.1,2,&3 show characteristics per one chip.

6. Characteristics



7. Derating

