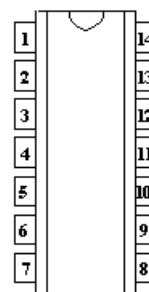
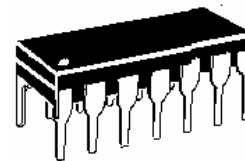


DESCRIPTION

These low capacitance diode arrays are multiple, discrete, isolated junctions fabricated by a planar process and mounted in a 14-PIN package for use as steering diodes protecting up to eight I/O ports from ESD, EFT, or surge by directing them either to the positive side of the power supply line or to ground (see figure 1). An external TVS diode may be added between the positive supply line and ground to prevent overvoltage on the supply rail. They may also be used in fast switching core-driver applications. This includes computers and peripheral equipment such as magnetic cores, thin-film memories, plated-wire memories, etc., as well as decoding or encoding applications. These arrays offer many advantages of integrated circuits such as high-density packaging and improved reliability. This is a result of fewer pick and place operations, smaller footprint, smaller weight, and elimination of various discrete packages that may not be as user friendly in PC board mounting. They are available with either Tin-Lead plating terminations or as RoHS Compliant with annealed matte-Tin finish by adding an "e3" suffix to the part number.

APPEARANCE


Top Viewing Pin Layout

IMPORTANT: For the most current data, consult MICROSEMI's website: <http://www.microsemi.com>

FEATURES

- 16 Diode Array / protects 8 lines
- Molded 14-Pin Dual-In-Line Package
- Low Capacitance 1.5 pF per diode
- Switching speeds less than 5 ns
- RoHS compliant devices available by adding "e3" suffix
- IEC 61000-4 compatible
 - 61000-4-2 (ESD): Air 15 kV, contact 8 kV
 - 61000-4-4 (EFT): 40 A, 5/50 ns
 - 61000-4-5 (surge): 12 A, 8/20 μ s

APPLICATIONS / BENEFITS

- Low capacitance steering diode protection for high frequency data lines
- RS-232 & RS-422 Interface Networks
- Ethernet: 10 Base T
- Computer I / O Ports
- LAN
- Switching Core Drivers

MAXIMUM RATINGS

- Operating Temperature: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Forward Surge Current: 2 Amps (8.3 ms)
12 Amps (8/20 μ s)
- Continuous Forward Current: 400 mA (one diode)
- Power Dissipation (P_D): 1500 mW (total)
- Solder temperatures: 260°C for 10 s (maximum)

MECHANICAL AND PACKAGING

- CASE: Void-free transfer molded thermosetting epoxy body meeting UL94V-0 flammability classification
- TERMINALS: Tin-Lead or RoHS Compliant annealed matte-Tin plating solderable per MIL-STD-750 method 2026
- MARKING: MSC logo, MAD1103 or MAD1103e3 and date code. Pin #1 is to the left of the dot or indent on top of package.
- WEIGHT: 0.997 grams (approximate)
- Carrier tubes: 25 pcs (Standard)

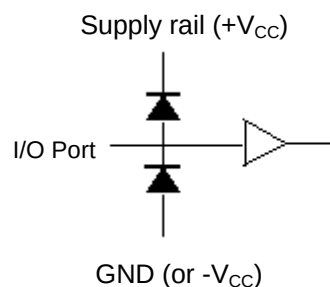
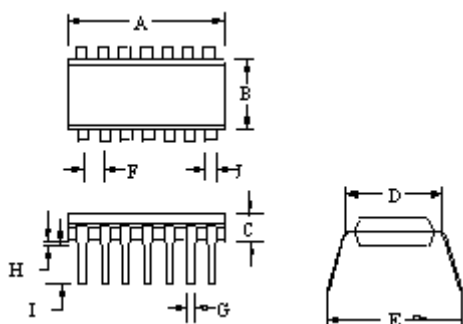
ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless otherwise specified

PART NUMBER	BREAKDOWN VOLTAGE V_{BR} @ $I_{BR} = 100\mu A$	WORKING PEAK REVERSE VOLTAGE V_{RWM} V	LEAKAGE CURRENT I_R $T_A = 25^\circ C$		LEAKAGE CURRENT I_R $T_A = 150^\circ C$		CAPACITANCE C @ 0 V	REVERSE RECOVERY TIME t_{rr}	FORWARD VOLTAGE V_F $I_F = 10$ mA	FORWARD VOLTAGE V_F $I_F = 100$ mA
	V	V	μA		μA		pF	ns	V	V
	MIN	MAX	MAX	@ V_R	MAX	@ V_R	TYP	MAX	MAX	MAX
MAD1103 MAD1103e3	90	75	0.200	20	300	20	1.5	5.0	1.00	1.20

SYMBOLS & DEFINITIONS

Symbol	Definition
V_{BR}	Minimum Breakdown Voltage: The minimum voltage the device will exhibit at a specified current.
V_{RWM}	Working Peak Reverse Voltage: The maximum peak voltage that can be applied over the operating temperature range.
V_F	Maximum Forward Voltage: The maximum forward voltage the device will exhibit at a specified current.
I_R	Maximum Leakage Current: The maximum leakage current that will flow at the specified voltage and temperature.
C	Capacitance: The capacitance of the TVS as defined @ 0 volts at a frequency of 1 MHz and stated in picofarads.

OUTLINE AND CIRCUIT

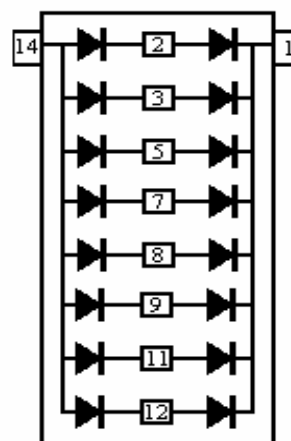


STEERING DIODE APPLICATION

figure 1

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.740	0.780	18.80	19.81
B	0.235	0.265	5.969	6.731
C	0.120	0.140	3.048	3.556
D	0.270	0.330	6.858	8.382
E	0.320	0.380	8.128	9.652
F	0.100 BSC		2.540 BSC	
G	0.015	0.021	0.381	0.533
H	0.017	0.023	0.431	0.584
I	0.140	0.160	3.556	4.064
J	0.040	0.070	1.016	1.778

OUTLINE



CIRCUIT CONFIGURATION