

# 10V Drive Nch MOSFET

# RSJ450N04

#### Structure

Silicon N-channel MOSFET

#### Features

- 1) Low on-resistance.
- 2) High current
- 3) High power Package

#### Application

Switching

# Packaging specifications

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	1000
RSJ450N0	0	

# ●Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		$V_{DSS}$	40	V
Gate-source voltage		$V_{GSS}$	±20	V
Drain current	Continuous	$I_D$	±45	Α
	Pulsed	I <sub>DP</sub> *1	±90	Α
Source current	Continuous	Is	40	Α
(Body Diode)	Pulsed	I <sub>SP</sub> *1	90	Α
Power dissipation		P <sub>D</sub> *2	50	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

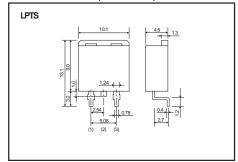
<sup>\*1</sup> Pw≤10µs, Duty cycle≤1%

#### Thermal resistance

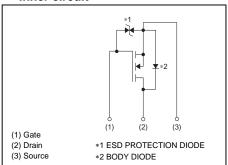
Parameter	Symbol	Limits	Unit
Channel to Case	R <sub>th (ch-c)</sub> *	2.5	°C/W

<sup>\*</sup> T<sub>c</sub>=25°C

#### • Dimensions (Unit : mm)



#### • Inner circuit



<sup>\*2</sup> T<sub>c</sub>=25°C

# ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	$I_{GSS}$	-	-	±10	μA	$V_{GS}=\pm20V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	40	1	1	<b>V</b>	$I_D=1$ mA, $V_{GS}=0$ V
Zero gate voltage drain current	I <sub>DSS</sub>	1	•	1	μA	$V_{DS}$ =40V, $V_{GS}$ =0V
Gate threshold voltage	V <sub>GS (th)</sub>	1.2	-	3.0	V	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub>	-	9.5	13.5	mΩ	I <sub>D</sub> =25A, V <sub>GS</sub> =10V
Forward transfer admittance	IY <sub>fs</sub> I*	10	1	1	S	I <sub>D</sub> =25A, V <sub>DS</sub> =10V
Input capacitance	C <sub>iss</sub>	1	2400	1	рF	V <sub>DS</sub> =25V
Output capacitance	C <sub>oss</sub>	1	380	-	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	$C_{rss}$	1	170	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	1	25	-	ns	I <sub>D</sub> =25A, V <sub>DD</sub> ≒25V
Rise time	t <sub>r</sub> *	1	225	-	ns	V <sub>GS</sub> =10V
Turn-off delay time	t <sub>d(off)</sub> *	1	90	-	ns	$R_L=1.0\Omega$
Fall time	t <sub>f</sub> *	1	390	-	ns	$R_G=10\Omega$
Total gate charge	Q <sub>g</sub> *	-	43	-	nC	V <sub>DD</sub> ≒25V
Gate-source charge	Q <sub>gs</sub> *	-	12	-	nC	I <sub>D</sub> =45A,
Gate-drain charge	Q <sub>gd</sub> *	-	6	-	nC	V <sub>GS</sub> =10V

<sup>\*</sup>Pulsed

# ●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V <sub>SD</sub> *	-	-	1.2	V	I <sub>s</sub> =25A, V <sub>GS</sub> =0V

<sup>\*</sup>Pulsed

#### ●Electrical characteristic curves (Ta=25°C)

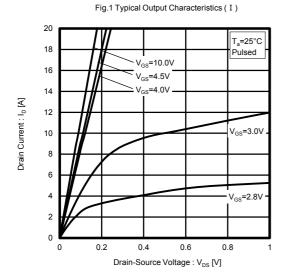


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

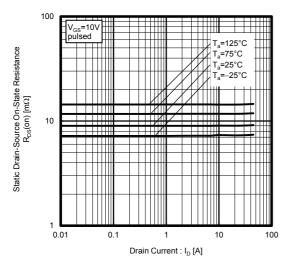


Fig.5 Typical Transfer Characteristics

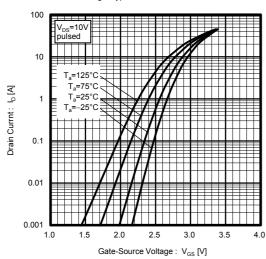


Fig.2 Typical Output Characteristics ( II )

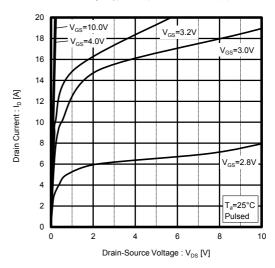


Fig.4 Forward Transfer Admittance vs. Drain Current

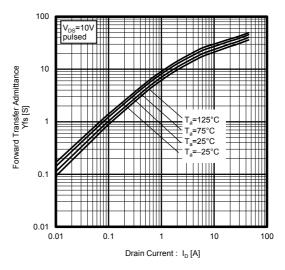


Fig.6 Source Current vs. Source-Drain Voltage

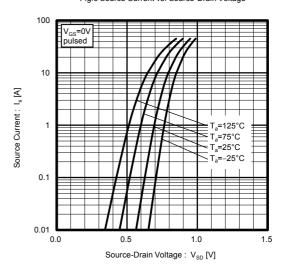
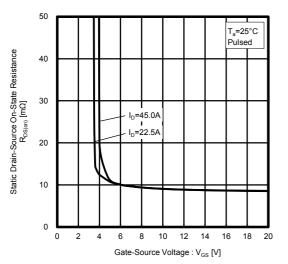


Fig.7 Static Drain-Source On-State Resistance vs. Gate-Source Voltage



 $\begin{array}{c} \text{10000} \\ \text{1000} \\ \text{1000} \\ \text{100} \\ \text{10} \\$ 

0.01

0.1

Fig.8 Switching Characteristics

Fig.9 Dynamic Input Characteristics

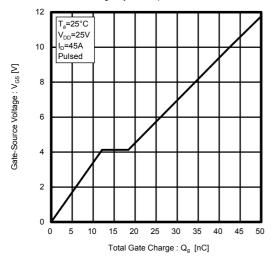


Fig.10 Typical Capacitance vs. Drain-Source Voltage

Drain Current :  $I_D$  [A]

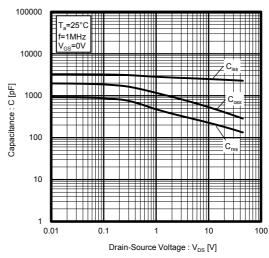


Fig.11 Maximum Safe Operating Area

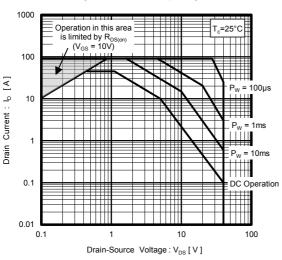
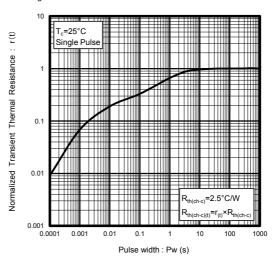


Fig.12 Normalized Transient Thermal Resistance v.s. Pulse Width



# Measurement circuits

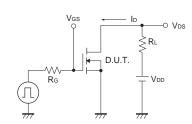


Fig.1-1 Switching Time Measurement Circuit

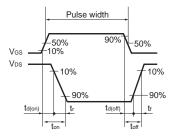


Fig.1-2 Switching Waveforms

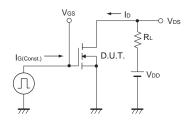


Fig.2-1 Gate Charge Measurement Circuit

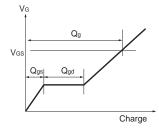


Fig.2-2 Gate Charge Waveform

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