



N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^{a, e}	Q_g (Typ.)
20	0.027 at $V_{GS} = 4.5$ V	8	8.3 nC
	0.032 at $V_{GS} = 2.5$ V	8	
	0.040 at $V_{GS} = 1.8$ V	8	

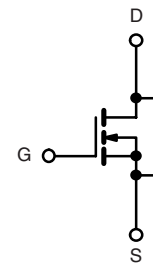
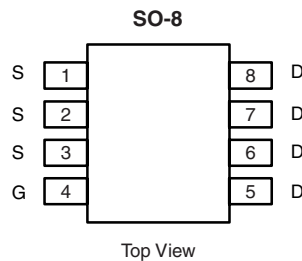
FEATURES

- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC


RoHS
 COMPLIANT

APPLICATIONS

- DC/DC Converter
- Load Switches



Ordering Information: Si4196DY-T1-E3 (Lead (Pb)-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 8	
Continuous Drain Current ($T_J = 150^\circ\text{C}$)	$T_C = 25^\circ\text{C}$	8^e	A
	$T_C = 70^\circ\text{C}$	8^e	
	$T_A = 25^\circ\text{C}$	$6.4^{b, c}$	
	$T_A = 70^\circ\text{C}$	$5.1^{b, c}$	
Pulsed Drain Current	I_{DM}	20	A
Continuous Source-Drain Diode Current	$T_C = 25^\circ\text{C}$	3.8	
	$T_A = 25^\circ\text{C}$	$1.6^{b, c}$	
Single Pulse Avalanche Current	$L = 0.1$ mH	5	mJ
Avalanche Energy	E_{AS}	1.25	
Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	4.6	W
	$T_C = 70^\circ\text{C}$	2.9	
	$T_A = 25^\circ\text{C}$	$2.0^{b, c}$	
	$T_A = 70^\circ\text{C}$	$1.28^{b, c}$	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	$t \leq 10$ s	R_{thJA}	52	$^\circ\text{C/W}$
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	22	

Notes:

 a. Based on $T_C = 25^\circ\text{C}$.

b. Surface Mounted on 1" x 1" FR4 board.

 c. $t = 10$ s.

 d. Maximum under Steady State conditions is 110°C/W .

e. Package limited.

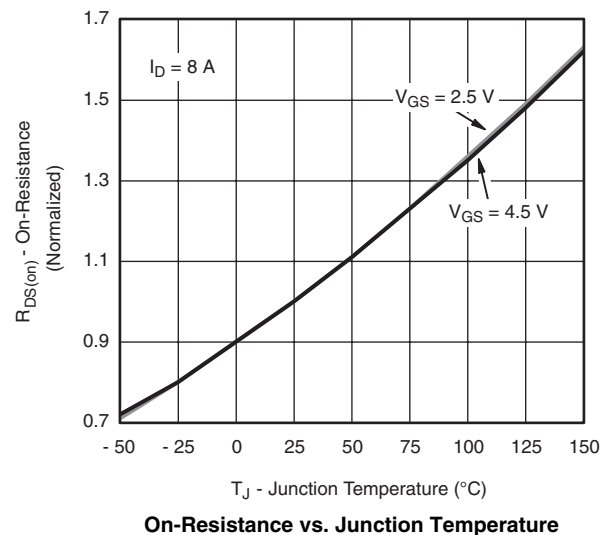
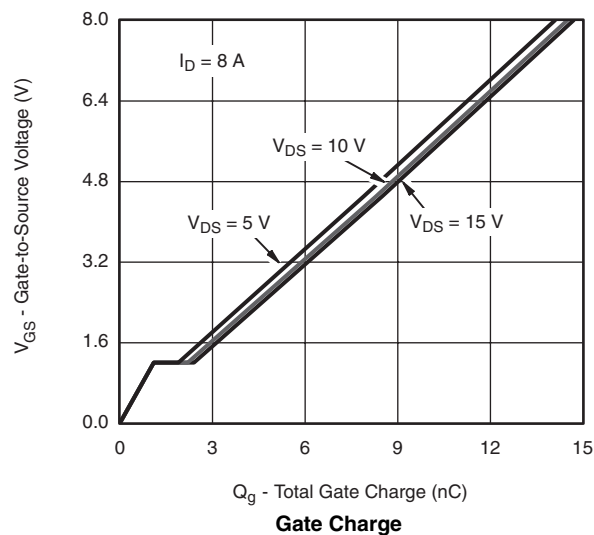
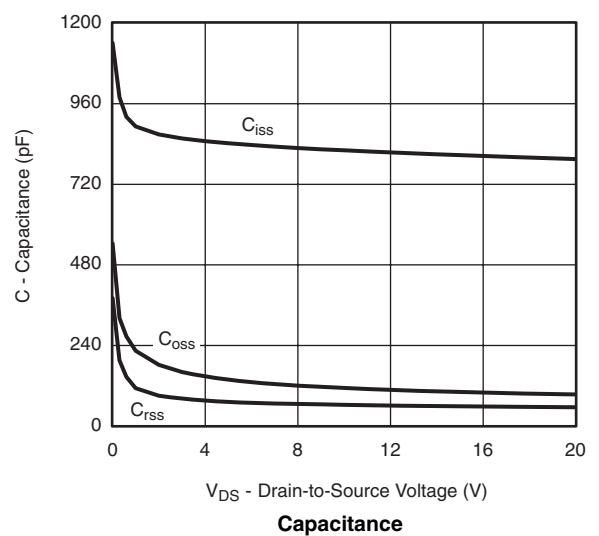
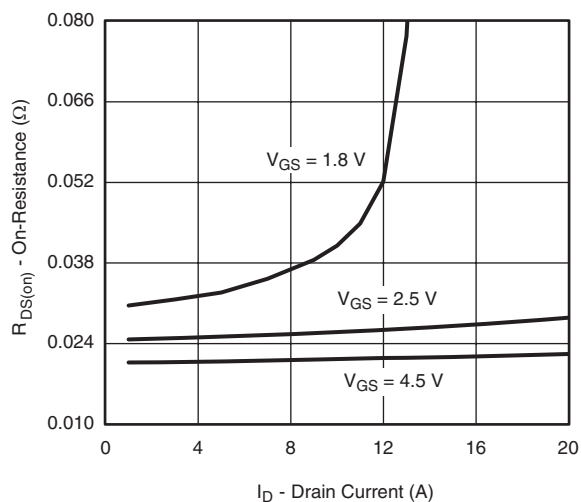
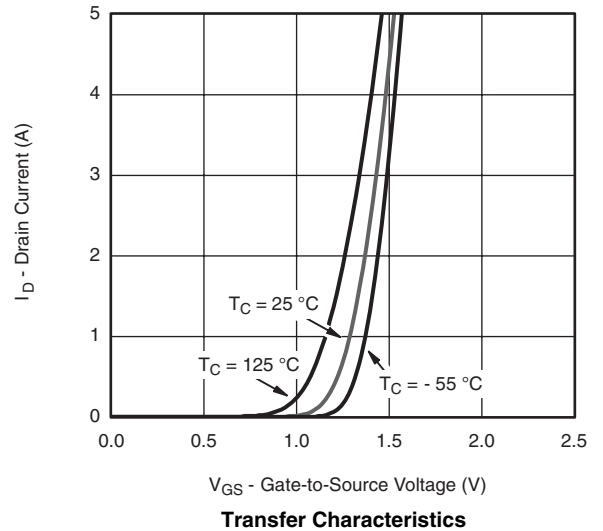
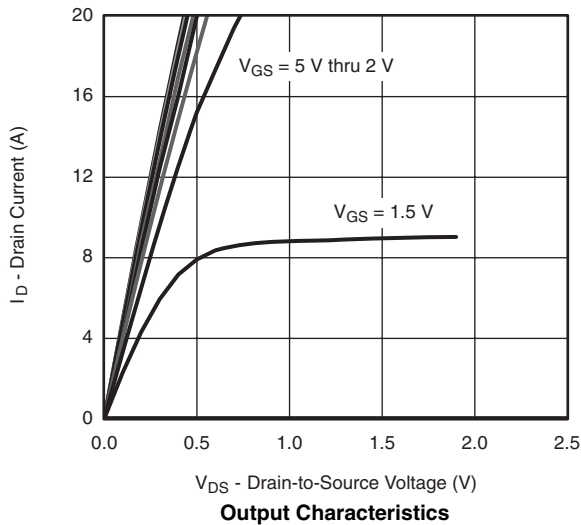
SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	20			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		21		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 3.0		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.4		1.0	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V			1	μA
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 4.5 V	10			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 8 A		0.021	0.027	Ω
		V _{GS} = 2.5 V, I _D = 5 A		0.025	0.032	
		V _{GS} = 1.8 V, I _D = 2.5 A		0.031	0.040	
Forward Transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 8 A		28		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		830		pF
Output Capacitance	C _{oss}			115		
Reverse Transfer Capacitance	C _{rss}			63		
Total Gate Charge	Q _g	V _{DS} = 10 V, V _{GS} = 8 V, I _D = 8 A		14.5	22	nC
		V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 8 A		8.3	12.5	
Gate-Source Charge	Q _{gs}			1.1		
Gate-Drain Charge	Q _{gd}			1.1		
Gate Resistance	R _g	f = 1 MHz	0.6	3.0	6.0	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 10 V, R _L = 2 Ω I _D ≅ 5 A, V _{GEN} = 4.5 V, R _g = 1 Ω		8	16	ns
Rise Time	t _r			13	25	
Turn-Off Delay Time	t _{d(off)}			33	60	
Fall Time	t _f			9	18	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 10 V, R _L = 2 Ω I _D ≅ 5 A, V _{GEN} = 8 V, R _g = 1 Ω		5	10	
Rise Time	t _r			12	24	
Turn-Off Delay Time	t _{d(off)}			22	40	
Fall Time	t _f			8	16	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			3.8	A
Pulse Diode Forward Current ^a	I _{SM}				20	
Body Diode Voltage	V _{SD}	I _S = 5.4 A		0.78	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 5.4 A, dI/dt = 100 A/μs, T _J = 25 °C		11	22	ns
Body Diode Reverse Recovery Charge	Q _{rr}			4	8	nC
Reverse Recovery Fall Time	t _a			6		ns
Reverse Recovery Rise Time	t _b			5		

Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

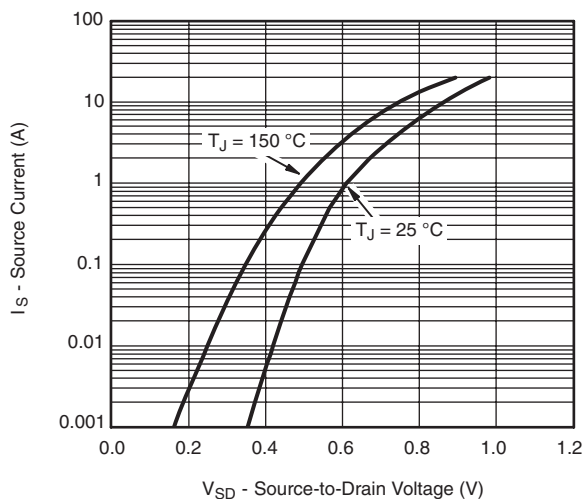
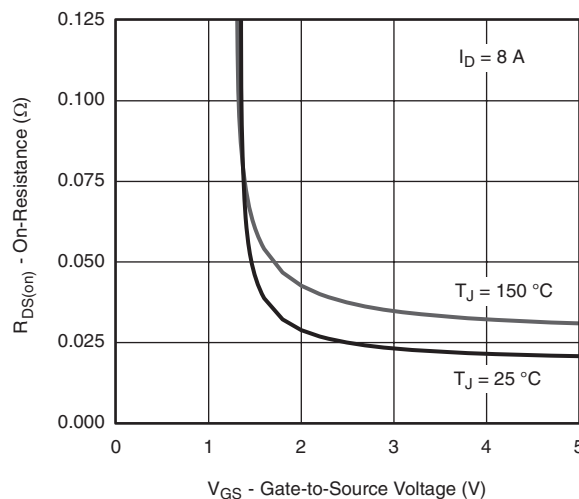
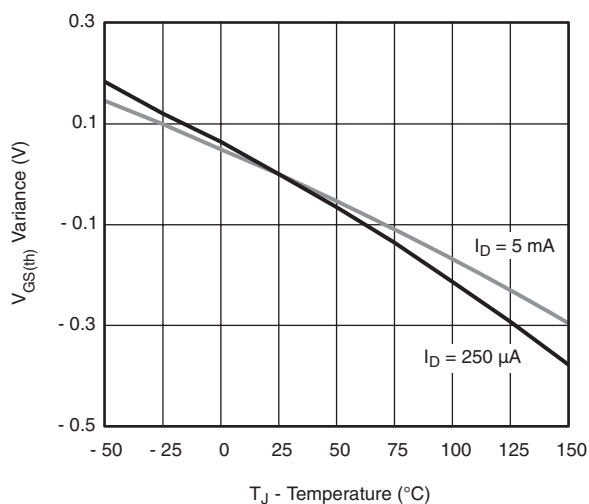
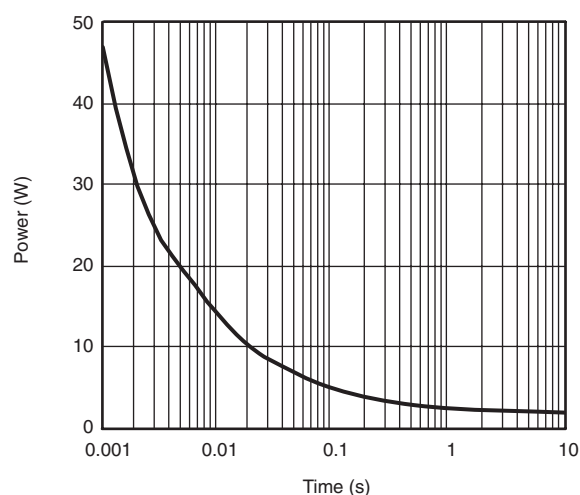
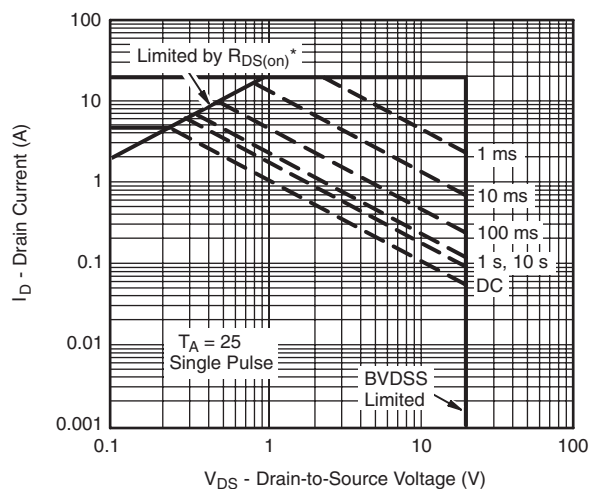
b. Guaranteed by design, not subject to production testing.

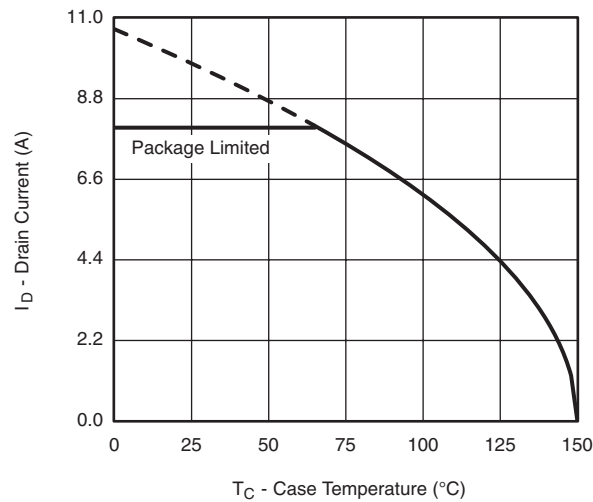
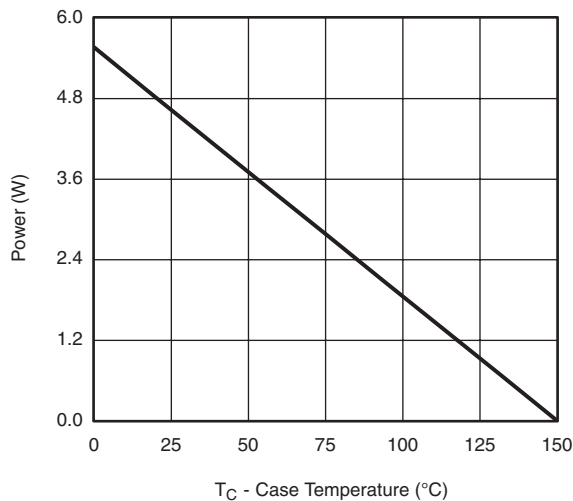
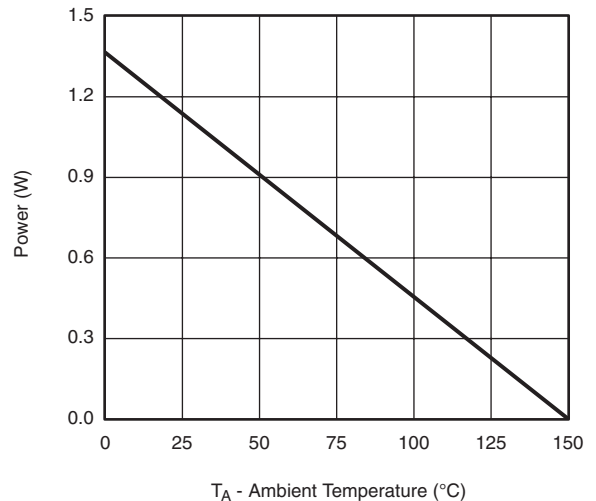
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


Si4196DY

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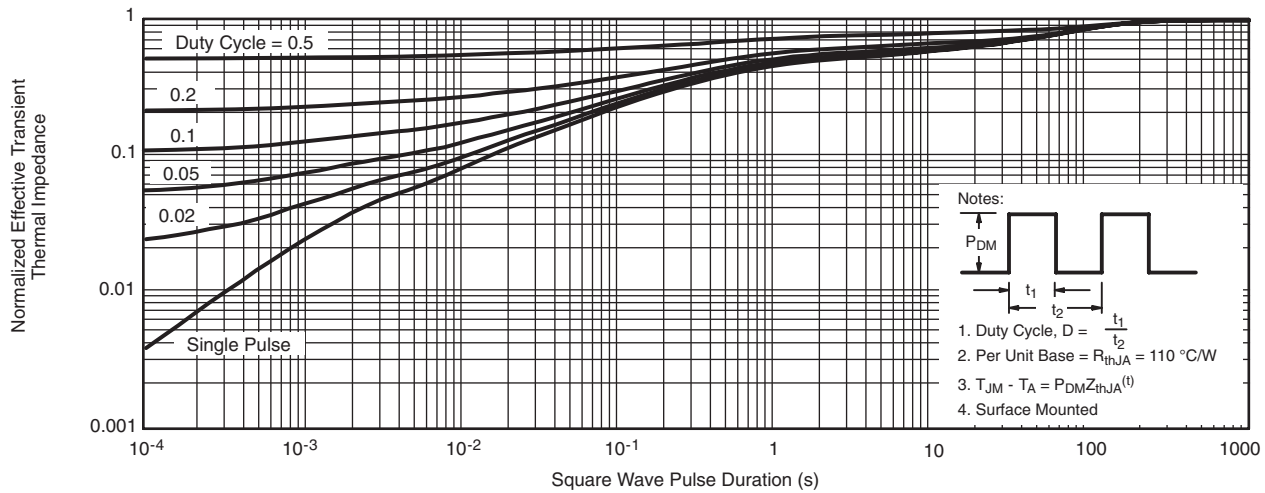
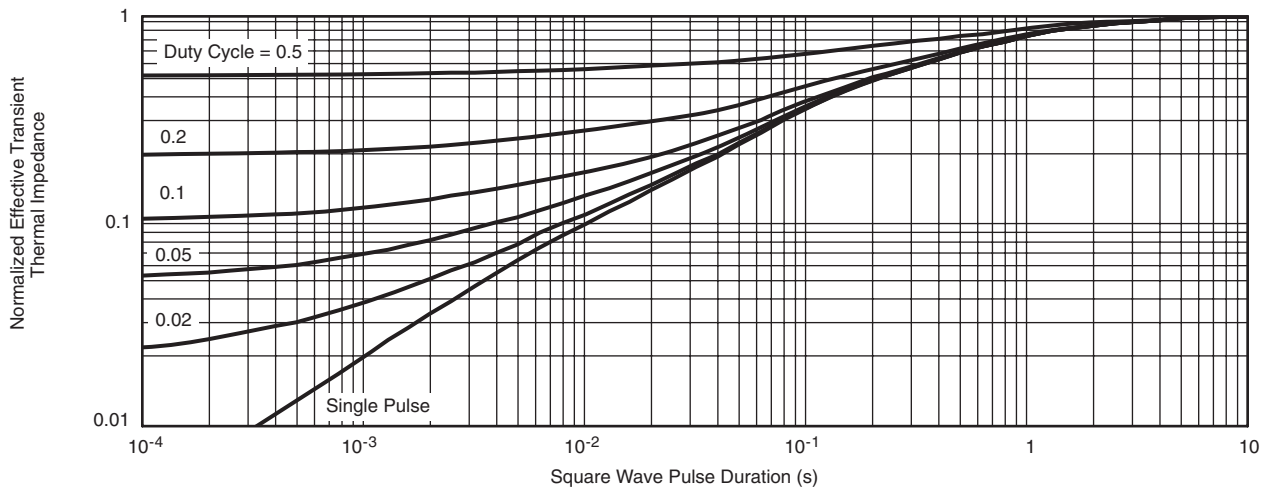
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power, Junction-to-Ambient*** $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Current Derating*

Power, Junction-to-Foot

Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

Si4196DY

Vishay Siliconix

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Foot**

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