

Dual N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

	V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
Channel-1	30	0.0093 at $V_{GS} = 10$ V	25	8.2
		0.0124 at $V_{GS} = 4.5$ V	25	
Channel-2	30	0.0053 at $V_{GS} = 10$ V	30	15.3
		0.007 at $V_{GS} = 4.5$ V	30	

FEATURES

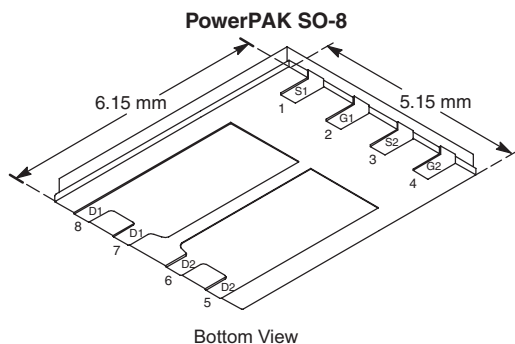
- Halogen-free According to IEC 61249-2-21
- TrenchFET[®] Power MOSFET
- PWM Optimized

APPLICATIONS

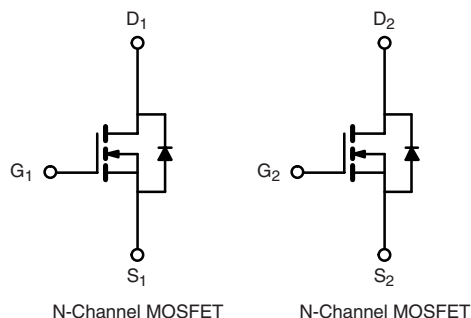
- System Power DC/DC



RoHS
COMPLIANT
HALOGEN
FREE



Ordering Information: Si7998DP-T1-GE3 (Lead (Pb)-free and Halogen-free)



ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Channel-1	Channel-2	Unit
Drain-Source Voltage	V_{DS}	30	30	V
Gate-Source Voltage	V_{GS}	± 20	± 20	
Continuous Drain Current ($T_J = 150^\circ\text{C}$)	$T_C = 25^\circ\text{C}$	25 ^a	30 ^a	A
	$T_C = 70^\circ\text{C}$	25 ^a	30 ^a	
	$T_A = 25^\circ\text{C}$	15 ^{b, c}	21 ^{b, c}	
	$T_A = 70^\circ\text{C}$	12 ^{b, c}	17 ^{b, c}	
Pulsed Drain Current	I_{DM}	60	80	
Source-Drain Current Diode Current	$T_C = 25^\circ\text{C}$	19	30 ^a	
	$T_A = 25^\circ\text{C}$	3.0 ^{b, c}	3.3 ^{b, c}	
Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	22	40	W
	$T_C = 70^\circ\text{C}$	14	25	
	$T_A = 25^\circ\text{C}$	3.6 ^{b, c}	4.0 ^{b, c}	
	$T_A = 70^\circ\text{C}$	2.3 ^{b, c}	2.5 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		$^\circ\text{C}$
Soldering Recommendations (Peak Temperature) ^{d, e}		260		

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Channel-1		Channel-2		Unit
			Typ.	Max.	Typ.	Max.	
Maximum Junction-to-Ambient ^{b, f}	t ≤ 10 s	R _{thJA}	26	35	22	31	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	4	5.5	2.2	3.1	

Notes:

a. Package Limited.

b. Surface Mounted on 1" x 1" FR4 board.

c. $t = 10$ s.

d. See Solder Profile (www.vishay.com/ppg?73257). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

f. Maximum under Steady State conditions for channel 1 and channel 2 is 80°C/W .

SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions		Min.	Typ.	Max.	Unit	
Static								
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	Ch-1	30			V	
		V _{GS} = 0 V, I _D = 250 μA	Ch-2	30				
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA	Ch-1		28		mV/°C	
		I _D = 250 μA	Ch-2		26			
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J	I _D = 250 μA	Ch-1		- 5.6			
		I _D = 250 μA	Ch-2		- 6			
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	Ch-1	1.2		2.5	V	
		V _{DS} = V _{GS} , I _D = 250 μA	Ch-2	1.2		2.5		
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	Ch-1			100	nA	
		V _{DS} = 0 V, V _{GS} = ± 20 V	Ch-2			100		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	Ch-1			1	μA	
		V _{DS} = 30 V, V _{GS} = 0 V	Ch-2			1		
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 85 °C	Ch-1			10		
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 85 °C	Ch-2			10		
On-State Drain Current ^b	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	Ch-1	30			A	
		V _{DS} ≥ 5 V, V _{GS} = 10 V	Ch-2	30				
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 10 V, I _D = 15 A	Ch-1		0.0076	0.0093	Ω	
		V _{GS} = 10 V, I _D = 20 A	Ch-2		0.0044	0.0053		
		V _{GS} = 4.5 V, I _D = 13 A	Ch-1		0.0103	0.0124		
		V _{GS} = 4.5 V, I _D = 18 A	Ch-2		0.0058	0.007		
Forward Transconductance ^b	g _{fs}	V _{DS} = 10 V, I _D = 15 A	Ch-1		45		S	
		V _{DS} = 10 V, I _D = 20 A	Ch-2		71			
Dynamic ^a								
Input Capacitance	C _{iss}	Channel-1 V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz	Ch-1		1100		pF	
			Ch-2		2000			
Output Capacitance	C _{oss}	Channel-2 V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz	Ch-1		200			
			Ch-2		390			
Reverse Transfer Capacitance	C _{rss}		Ch-1		90			
			Ch-2		160			
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 15 A	Ch-1		17	26	nC	
		V _{DS} = 15 V, V _{GS} = 10 V, I _D = 20 A	Ch-2		32	48		
		Channel-1 V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 15 A	Ch-1		8.2	13		
			Ch-2		15.3	23		
Gate-Source Charge	Q _{gs}	Channel-2 V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 20 A	Ch-1		3.2			
			Ch-2		6.3			
Gate-Drain Charge	Q _{gd}		Ch-1		2.7			
			Ch-2		4.7			
Gate Resistance	R _g	f = 1 MHz	Ch-1		3.5	7	Ω	
			Ch-2		3.5	7		

Notes:

a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.



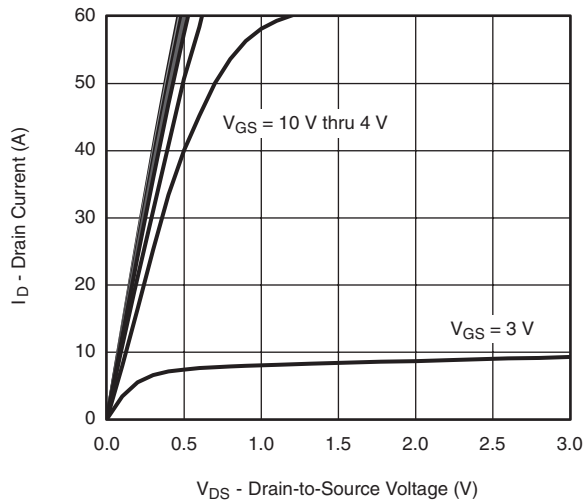
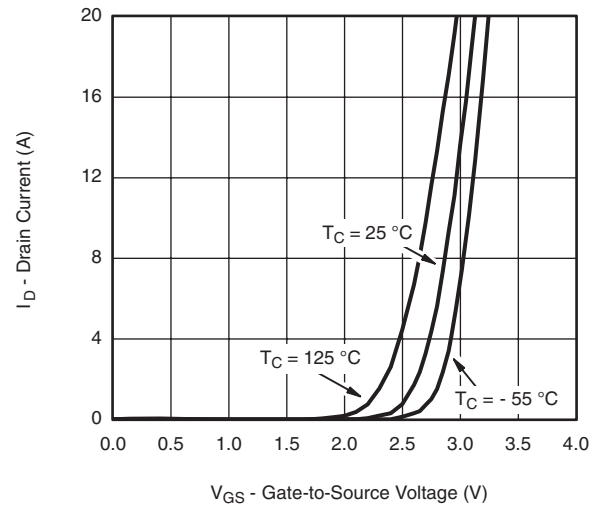
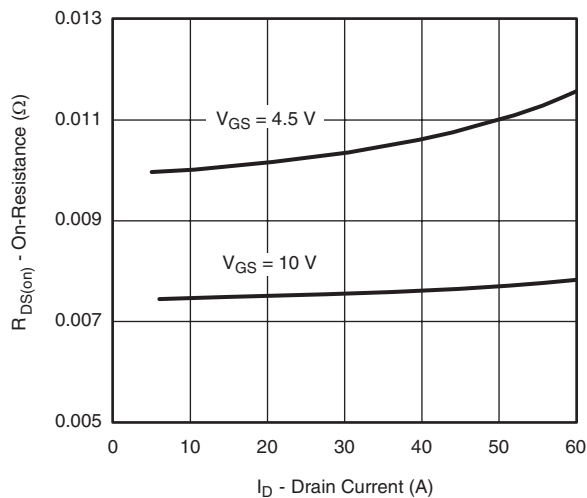
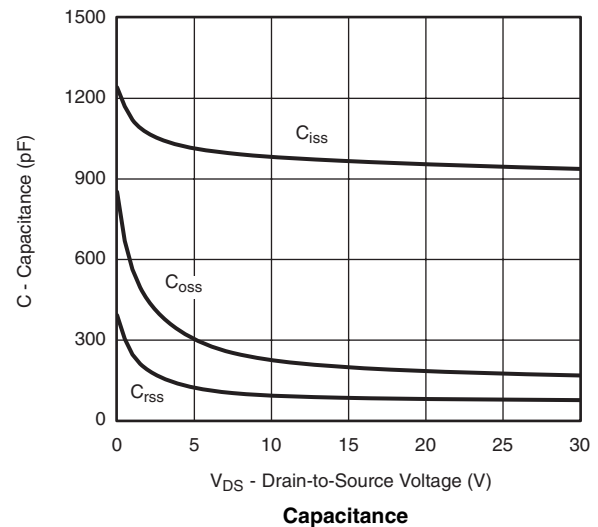
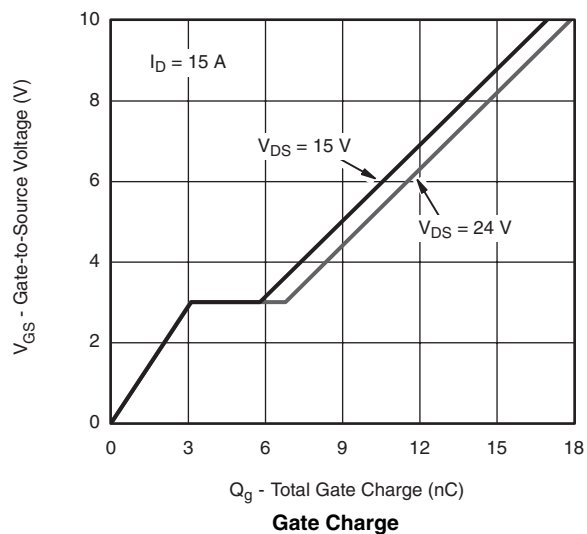
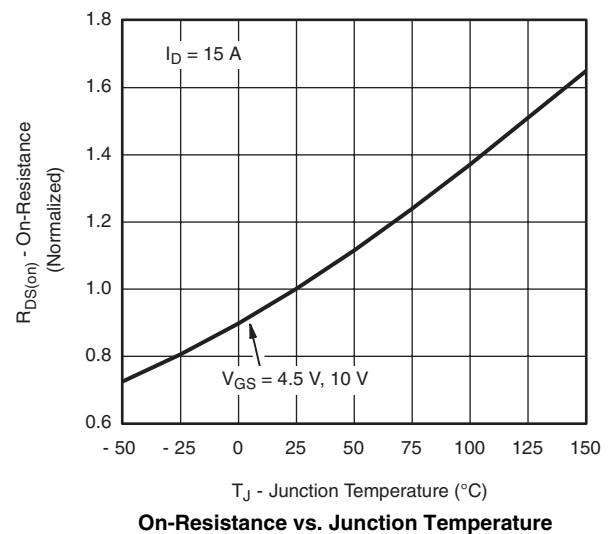
SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Dynamic ^a							
Turn-On Delay Time	$t_{d(on)}$	Channel-1 $V_{DD} = 15\text{ V}$, $R_L = 1.5\text{ }\Omega$ $I_D \cong 10\text{ A}$, $V_{GEN} = 4.5\text{ V}$, $R_g = 1\text{ }\Omega$	Ch-1		20	30	ns
			Ch-2		26	40	
Rise Time	t_r		Ch-1		15	25	
			Ch-2		17	30	
Turn-Off Delay Time	$t_{d(off)}$	Channel-2 $V_{DD} = 15\text{ V}$, $R_L = 1.5\text{ }\Omega$ $I_D \cong 10\text{ A}$, $V_{GEN} = 4.5\text{ V}$, $R_g = 1\text{ }\Omega$	Ch-1		22	35	
			Ch-2		35	55	
Fall Time	t_f		Ch-1		10	15	
			Ch-2		13	20	
Turn-On Delay Time	$t_{d(on)}$	Channel-1 $V_{DD} = 15\text{ V}$, $R_L = 1.5\text{ }\Omega$ $I_D \cong 10\text{ A}$, $V_{GEN} = 10\text{ V}$, $R_g = 1\text{ }\Omega$	Ch-1		10	15	
			Ch-2		13	20	
Rise Time	t_r		Ch-1		10	15	
			Ch-2		10	15	
Turn-Off Delay Time	$t_{d(off)}$	Channel-2 $V_{DD} = 15\text{ V}$, $R_L = 1.5\text{ }\Omega$ $I_D \cong 10\text{ A}$, $V_{GEN} = 10\text{ V}$, $R_g = 1\text{ }\Omega$	Ch-1		22	35	
			Ch-2		32	50	
Fall Time	t_f		Ch-1		10	15	
			Ch-2		10	15	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^{\circ}\text{C}$	Ch-1			13	A
			Ch-2			30	
Pulse Diode Forward Current ^a	I_{SM}		Ch-1			30	
			Ch-2			80	
Body Diode Voltage	V_{SD}	$I_S = 10\text{ A}$	Ch-1		0.8	1.2	V
		$I_S = 10\text{ A}$	Ch-2		0.8	1.2	
Body Diode Reverse Recovery Time	t_{rr}	Channel-1 $I_F = 10\text{ A}$, $dI/dt = 100\text{ A}/\mu\text{s}$, $T_J = 25\text{ }^{\circ}\text{C}$	Ch-1		20	30	ns
			Ch-2		27	40	
Body Diode Reverse Recovery Charge	Q_{rr}		Ch-1		15	25	nC
			Ch-2		22	35	
Reverse Recovery Fall Time	t_a	Channel-2 $I_F = 10\text{ A}$, $dI/dt = 100\text{ A}/\mu\text{s}$, $T_J = 25\text{ }^{\circ}\text{C}$	Ch-1		11		ns
			Ch-2		15		
Reverse Recovery Rise Time	t_b		Ch-1		9		
			Ch-2		12		

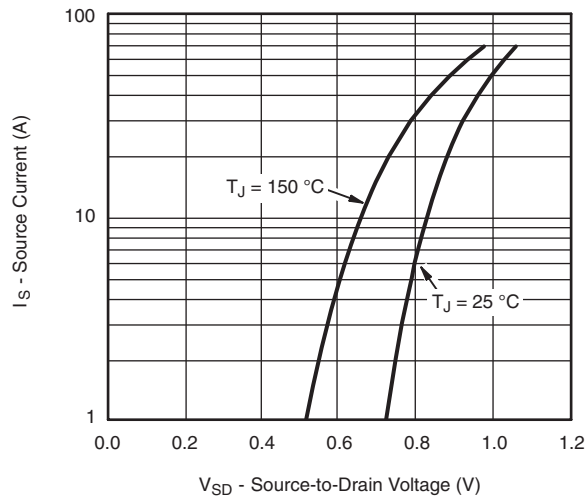
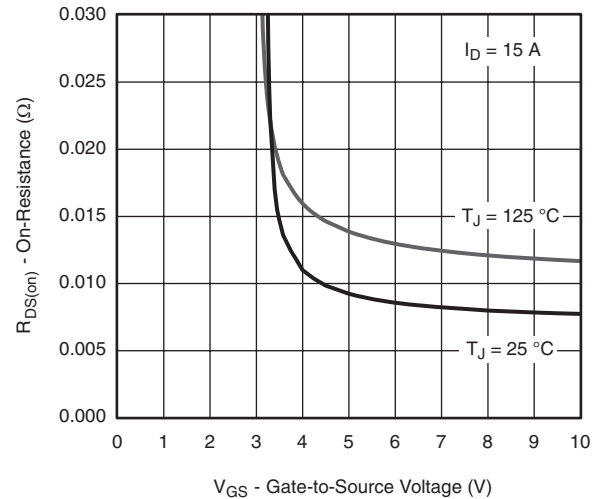
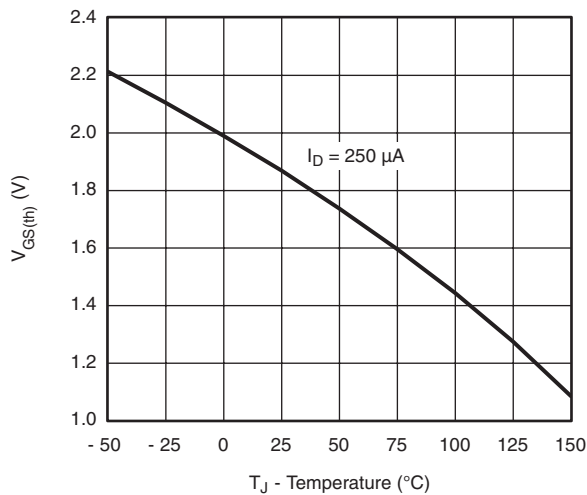
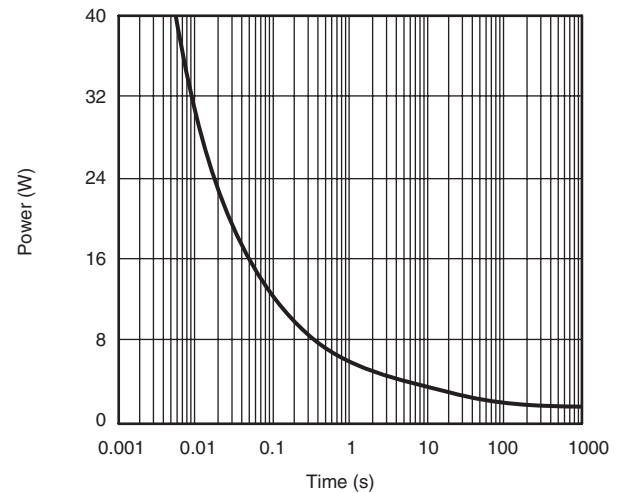
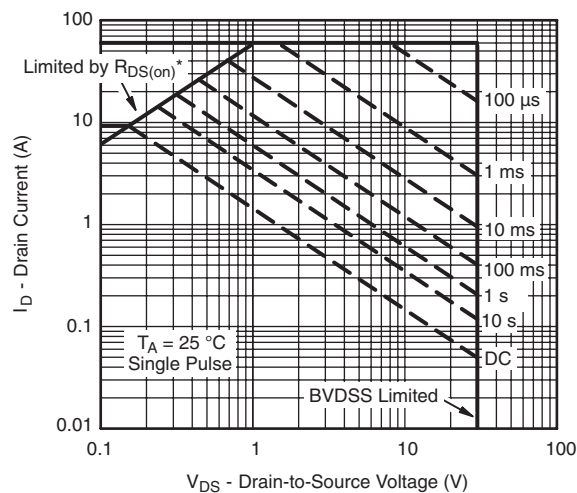
Notes:

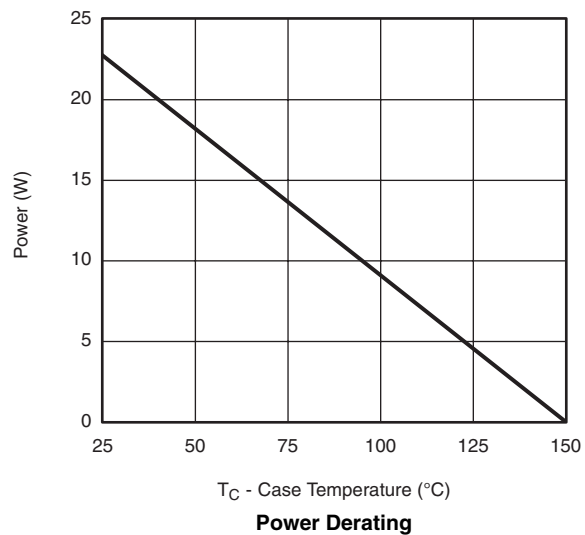
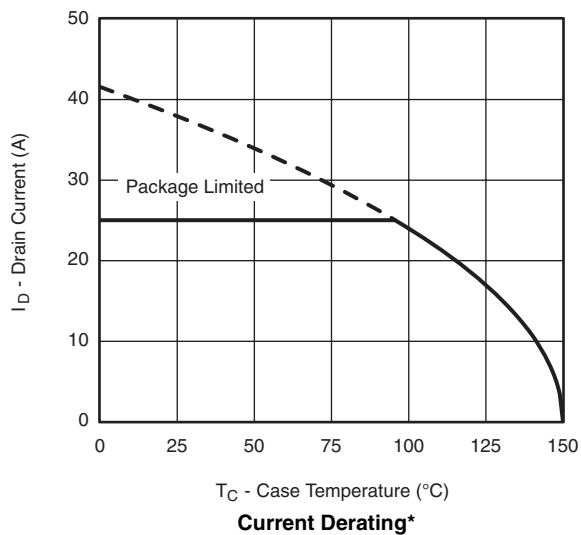
a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Output Characteristics****Transfer Characteristics****On-Resistance vs. Drain Current****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

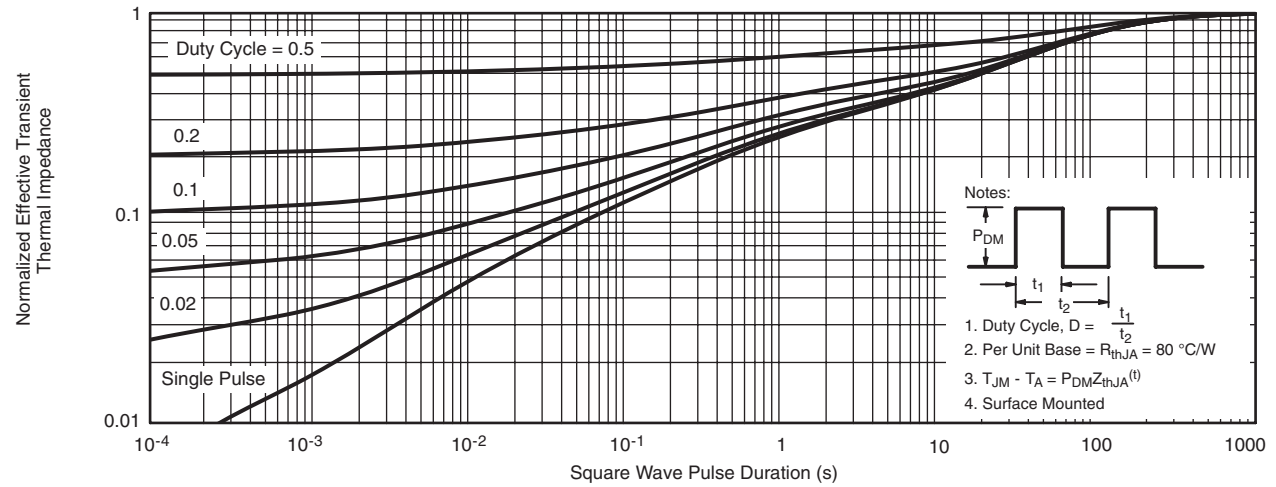
**CHANNEL-1 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power*** $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**

CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

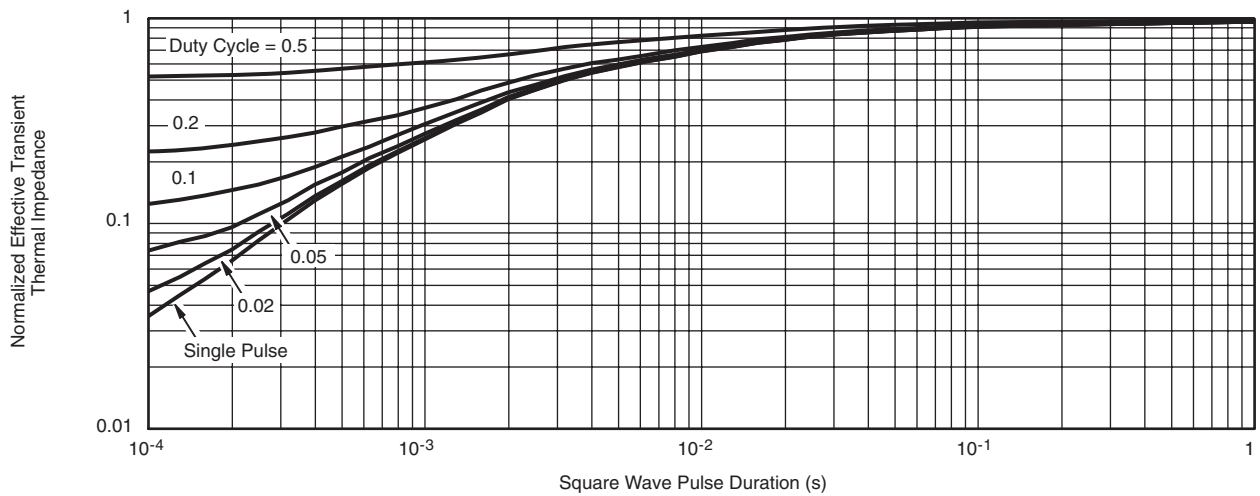
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



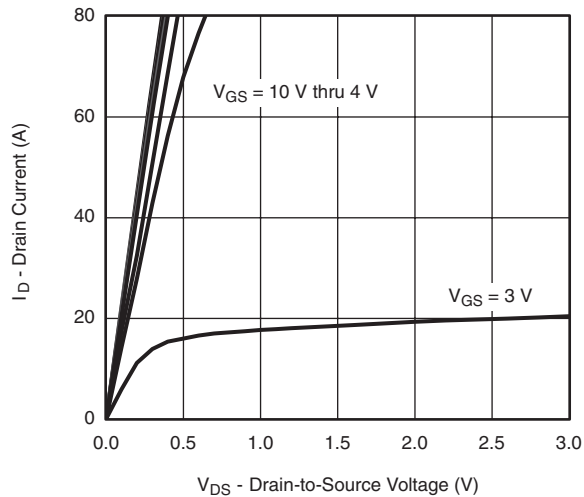
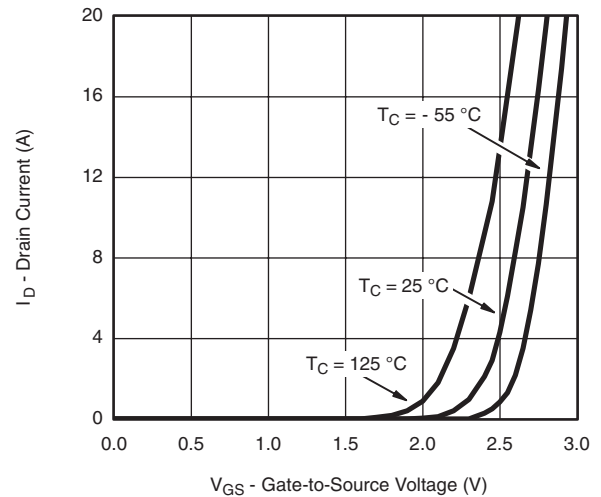
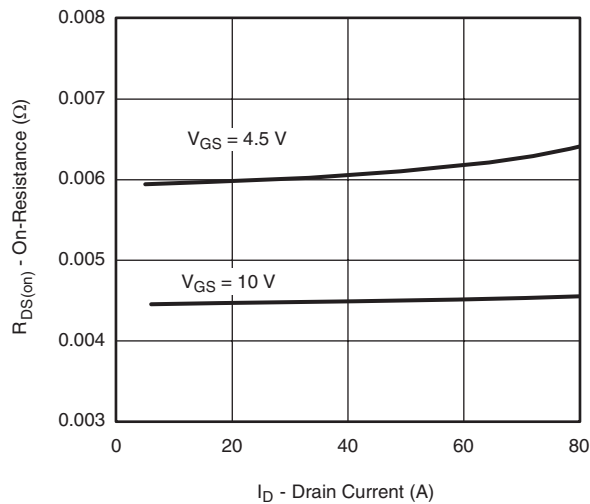
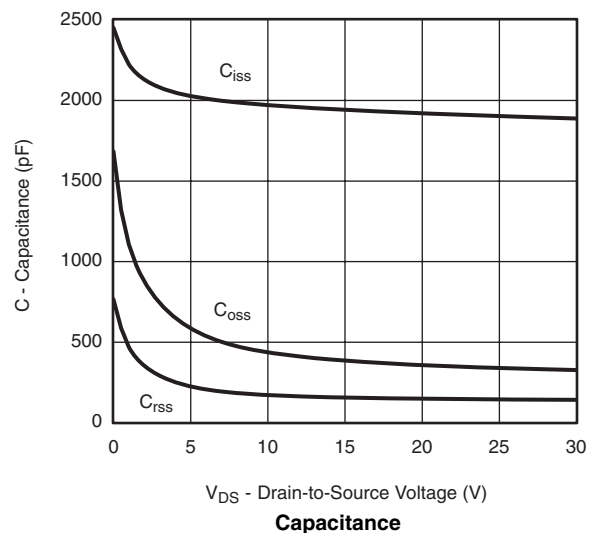
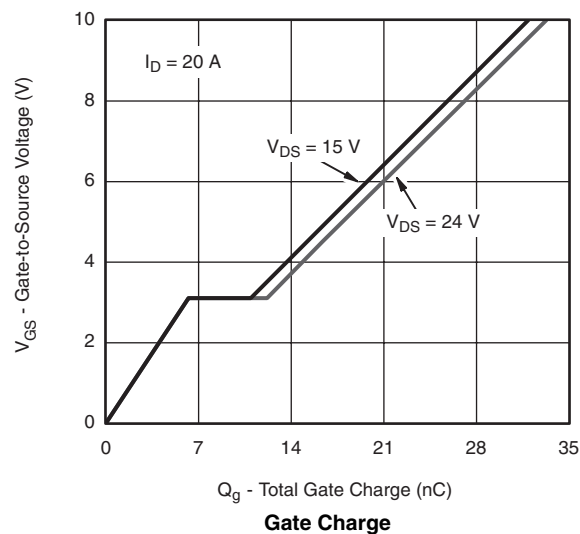
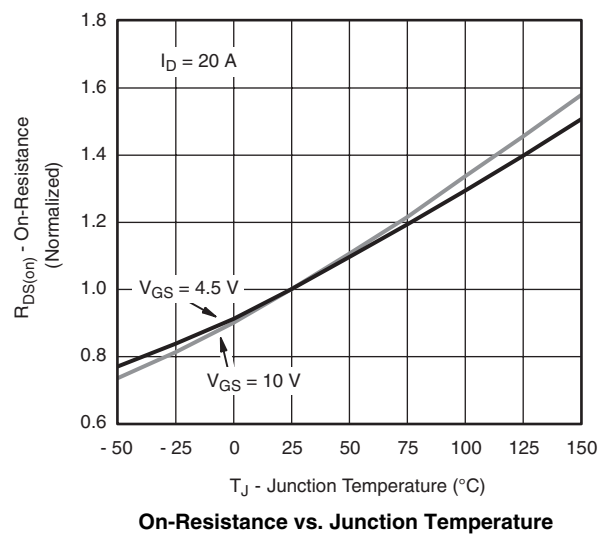
CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

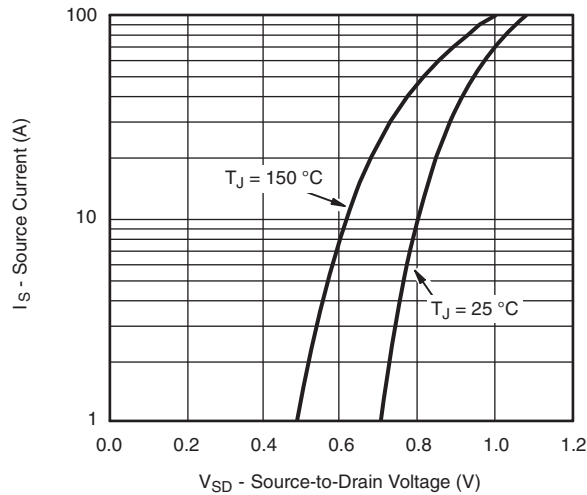
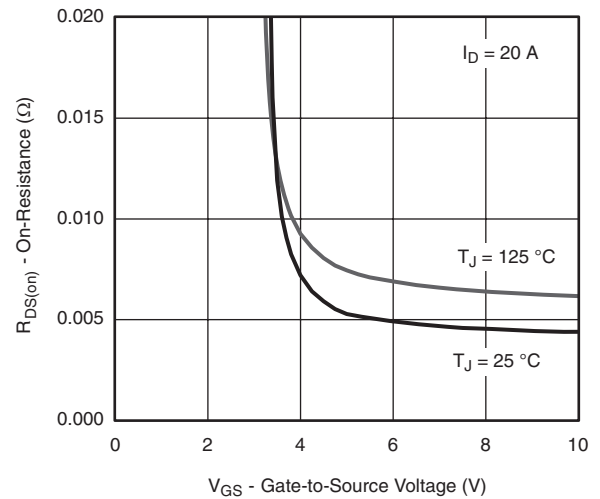
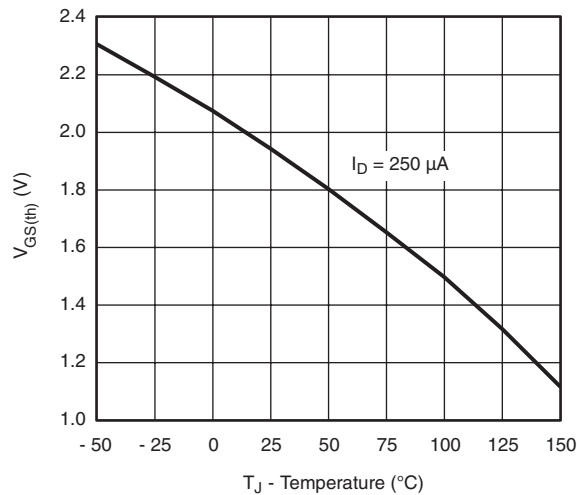
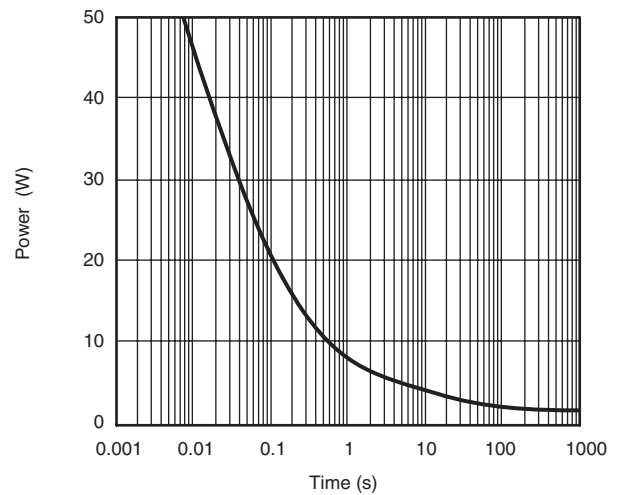
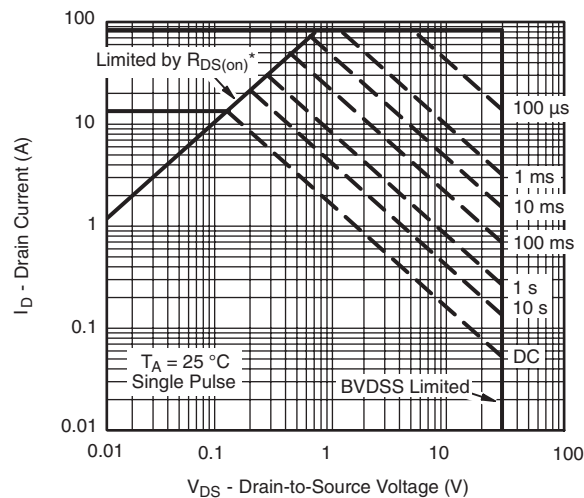


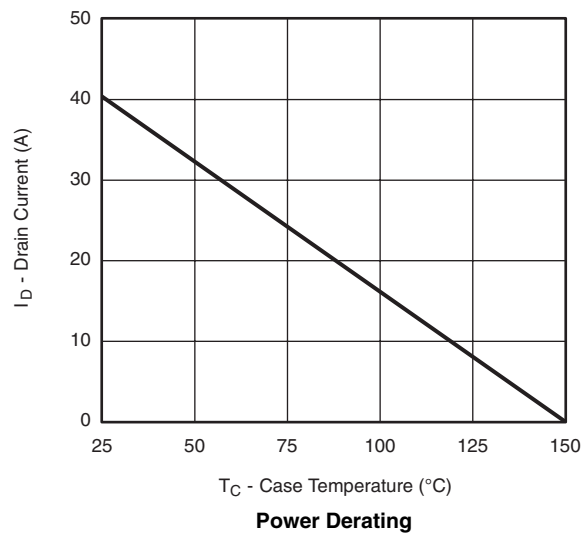
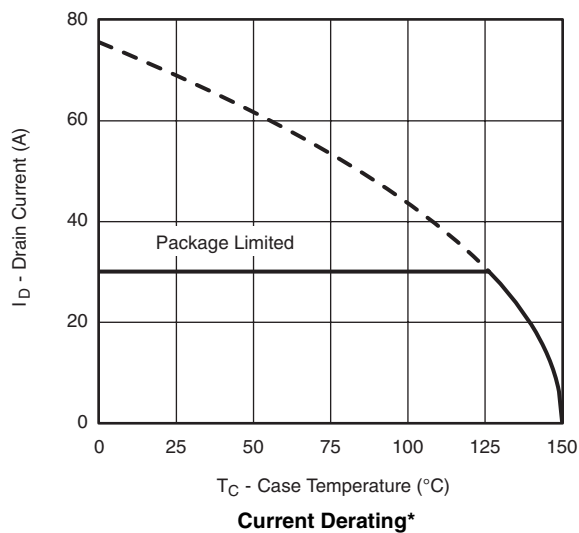
Normalized Thermal Transient Impedance, Junction-to-Ambient



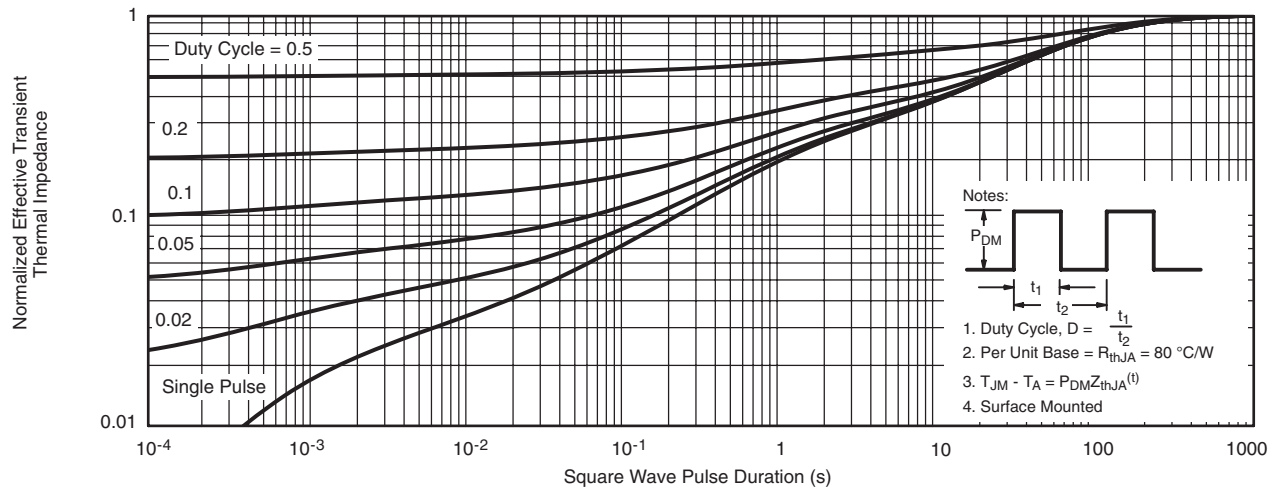
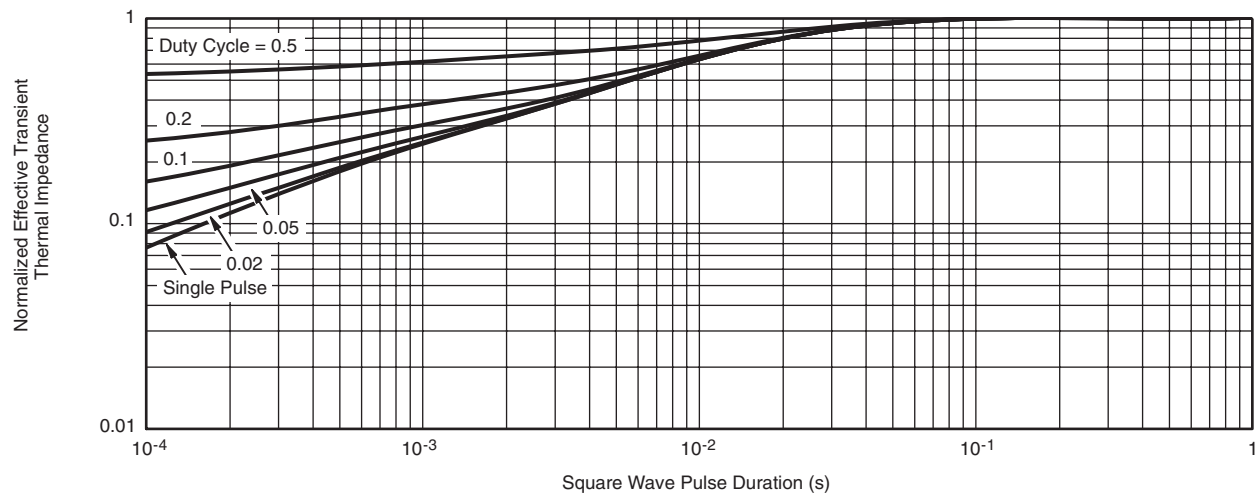
Normalized Thermal Transient Impedance, Junction-to-Case

CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted**Output Characteristics****Transfer Characteristics****On-Resistance vs. Drain Current****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

**CHANNEL-2 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Reverse Current vs. Junction Temperature****Single Pulse Power*** $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**

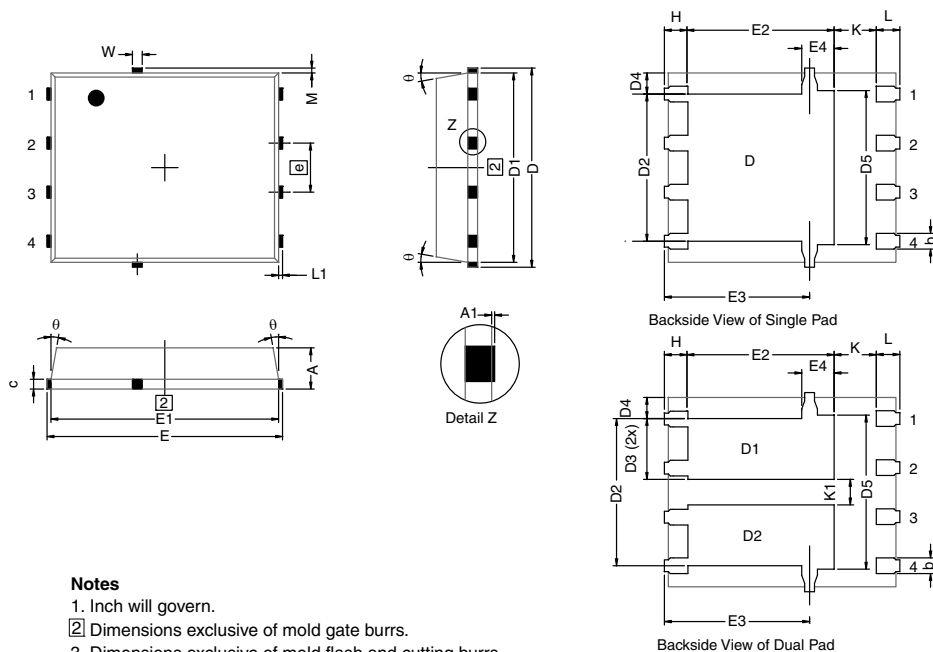
CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**CHANNEL-2 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Case**

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?68970.

PowerPAK® SO-8, (Single/Dual)

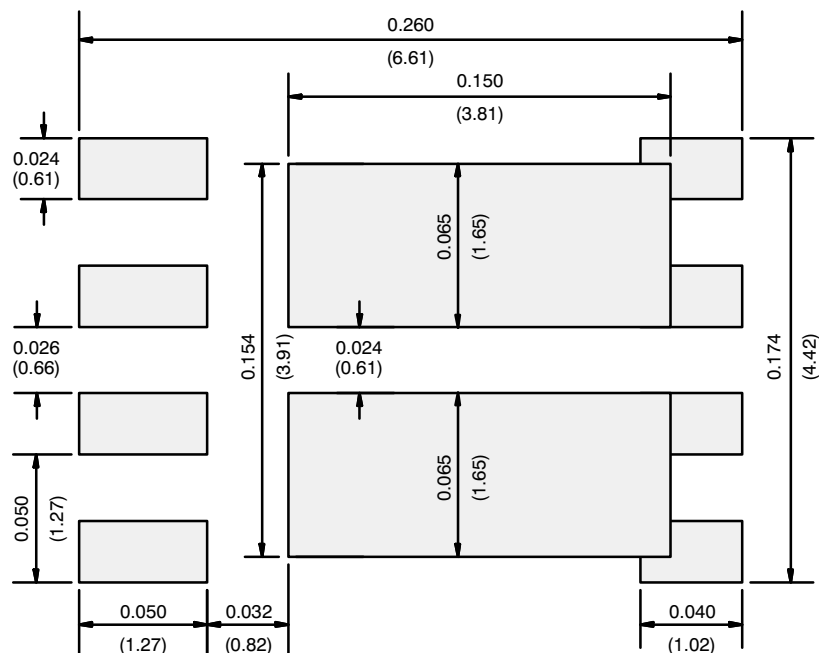


Notes

1. Inch will govern.
2. Dimensions exclusive of mold gate burrs.
3. Dimensions exclusive of mold flash and cutting burrs.

DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.97	1.04	1.12	0.038	0.041	0.044
A1		-	0.05	0	-	0.002
b	0.33	0.41	0.51	0.013	0.016	0.020
c	0.23	0.28	0.33	0.009	0.011	0.013
D	5.05	5.15	5.26	0.199	0.203	0.207
D1	4.80	4.90	5.00	0.189	0.193	0.197
D2	3.56	3.76	3.91	0.140	0.148	0.154
D3	1.32	1.50	1.68	0.052	0.059	0.066
D4	0.57 typ.			0.0225 typ.		
D5	3.98 typ.			0.157 typ.		
E	6.05	6.15	6.25	0.238	0.242	0.246
E1	5.79	5.89	5.99	0.228	0.232	0.236
E2 (for AL product)	3.30	3.48	3.66	0.130	0.137	0.144
E2 (for other product)	3.48	3.66	3.84	0.137	0.144	0.151
E3	3.68	3.78	3.91	0.145	0.149	0.154
E4 (for AL product)	0.58 typ.			0.023 typ.		
E4 (for other product)	0.75 typ.			0.030 typ.		
e	1.27 BSC			0.050 BSC		
K (for AL product)	1.45 typ.			0.057 typ.		
K (for other product)	1.27 typ.			0.050 typ.		
K1	0.56	-	-	0.022	-	-
H	0.51	0.61	0.71	0.020	0.024	0.028
L	0.51	0.61	0.71	0.020	0.024	0.028
L1	0.06	0.13	0.20	0.002	0.005	0.008
θ	0°	-	12°	0°	-	12°
W	0.15	0.25	0.36	0.006	0.010	0.014
M	0.125 typ.			0.005 typ.		
ECN: C13-0702-Rev. K, 20-May-13						
DWG: 5881						

RECOMMENDED MINIMUM PADS FOR PowerPAK® SO-8 Dual



Recommended Minimum Pads Dimensions in Inches/(mm)

[Return to Index](#)



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.